

Single-Stage Power Factor Corrected Off-Line Switching Regulator ICs

Features and Benefits

- Integrated on-time control circuit (it realizes high power factor by average current control)
- Integrated startup circuit (no external startup circuit necessary)
- Integrated soft-start circuit (reduces power stress during start-up on the incorporated power MOSFET and output rectifier)
- Integrated bias assist circuit (improves startup performance, suppresses VCC voltage droop during operation, and allows use of low-rated ceramic capacitor on VCC pin)
- Integrated Leading Edge Blanking (LEB) circuit
- Integrated maximum on-time limit circuit
- Protection features:
 - Overcurrent protection (OCP): pulse-by-pulse
 - Overvoltage protection (OVP): latched shutdown
 - Overload protection (OLP): latched shutdown
 - Thermal shutdown (TSD): latched shutdown

Description

The LC5540LF series is the power IC for the isolated type LED driver which has an incorporated power MOSFET, designed for input capacitorless applications, and making it possible for systems to comply with the harmonics standard (IEC61000-3-2 class C), even during light load condition. The controller adapts the average current control method for realizing high power factors, and the quasi-resonant topology contributes to high efficiency and low EMI noise. The series is housed in TO-220 packages. The rich set of protection features helps to realize low component counts, and high performance-to-cost power supply.

The incorporated MOSFET has a $V_{DS(s)}$ rating from 650 V (LC5546LF and LC5547LF) to 800 V (LC5549LF). The $R_{DS(on)}$ (max) is 1.1 Ω (LC5547LF) to 1.9 Ω (LC5546LF). It is capable of a maximum output power of 80 W on 230 VAC supply to 55 W on universal input supply (85 to 265 VAC) (LC5547LF) based on the thermal rating. Note that the maximum output power can be up to 120% to 140% of this value. However, it may be limited in applications with low output voltage or short duty cycle.

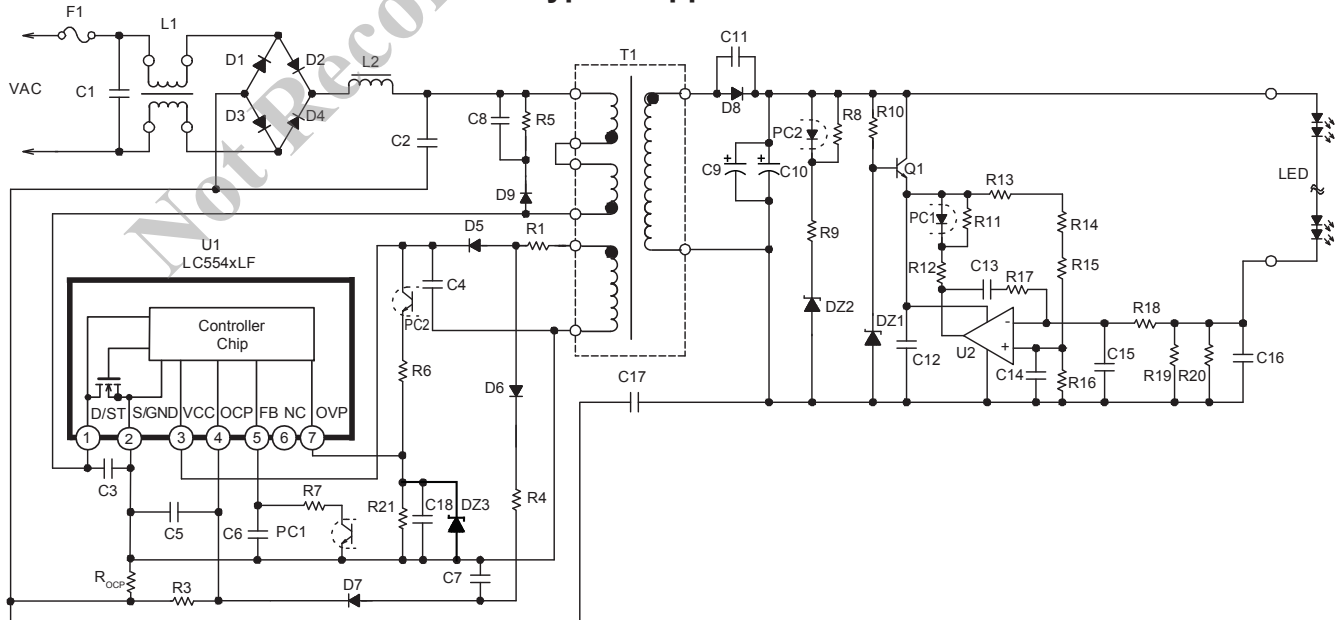
Packages: 7-pin TO-220F



Applications

- LED lighting fixtures
- LED light bulbs

Typical Application



LC5540LF Series

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Selection Guide

Part Number	MOSFET $V_{DS(s)}(\min)$ (V)	$R_{DS(on)}$ (max) (Ω)	PWM Operation Frequency, $f_{osc}(\text{typ})$ (kHz)	On-Time $t_{ON(MAX)}(\text{typ})$ (μs)	P_{OUT}^* (W)	
					230 VAC	85 to 265 VAC
LC5546LF	650	1.9	40	17.5	60	40
LC5547LF		1.1			80	55
LC5549LF	800	1.7			60	40

*Based on the thermal rating; the allowable maximum output power can be up to 120% to 140% of this value. However, maximum output power may be limited in such an application with low output voltage or short duty cycle.

The polarity value for current specifies a sink as "+," and a source as "–," referencing the IC.

Absolute Maximum Ratings Unless specifically noted, T_A is 25°C

Characteristic	Symbol	Notes	Pins	Rating	Unit
Drain Current ¹	I_{DPeak}	LC5546LF	1 – 2	9.2	A
		LC5547LF		13.0	A
		LC5549LF		10.5	A
Single Pulse Avalanche Energy ²	E_{AS}	LC5546LF $I_{LPeak} = 2.9A, V_{DD} = 99V, L = 20mH$	1 – 2	99	mJ
		LC5547LF $I_{LPeak} = 4.4A, V_{DD} = 99V, L = 20mH$		233	mJ
		LC5549LF $I_{LPeak} = 2.8A, V_{DD} = 99V, L = 20mH$		92	mJ
Control Part Input Voltage	V_{CC}		3 – 2	35	V
OCP Pin Voltage	V_{OCP}		4 – 2	–2.0 to 5.0	V
FB Pin Voltage	V_{FB}		5 – 2	–0.3 to 7.0	V
OVP Pin Voltage	V_{OVP}		7 – 2	–0.3 to 5.0	V
Allowable Power Dissipation of MOSFET ³	P_{D1}	LC5546LF	1 – 2	20.2	W
		LC5547LF		23.6	W
		LC5549LF		1.8	W
Internal Frame Temperature in Operation	T_F	Without heatsink	—	–20 to 115	°C
Operating Ambient Temperature	T_{OP}		—	–55 to 115	°C
Storage Temperature	T_{stg}		—	–55 to 125	°C
Channel Temperature	T_{ch}		—	150	°C

¹Refer to MOSFET Safe Operating Area Curve.

²Refer to MOSFET Avalanche Energy Derating Coefficient Curve.

³Refer to MOSFET Temperature versus Power Dissipation Curve.

LC5540LF Series

Single-Stage Power Factor Corrected Off-Line Switching Regulator ICs

Electrical Characteristics of Control Part Unless specifically noted, T_A is 25°C, $V_{CC} = 20\text{ V}$

Characteristic	Symbol	Test Conditions	Pins	Min.	Typ.	Max.	Unit
Power Supply Startup Operation							
Operation Start Voltage	$V_{CC(ON)}$		3 – 2	13.8	15.1	17.3	V
Operation Stop Voltage*	$V_{CC(OFF)}$		3 – 2	8.4	9.4	10.7	V
Circuit Current in Operation	$I_{CC(ON)}$		3 – 2	–	–	4.7	mA
Startup Circuit Operation Voltage	$V_{STARTUP}$		1 – 2	18	21	24	V
Startup Current	$I_{CC(STARTUP)}$	$V_{CC} = 13\text{ V}$	3 – 2	–8.5	–4.0	–1.5	mA
Startup Current Threshold Biasing Voltage*	$V_{CC(BIAS)}$		3 – 2	9.5	11.0	12.5	V
Normal Operation							
PWM Operation Frequency	f_{OSC}		1 – 2	33	40	47	kHz
Maximum On-Time	$t_{ON(MAX)}$		1 – 2	14	17.5	21	μs
FB Pin Control Minimum Voltage	$V_{FB(MIN)}$		5 – 2	0.50	0.85	1.20	V
Maximum Feedback Current	$I_{FB(MAX)}$		5 – 2	–40	–25	–10	μA
Leading Edge Blanking Time	$t_{ON(LEB)}$		4 – 2	–	600	–	ns
Quasi-Resonant Operation Threshold Voltage-1	$V_{BD(TH1)}$		4 – 2	0.14	0.24	0.34	V
Quasi-Resonant Operation Threshold Voltage-2	$V_{BD(TH2)}$		4 – 2	0.11	0.16	0.21	V
Protection Operation							
OCP Pin Overcurrent Protection (OCP) Threshold Voltage	V_{OCP}		4 – 2	–0.66	–0.60	–0.54	V
OCP Pin Source Current	I_{OCP}		4 – 2	–120	–40	–10	μA
OCP Pin Overvoltage Protection (OVP) Operation Voltage	$V_{BD(OVP)}$		4 – 2	2.2	2.6	3.0	V
Overload Protection (OLP) Threshold Voltage	$V_{FB(OLP)}$		5 – 2	4.1	4.5	4.9	V
OVP Pin OVP Threshold Voltage	$V_{OVP(OVP)}$		7 – 2	1.6	2.0	2.4	V
VCC Pin OVP Threshold Voltage	$V_{CC(OVP)}$		3 – 2	28.5	31.5	34.0	V
Thermal Shutdown Activating Temperature	$T_{J(TSD)}$		—	135	–	–	°C

* $V_{CC(BIAS)} > V_{CC(OFF)}$ always.

LC5540LF Series

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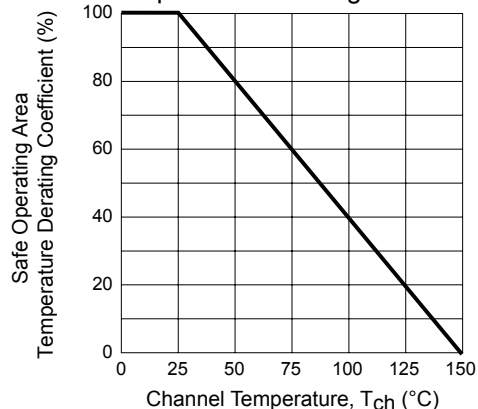
Electrical Characteristics of MOSFET Unless specifically noted, T_A is 25°C

Characteristic	Symbol	Test Conditions	Pins	Min.	Typ.	Max.	Unit
Drain-to-Source Breakdown Voltage	V_{DS}	LC5546LF	1 – 2	650	—	—	V
		LC5547LF		800	—	—	V
Drain Leakage Current	I_{DSS}		1 – 2	—	—	300	μA
On-Resistance	$R_{DS(ON)}$	LC5546LF	1 – 2	—	—	1.9	Ω
		LC5547LF		—	—	1.1	Ω
		LC5549LF		—	—	1.7	Ω
Switching Time	t_f	LC5546LF	1 – 2	—	—	400	ns
		LC5547LF		—	—	300	ns
		LC5549LF		—	—	300	ns
Thermal Resistance*	$R_{\theta ch-F}$	LC5546LF	1 – 2	—	—	3.1	$^{\circ}C/W$
		LC5547LF		—	—	2.2	$^{\circ}C/W$
		LC5549LF		—	—	2.2	$^{\circ}C/W$

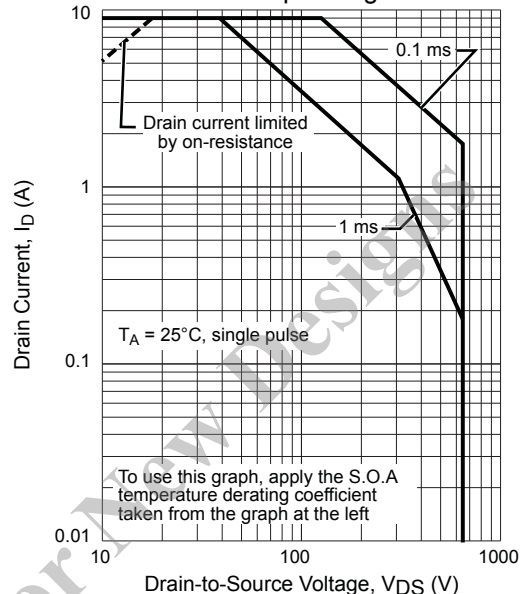
*The thermal resistance between the channels of the MOSFET and the internal frame.

Characteristic Performance LC5546LF

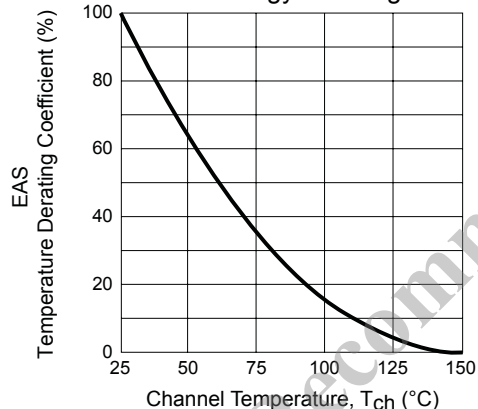
S. O. A. Temperature Derating Coefficient Curve



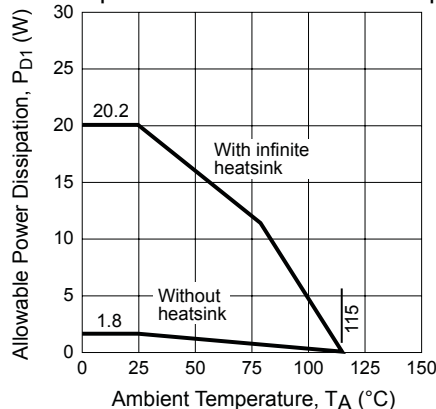
MOSFET Safe Operating Area Curve



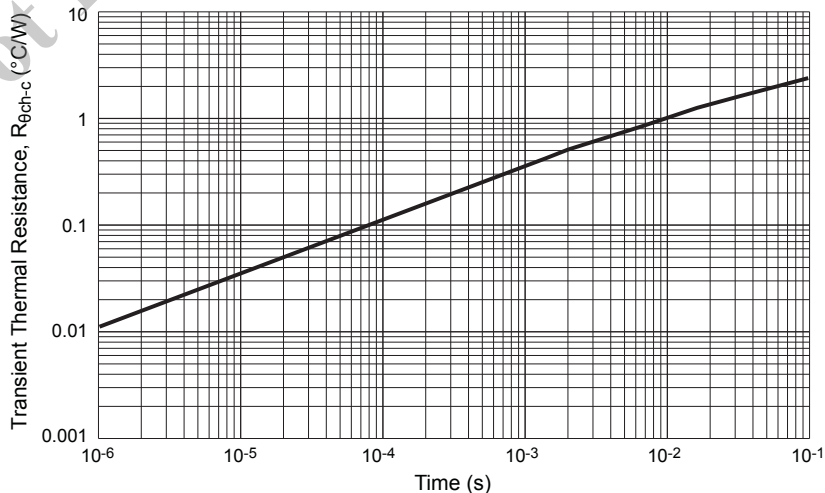
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve

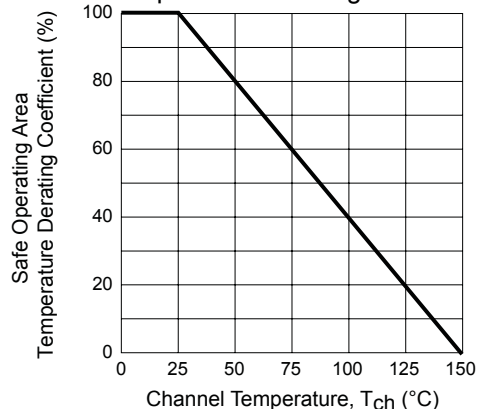


Transient Thermal Resistance Curve

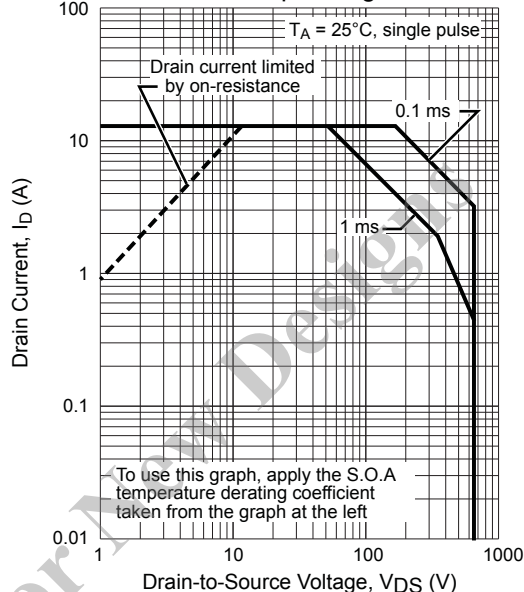


Characteristic Performance LC5547LF

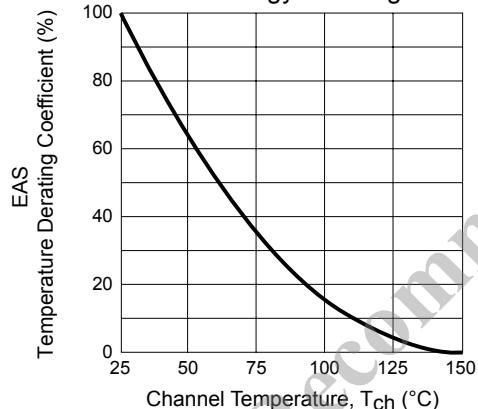
S. O. A. Temperature Derating Coefficient Curve



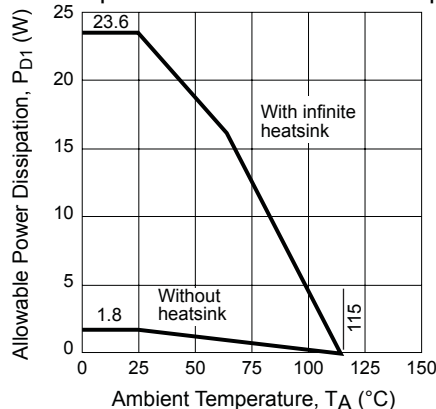
MOSFET Safe Operating Area Curve



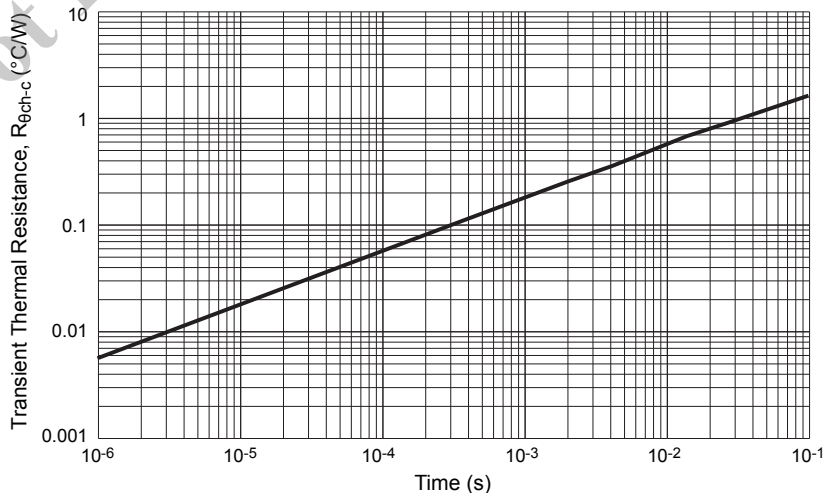
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve

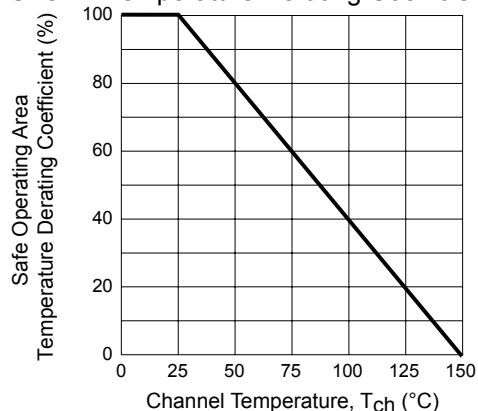


Transient Thermal Resistance Curve

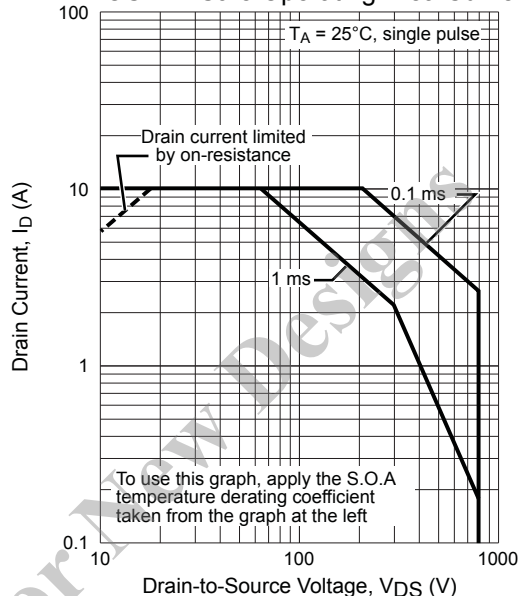


Characteristic Performance LC5549LF

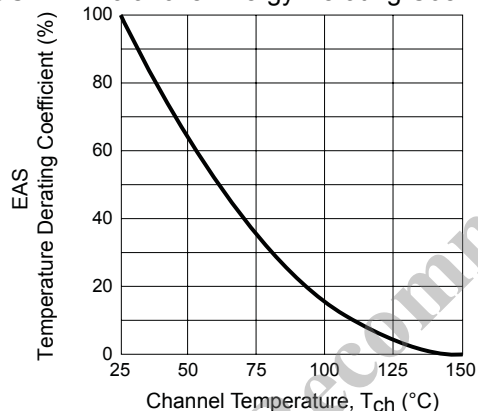
S. O. A. Temperature Derating Coefficient Curve



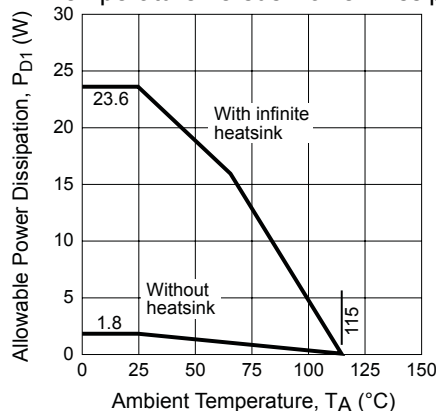
MOSFET Safe Operating Area Curve



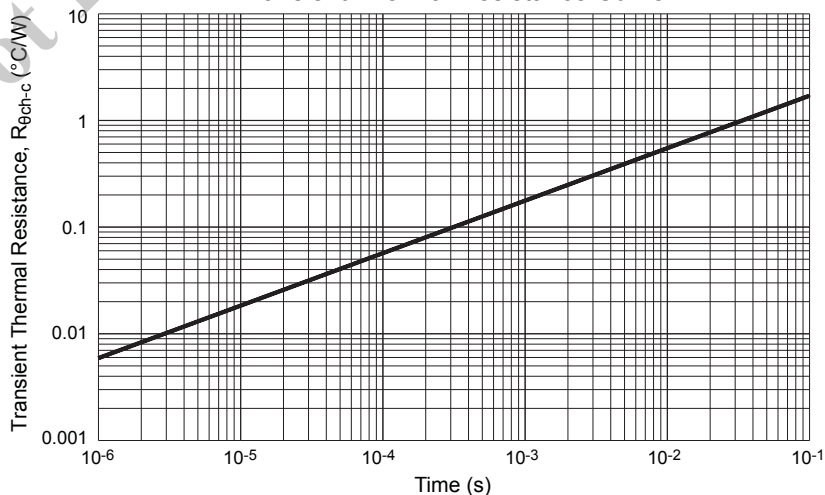
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve



Transient Thermal Resistance Curve



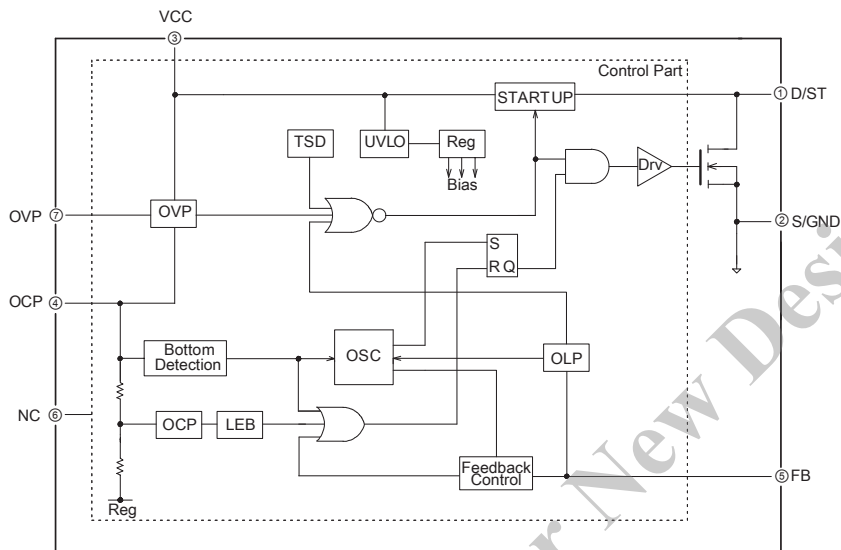
LC5540LF

Series

Single-Stage Power Factor Corrected

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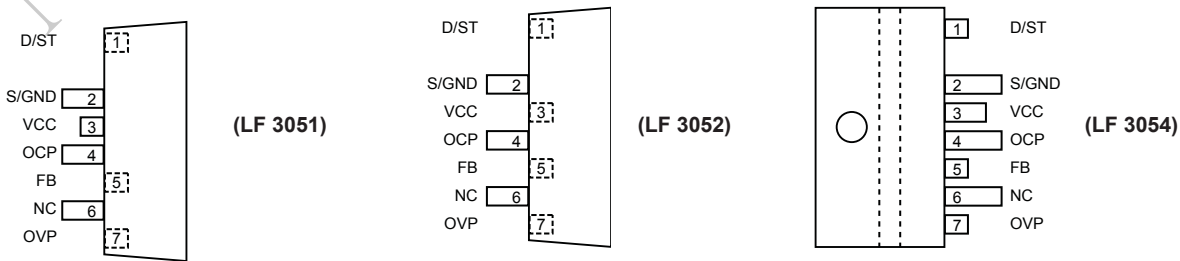
Functional Block Diagram



Pin List Table

Number	Name	Function
1	D/ST	MOSFET drain pin and input of the startup current
2	S/GND	MOSFET source and GND pin for the Control Part
3	VCC	Supply voltage input and Overvoltage protection (OVP) signal input
4	OCP	Overcurrent Protection, quasi-resonant signal input pin, and Overvoltage Protection (OVP) signal input
5	FB	Feedback signal input and Overload Protection (OLP) signal input
6	NC	No connection
7	OVP	Overvoltage Protection (OVP) signal input

Pin-out Diagrams



Technical drawing of the TO-220F-7L package showing front, side, and detail views with dimensions in mm.

Front View Dimensions:

- Gate burr: 10 ± 0.2
- Gate burr: 2.8 ± 0.2
- Gate burr: (5.6)
- Gate burr: 15 ± 0.3
- Gate burr: 10.4 ± 0.5
- Gate burr: 7-0.62 ± 0.15
- Gate burr: 7-0.55 ^{+0.2}/_{-0.1}
- Gate burr: 2 ± 0.15
- Gate burr: 5 × P1.17 ± 0.15 = 5.85 ± 0.15
- Gate burr: (At base of pin)

Side View Dimensions:

- Gate burr: 4.2 ± 0.2
- Gate burr: 2.6 ± 0.2
- Gate burr: Ø3.2 ± 0.2
- Gate burr: (1.1)
- Gate burr: 2.6 ± 0.1
- Gate burr: (At base of pin)
- Gate burr: 5 ± 0.5
- Gate burr: R-end
- Gate burr: 5 ± 0.5
- Gate burr: R-end
- Gate burr: 0.45 ^{+0.2}/_{-0.1}
- Gate burr: 2.54 ± 0.6
- Gate burr: (At tip of pin)
- Gate burr: 5.08 ± 0.6
- Gate burr: (At tip of pin)

Detail View Dimensions:

- Gate burr: 1
- Gate burr: 2
- Gate burr: 3
- Gate burr: 4
- Gate burr: 5
- Gate burr: 6
- Gate burr: 7

Front view and Side view dimensions:

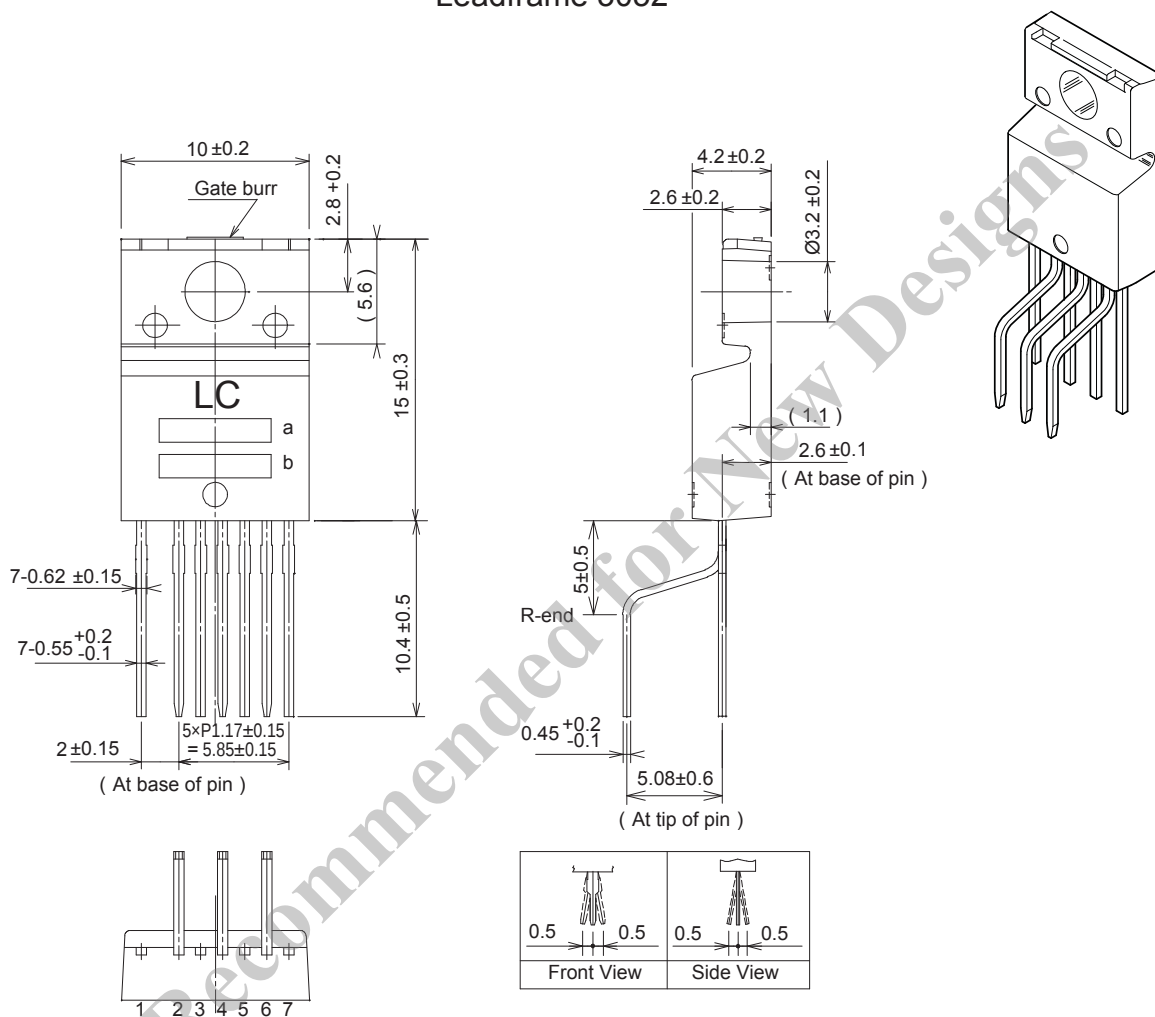
- Gate burr: 0.5
- Gate burr: 0.5
- Gate burr: 0.5
- Gate burr: 0.5
- Gate burr: Front view
- Gate burr: Side view

Unit: mm
Package: TO-220F-7L
a: Part # 55xxF

a: Part # 55xxF
b: Lot number
1st letter: Last digit of year
2nd letter: Month
Jan to September: Numeric
October: O
November: N
December: D
3rd and 4th letter: Date
01 to 31: Numeric
5th letter: Sanken control number

9
January 28, 2013

Package Outline Drawing Leadframe 3052

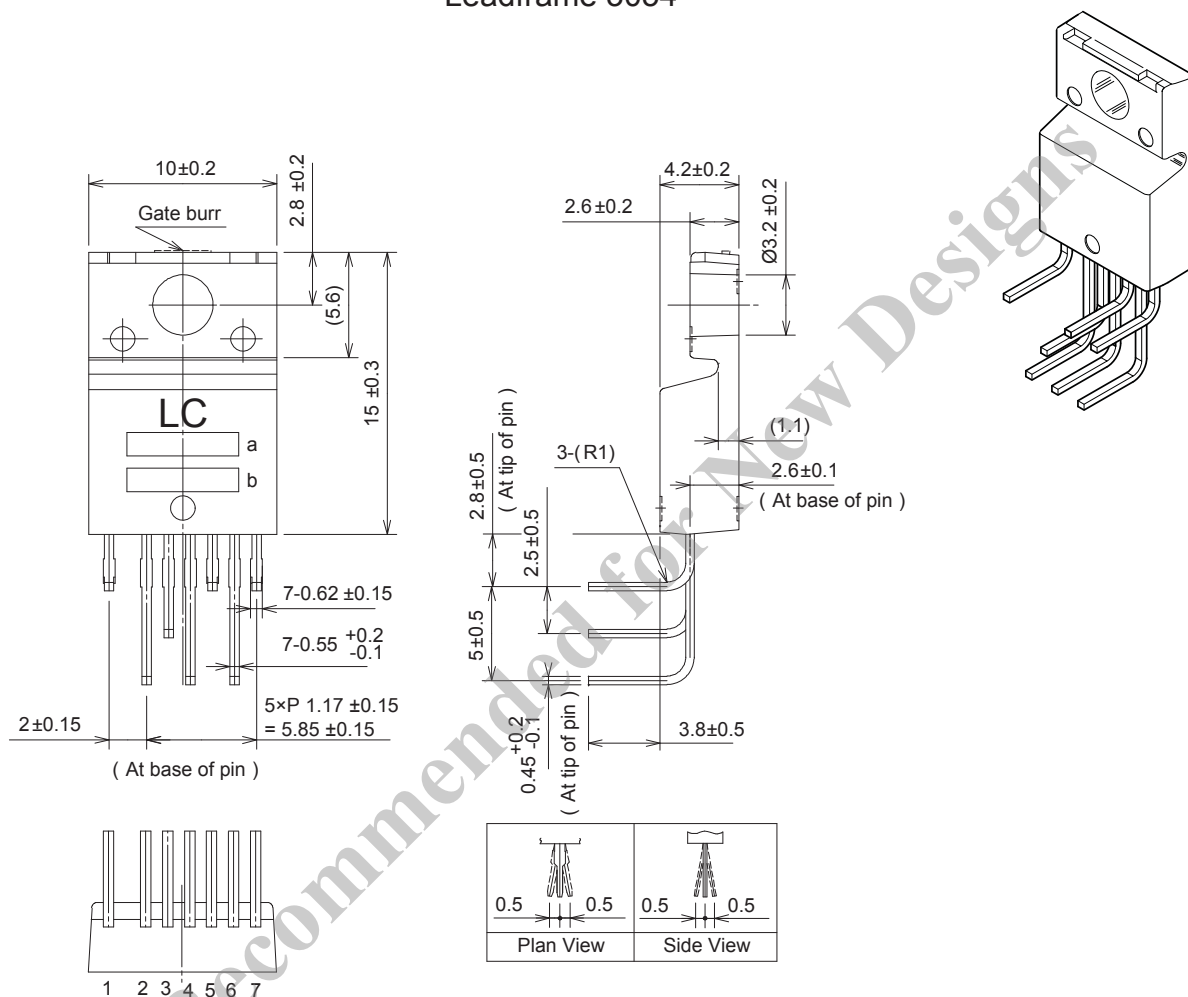


"Gate Burr" shows area
where 0.3 mm (max) gate
burr may be present

a: Part # 55xxLF
b: Lot number
1st letter: Last digit of year
2nd letter: Month
Jan to September: Numeric
October: O
November: N
December: D
3rd and 4th letter: Date
01 to 31: Numeric
5th letter: Sanken control number

Pin treatment Pb-free. Device composition
compliant with the RoHS directive.

Package Outline Drawing Leadframe 3054



Unit: mm

Package: TO-220F-7L

"Gate Burr" shows area
where 0.3 mm (max) gate
burr may be present

a: Part # 55xxLF

b: Lot number

1st letter: Last digit of year

2nd letter: Month

Jan to September: Numeric

October: O

November: N

December: D

3rd and 4th letter: Date

01 to 31: Numeric

5th letter: Sanken control number

Pin treatment Pb-free. Device composition
compliant with the RoHS directive.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting the products on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone greases with low consistency (hard grease) may cause cracks in the mold resin when screwing the products to a heatsink.

Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Type	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	Momentive Performance Materials Inc.
SC102	Dow Corning Toray Co., Ltd.

Cautions for Mounting to a Heatsink

- When the flatness around the screw hole is insufficient, such as when mounting the products to a heatsink that has an extruded (burred) screw hole, the products can be damaged, even with a lower than

recommended screw torque. For mounting the products, the mounting surface flatness should be 0.05 mm or less.

- Please select suitable screws for the product shape. Do not use a flat-head machine screw because of the stress to the products. Self-tapping screws are not recommended. When using self-tapping screws, the screw may enter the hole diagonally, not vertically, depending on the conditions of hole before threading or the work situation. That may stress the products and may cause failures.
- Recommended screw torque: 0.588 to 0.785 N•m (6 to 8 kgf•cm).
- For tightening screws, if a tightening tool (such as a driver) hits the products, the package may crack, and internal stress fractures may occur, which shorten the lifetime of the electrical elements and can cause catastrophic failure. Tightening with an air driver makes a substantial impact. In addition, a screw torque higher than the set torque can be applied and the package may be damaged. Therefore, an electric driver is recommended.

When the package is tightened at two or more places, first pre-tighten with a lower torque at all places, then tighten with the specified torque. When using a power driver, torque control is mandatory.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
260±5°C 10±1 s (Flow, 2 times)
380±10°C 3.5±0.5 s (Soldering iron, 1 time)
- Soldering should be at a distance of at least 2.0 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 MΩ of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

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- In the case that you use Sanken products or design your products by using Sanken products, the reliability largely depends on the degree of derating to be made to the rated values. Derating may be interpreted as a case that an operation range is set by derating the load from each rated value or surge voltage or noise is considered for derating in order to assure or improve the reliability. In general, derating factors include electric stresses such as electric voltage, electric current, electric power etc., environmental stresses such as ambient temperature, humidity etc. and thermal stress caused due to self-heating of semiconductor products. For these stresses, instantaneous values, maximum values and minimum values must be taken into consideration.

In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.

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