

USB3.0 Host Controller

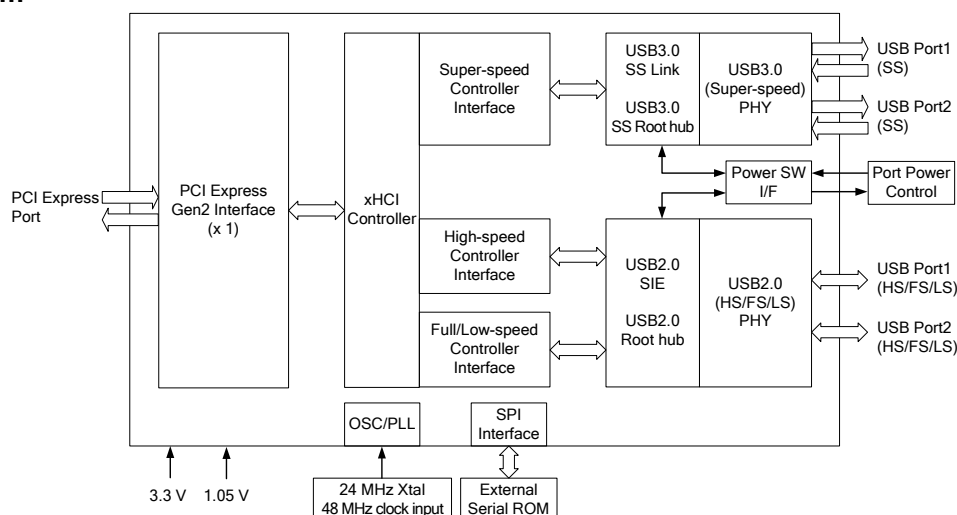
μ PD720200A



The μ PD720200A is the second generation Universal Serial Bus 3.0 host controller, which complies with Universal Serial Bus 3.0 Specification, and Intel's eXtensible Host Controller Interface (xHCI).

The μ PD720200A uses a PCI Express® Gen 2 system interface bus allowing system designers to easily add up to two USB3.0 SuperSpeed interfaces to systems containing the PCI Express bus interface. When connected to USB 3.0-compliant peripherals, the μ PD720200A can transfer information at clock speeds of up to 5 Gbps. The μ PD720200A and USB 3.0 standard are fully compliant and backward compatible with the previous USB2.0 Standard. The new USB 3.0 standard supports data transfer speeds of up to ten times faster than those of the previous-generation USB2.0 standard, enabling quick and efficient transfers of large amount of information.

Block Diagram



USB 3.0 Host Controller μ PD720200A Specification

Differences with the first generation μ PD720200

- Improved power consumption
- Pin compatible with the first generation μ PD720200

Features

- Universal Serial Bus 3.0 specification Revision 1.0
- PCI Express Base Specification Revision 2.0
- Intel's eXtensible Host Controller Interface (xHCI) Specification Revision 0.96
- PCI Express Card Electromechanical Specification Revision 2.0
- PCI Bus Power Management Interface Specification Revision 1.2
- Support USB legacy function
- Supports Serial Peripheral Interface (SPI) type ROM

- System clock : 24 MHz crystal or 48 MHz external clock
- 3.3 V and 1.05 V Power supply

Part Number

- μ PD720200AF1-DAP-A

Package

- 176-pin plastic FBGA (10 x 10 mm, 0.65 mm ball pitch)

Target Systems

- Desktop and Laptop computers
- PCI Express Card / Express Card
- Docking Station
- Server

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