

# SN54F138, SN74F138 3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS

SDFS051B – MARCH 1987 – REVISED JULY 1996

- Designed Specifically for High-Speed Memory Decoders and Data Transmission Systems
- Incorporates Three Enable Inputs to Simplify Cascading and/or Data Reception
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

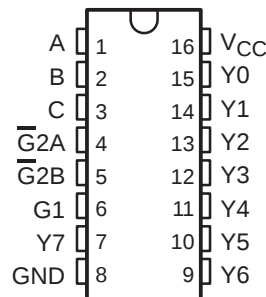
## description

The 'F138 is designed to be used in high-performance memory-decoding or data-routing applications requiring very short propagation delay times. In high-performance memory systems, these decoders can be used to minimize the effects of system decoding. When employed with high-speed memories utilizing a fast enable circuit, the delay times of this decoder and the enable time of the memory are usually less than the typical access time of the memory. This means that the effective system delay introduced by the decoder is negligible.

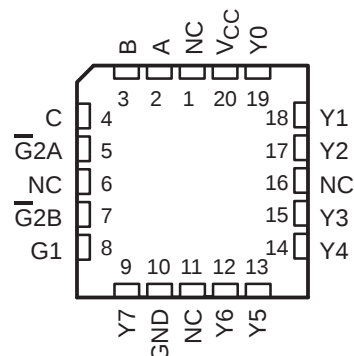
The conditions at the binary-select inputs and the three enable inputs select one of eight output lines. Two active-low and one active-high enable inputs reduce the need for external gates or inverters when expanding. A 24-line decoder can be implemented without external inverters and a 32-line decoder requires only one inverter. An enable input can be used as a data input for demultiplexing applications.

The SN54F138 is characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74F138 is characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

SN54F138 . . . J PACKAGE  
SN74F138 . . . D OR N PACKAGE  
(TOP VIEW)



SN54F138 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS  
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1996, Texas Instruments Incorporated

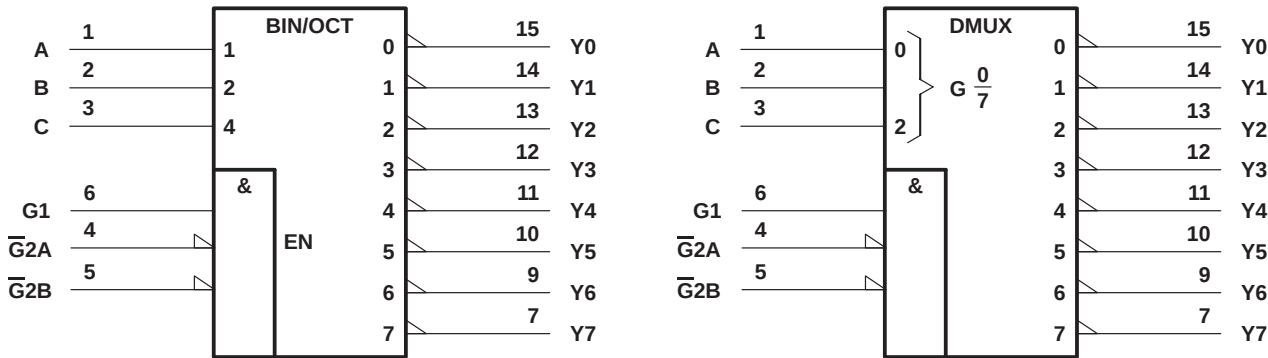
SN54F138, SN74F138  
3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS

SDFS051B – MARCH 1987 – REVISED JULY 1996

FUNCTION TABLE

| ENABLE INPUTS |                  |                  | SELECT INPUTS |   |   | OUTPUTS |    |    |    |    |    |    |    |
|---------------|------------------|------------------|---------------|---|---|---------|----|----|----|----|----|----|----|
| G1            | $\overline{G2A}$ | $\overline{G2B}$ | C             | B | A | Y0      | Y1 | Y2 | Y3 | Y4 | Y5 | Y6 | Y7 |
| X             | H                | X                | X             | X | X | H       | H  | H  | H  | H  | H  | H  | H  |
| X             | X                | H                | X             | X | X | H       | H  | H  | H  | H  | H  | H  | H  |
| L             | X                | X                | X             | X | X | H       | H  | H  | H  | H  | H  | H  | H  |
| H             | L                | L                | L             | L | L | L       | H  | H  | H  | H  | H  | H  | H  |
| H             | L                | L                | L             | L | H | H       | L  | H  | H  | H  | H  | H  | H  |
| H             | L                | L                | L             | H | L | H       | H  | L  | H  | H  | H  | H  | H  |
| H             | L                | L                | L             | H | H | H       | H  | L  | H  | H  | H  | H  | H  |
| H             | L                | L                | H             | L | L | H       | H  | H  | H  | L  | H  | H  | H  |
| H             | L                | L                | H             | L | H | H       | H  | H  | H  | H  | L  | H  | H  |
| H             | L                | L                | H             | H | L | H       | H  | H  | H  | H  | H  | L  | H  |
| H             | L                | L                | H             | H | H | H       | H  | H  | H  | H  | H  | H  | L  |

logic symbols (alternatives)<sup>†</sup>



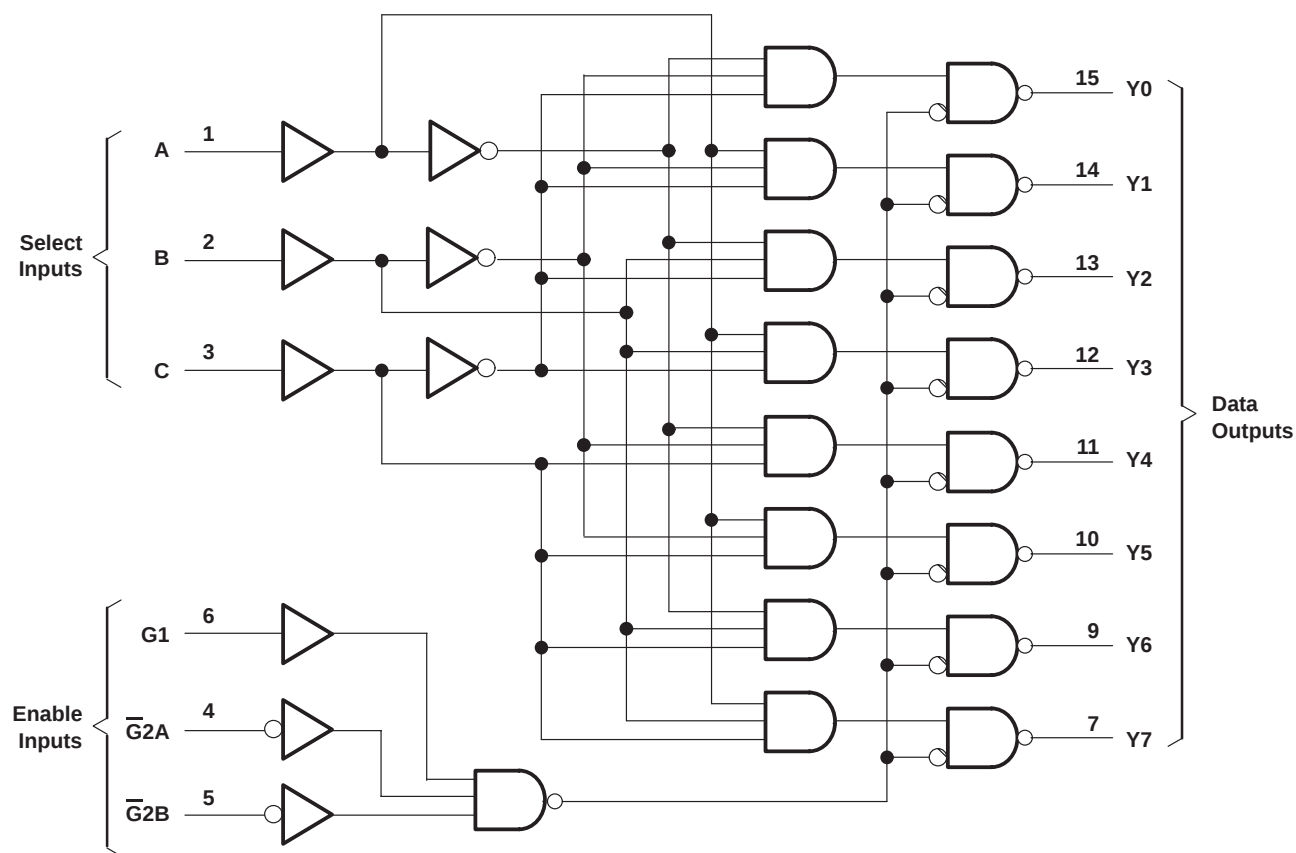
<sup>†</sup> These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

# SN54F138, SN74F138

## 3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS

SDFS051B – MARCH 1987 – REVISED JULY 1996

### logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

# SN54F138, SN74F138

## 3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS

SDFS051B – MARCH 1987 – REVISED JULY 1996

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                       |                    |
|-------------------------------------------------------|--------------------|
| Supply voltage range, $V_{CC}$                        | –0.5 V to 7 V      |
| Input voltage range, $V_I$ (see Note 1)               | –1.2 V to 7 V      |
| Input current range                                   | –30 mA to 5 mA     |
| Voltage range applied to any output in the high state | –0.5 V to $V_{CC}$ |
| Current into any output in the low state              | 40 mA              |
| Operating free-air temperature range: SN54F138        | –55°C to 125°C     |
| SN74F138                                              | 0°C to 70°C        |
| Storage temperature range                             | –65°C to 150°C     |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

### recommended operating conditions

|          |                                | SN54F138 |     |     | SN74F138 |     |     | UNIT |
|----------|--------------------------------|----------|-----|-----|----------|-----|-----|------|
|          |                                | MIN      | NOM | MAX | MIN      | NOM | MAX |      |
| $V_{CC}$ | Supply voltage                 | 4.5      | 5   | 5.5 | 4.5      | 5   | 5.5 | V    |
| $V_{IH}$ | High-level input voltage       | 2        |     |     | 2        |     |     | V    |
| $V_{IL}$ | Low-level input voltage        |          |     | 0.8 |          |     | 0.8 | V    |
| $I_{IK}$ | Input clamp current            |          |     | –18 |          |     | –18 | mA   |
| $I_{OH}$ | High-level output current      |          |     | –1  |          |     | –1  | mA   |
| $I_{OL}$ | Low-level output current       |          |     | 20  |          |     | 20  | mA   |
| $T_A$    | Operating free-air temperature | –55      |     | 125 | 0        |     | 70  | °C   |

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER     | TEST CONDITIONS                     | SN54F138 |                  |      | SN74F138 |                  |      | UNIT |
|---------------|-------------------------------------|----------|------------------|------|----------|------------------|------|------|
|               |                                     | MIN      | TYP <sup>‡</sup> | MAX  | MIN      | TYP <sup>‡</sup> | MAX  |      |
| $V_{IK}$      | $V_{CC} = 4.5$ V, $I_I = -18$ mA    |          |                  | –1.2 |          |                  | –1.2 | V    |
| $V_{OH}$      | $V_{CC} = 4.5$ V, $I_{OH} = -1$ mA  | 2.5      | 3.4              |      | 2.5      | 3.4              |      | V    |
|               | $V_{CC} = 4.75$ V, $I_{OH} = -1$ mA |          |                  |      | 2.7      |                  |      |      |
| $V_{OL}$      | $V_{CC} = 4.5$ V, $I_{OL} = 20$ mA  |          | 0.3              | 0.5  |          | 0.3              | 0.5  | V    |
| $I_I$         | $V_{CC} = 5.5$ V, $V_I = 7$ V       |          |                  | 0.1  |          |                  | 0.1  | mA   |
| $I_{IH}$      | $V_{CC} = 5.5$ V, $V_I = 2.7$ V     |          |                  | 20   |          |                  | 20   | μA   |
| $I_{IL}$      | $V_{CC} = 5.5$ V, $V_I = 0.5$ V     |          |                  | –0.6 |          |                  | –0.6 | mA   |
| $I_{OS}^{\S}$ | $V_{CC} = 5.5$ V, $V_O = 0$         | –60      |                  | –150 | –60      |                  | –150 | mA   |
| $I_{CC}$      | $V_{CC} = 5.5$ V, See Note 2        |          | 13               | 20   |          | 13               | 20   | mA   |

<sup>‡</sup> All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

<sup>\S</sup> Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

NOTE 2:  $I_{CC}$  is measured with outputs enabled and open.

# SN54F138, SN74F138

## 3-LINE TO 8-LINE DECODERS/DEMULTIPLEXERS

SDFS051B – MARCH 1987 – REVISED JULY 1996

### switching characteristics (see Note 3)

| PARAMETER        | FROM<br>(INPUT)                      | TO<br>(OUTPUT) | V <sub>CC</sub> = 5 V,<br>C <sub>L</sub> = 50 PF,<br>R <sub>L</sub> = 500 Ω,<br>T <sub>A</sub> = 25°C |     |     | V <sub>CC</sub> = 4.5 V TO 5.5 V,<br>C <sub>L</sub> = 50 PF,<br>R <sub>L</sub> = 500 Ω,<br>T <sub>A</sub> = MIN TO MAX† |      |          |     | UNIT |
|------------------|--------------------------------------|----------------|-------------------------------------------------------------------------------------------------------|-----|-----|-------------------------------------------------------------------------------------------------------------------------|------|----------|-----|------|
|                  |                                      |                | 'F138                                                                                                 |     |     | SN54F138                                                                                                                |      | SN74F138 |     |      |
|                  |                                      |                | MIN                                                                                                   | TYP | MAX | MIN                                                                                                                     | MAX  | MIN      | MAX |      |
| t <sub>PLH</sub> | A, B, or C                           | Y              | 2.7                                                                                                   | 5.2 | 7.5 | 2.7                                                                                                                     | 12   | 2.7      | 8.5 | ns   |
| t <sub>PHL</sub> |                                      |                | 3.2                                                                                                   | 5.7 | 8   | 3.2                                                                                                                     | 9.5  | 3.2      | 9   |      |
| t <sub>PLH</sub> | $\overline{G}2A$ or $\overline{G}2B$ | Y              | 2.7                                                                                                   | 5   | 7   | 2.7                                                                                                                     | 11   | 2.7      | 8   | ns   |
| t <sub>PHL</sub> |                                      |                | 2.2                                                                                                   | 4.9 | 7   | 2.2                                                                                                                     | 8    | 2.2      | 7.5 |      |
| t <sub>PLH</sub> | G1                                   | Y              | 3.2                                                                                                   | 5.8 | 8   | 3.2                                                                                                                     | 12.5 | 3.2      | 9   | ns   |
| t <sub>PHL</sub> |                                      |                | 2.7                                                                                                   | 5.2 | 7.5 | 2.7                                                                                                                     | 8.5  | 2.7      | 8.5 |      |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 3: Load circuits and waveforms are shown in Section 1.



## PACKAGING INFORMATION

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)     | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)                | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|---------------------|--------------------------------------|----------------------|--------------|----------------------------------------|-------------------------|
| 5962-9758201Q2A  | ACTIVE        | LCCC         | FK                 | 20   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-<br>9758201Q2A<br>SNJ54F<br>138FK | <a href="#">Samples</a> |
| 5962-9758201QEA  | ACTIVE        | CDIP         | J                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-9758201QE<br>A<br>SNJ54F138J      | <a href="#">Samples</a> |
| 5962-9758201QFA  | ACTIVE        | CFP          | W                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-9758201QF<br>A<br>SNJ54F138W      | <a href="#">Samples</a> |
| JM38510/33701B2A | ACTIVE        | LCCC         | FK                 | 20   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701B2A                   | <a href="#">Samples</a> |
| JM38510/33701BEA | ACTIVE        | CDIP         | J                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701BEA                   | <a href="#">Samples</a> |
| JM38510/33701BFA | ACTIVE        | CFP          | W                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701BFA                   | <a href="#">Samples</a> |
| M38510/33701B2A  | ACTIVE        | LCCC         | FK                 | 20   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701B2A                   | <a href="#">Samples</a> |
| M38510/33701BEA  | ACTIVE        | CDIP         | J                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701BEA                   | <a href="#">Samples</a> |
| M38510/33701BFA  | ACTIVE        | CFP          | W                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33701BFA                   | <a href="#">Samples</a> |
| SN54F138J        | ACTIVE        | CDIP         | J                  | 16   | 1              | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | SN54F138J                              | <a href="#">Samples</a> |
| SN74F138D        | ACTIVE        | SOIC         | D                  | 16   | 40             | RoHS & Green        | NIPDAU                               | Level-1-260C-UNLIM   | 0 to 70      | F138                                   | <a href="#">Samples</a> |
| SN74F138DR       | ACTIVE        | SOIC         | D                  | 16   | 2500           | RoHS & Green        | NIPDAU                               | Level-1-260C-UNLIM   | 0 to 70      | F138                                   | <a href="#">Samples</a> |
| SN74F138DRG4     | ACTIVE        | SOIC         | D                  | 16   | 2500           | RoHS & Green        | NIPDAU                               | Level-1-260C-UNLIM   | 0 to 70      | F138                                   | <a href="#">Samples</a> |
| SN74F138N        | ACTIVE        | PDIP         | N                  | 16   | 25             | RoHS & Green        | NIPDAU                               | N / A for Pkg Type   | 0 to 70      | SN74F138N                              | <a href="#">Samples</a> |
| SN74F138NE4      | ACTIVE        | PDIP         | N                  | 16   | 25             | RoHS & Green        | NIPDAU                               | N / A for Pkg Type   | 0 to 70      | SN74F138N                              | <a href="#">Samples</a> |
| SN74F138NSR      | ACTIVE        | SO           | NS                 | 16   | 2000           | RoHS & Green        | NIPDAU                               | Level-1-260C-UNLIM   | 0 to 70      | 74F138                                 | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package Drawing | Pins | Package Qty | Eco Plan<br>(2)     | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)                | Samples                 |
|------------------|---------------|--------------|-----------------|------|-------------|---------------------|--------------------------------------|----------------------|--------------|----------------------------------------|-------------------------|
| SNJ54F138FK      | ACTIVE        | LCCC         | FK              | 20   | 1           | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-<br>9758201Q2A<br>SNJ54F<br>138FK | <a href="#">Samples</a> |
| SNJ54F138J       | ACTIVE        | CDIP         | J               | 16   | 1           | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-9758201QE<br>A<br>SNJ54F138J      | <a href="#">Samples</a> |
| SNJ54F138W       | ACTIVE        | CFP          | W               | 16   | 1           | Non-RoHS<br>& Green | SNPB                                 | N / A for Pkg Type   | -55 to 125   | 5962-9758201QF<br>A<br>SNJ54F138W      | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and

continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**OTHER QUALIFIED VERSIONS OF SN54F138, SN74F138 :**

- Catalog : [SN74F138](#)
- Military : [SN54F138](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Military - QML certified for Military and Defense Applications

## TAPE AND REEL INFORMATION



\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74F138DR  | SOIC         | D               | 16   | 2500 | 330.0              | 16.4               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |
| SN74F138NSR | SO           | NS              | 16   | 2000 | 330.0              | 16.4               | 8.2     | 10.5    | 2.5     | 12.0    | 16.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74F138DR  | SOIC         | D               | 16   | 2500 | 340.5       | 336.1      | 32.0        |
| SN74F138NSR | SO           | NS              | 16   | 2000 | 367.0       | 367.0      | 38.0        |

## TUBE



\*All dimensions are nominal

| Device           | Package Name | Package Type | Pins | SPQ | L (mm) | W (mm) | T (μm) | B (mm) |
|------------------|--------------|--------------|------|-----|--------|--------|--------|--------|
| 5962-9758201Q2A  | FK           | LCCC         | 20   | 1   | 506.98 | 12.06  | 2030   | NA     |
| 5962-9758201QFA  | W            | CFP          | 16   | 1   | 506.98 | 26.16  | 6220   | NA     |
| JM38510/33701B2A | FK           | LCCC         | 20   | 1   | 506.98 | 12.06  | 2030   | NA     |
| JM38510/33701BFA | W            | CFP          | 16   | 1   | 506.98 | 26.16  | 6220   | NA     |
| M38510/33701B2A  | FK           | LCCC         | 20   | 1   | 506.98 | 12.06  | 2030   | NA     |
| M38510/33701BFA  | W            | CFP          | 16   | 1   | 506.98 | 26.16  | 6220   | NA     |
| SN74F138D        | D            | SOIC         | 16   | 40  | 507    | 8      | 3940   | 4.32   |
| SN74F138N        | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| SN74F138N        | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| SN74F138NE4      | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| SN74F138NE4      | N            | PDIP         | 16   | 25  | 506    | 13.97  | 11230  | 4.32   |
| SNJ54F138FK      | FK           | LCCC         | 20   | 1   | 506.98 | 12.06  | 2030   | NA     |
| SNJ54F138W       | W            | CFP          | 16   | 1   | 506.98 | 26.16  | 6220   | NA     |

## N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



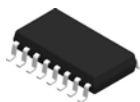
| PINS **             | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| DIM                 |                  |                  |                  |                  |
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



14/18 Pin Only  
20 Pin vendor option

4040049/E 12/2002

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.

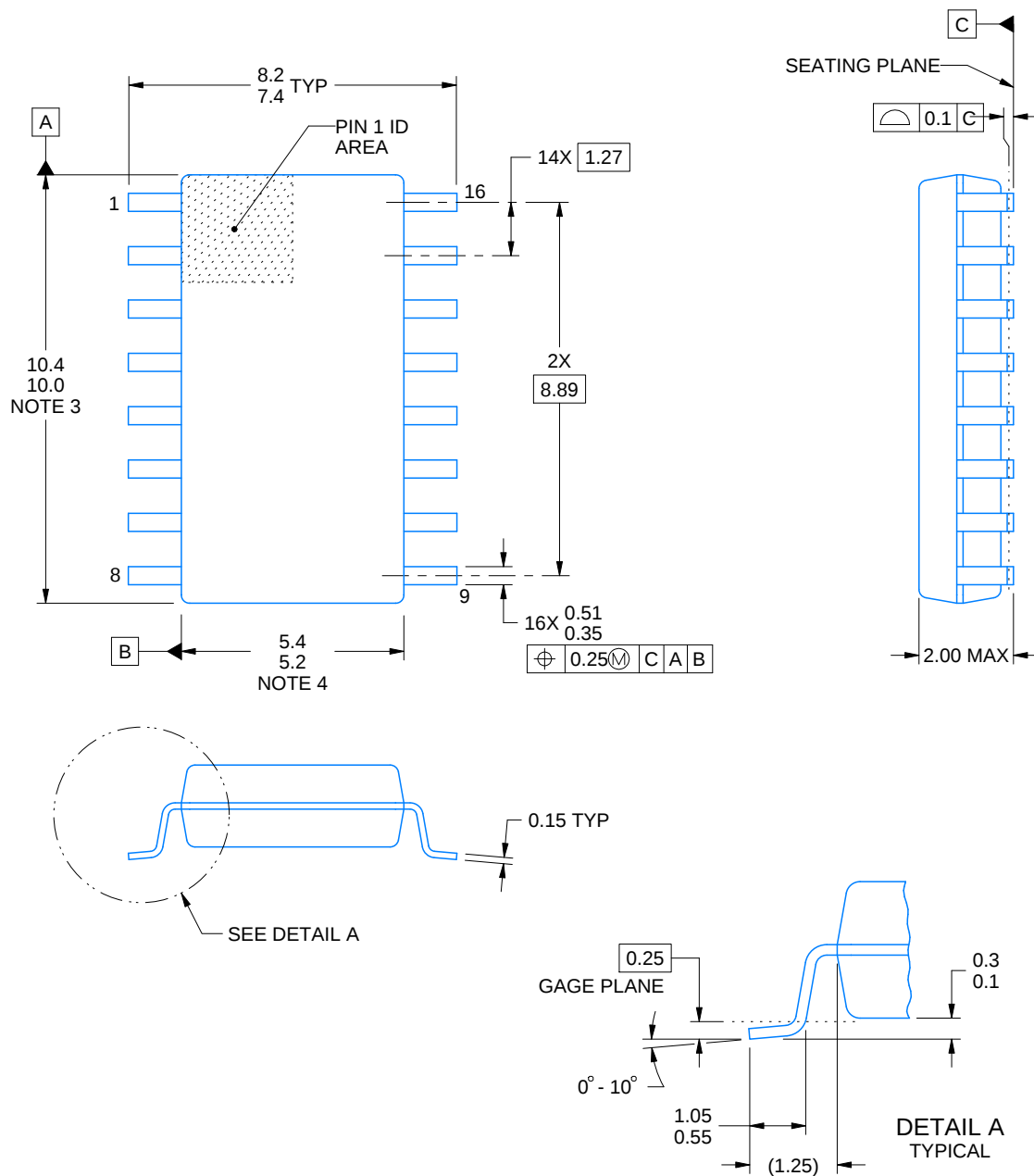


# PACKAGE OUTLINE

NS0016A

SOP - 2.00 mm max height

SOP

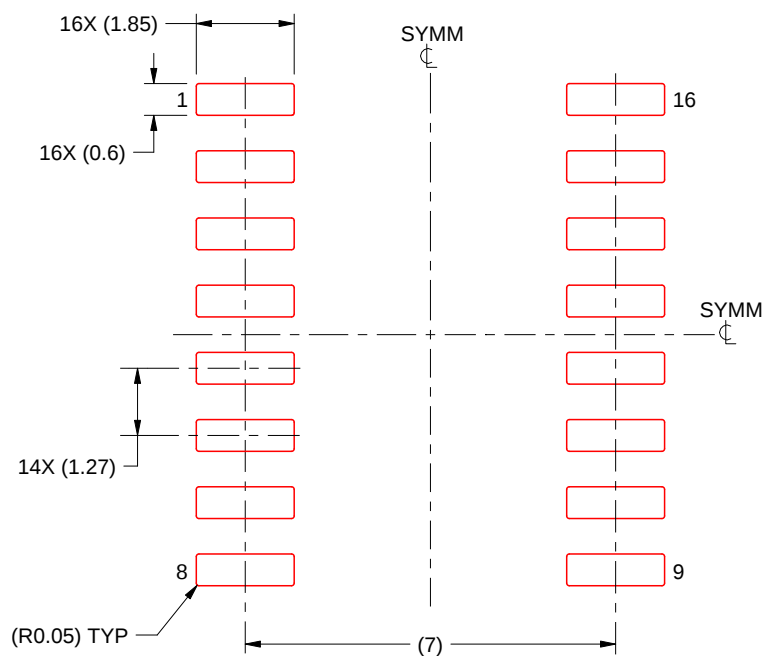


4220735/A 12/2021

## NOTES:

1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm, per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm, per side.





SOLDER PASTE EXAMPLE  
 BASED ON 0.125 mm THICK STENCIL  
 SCALE:7X

4220735/A 12/2021

NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
8. Board assembly site may have different recommendations for stencil design.

FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



| NO. OF<br>TERMINALS<br>** | A                |                  | B                |                  |
|---------------------------|------------------|------------------|------------------|------------------|
|                           | MIN              | MAX              | MIN              | MAX              |
| 20                        | 0.342<br>(8,69)  | 0.358<br>(9,09)  | 0.307<br>(7,80)  | 0.358<br>(9,09)  |
| 28                        | 0.442<br>(11,23) | 0.458<br>(11,63) | 0.406<br>(10,31) | 0.458<br>(11,63) |
| 44                        | 0.640<br>(16,26) | 0.660<br>(16,76) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 52                        | 0.740<br>(18,78) | 0.761<br>(19,32) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 68                        | 0.938<br>(23,83) | 0.962<br>(24,43) | 0.850<br>(21,6)  | 0.858<br>(21,8)  |
| 84                        | 1.141<br>(28,99) | 1.165<br>(29,59) | 1.047<br>(26,6)  | 1.063<br>(27,0)  |



4040140/D 01/11

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a metal lid.
  - Falls within JEDEC MS-004

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE



4040047-6/M 06/11

NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only.
  - Falls within MIL STD 1835 GDFP2-F16

J (R-GDIP-T\*\*)

14 LEADS SHOWN

# CERAMIC DUAL IN-LINE PACKAGE



| PINS **<br>DIM | 14                     | 16                     | 18                     | 20                     |
|----------------|------------------------|------------------------|------------------------|------------------------|
| A              | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC |
| B MAX          | 0.785<br>(19,94)       | .840<br>(21,34)        | 0.960<br>(24,38)       | 1.060<br>(26,92)       |
| B MIN          | —                      | —                      | —                      | —                      |
| C MAX          | 0.300<br>(7,62)        | 0.300<br>(7,62)        | 0.310<br>(7,87)        | 0.300<br>(7,62)        |
| C MIN          | 0.245<br>(6,22)        | 0.245<br>(6,22)        | 0.220<br>(5,59)        | 0.245<br>(6,22)        |



4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package is hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](#) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2022, Texas Instruments Incorporated