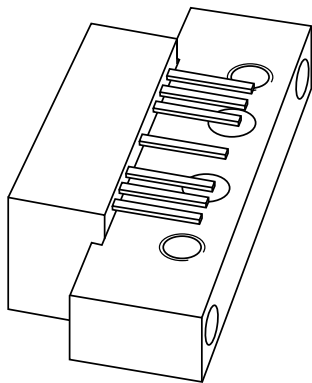


DATA SHEET



BGY888

860 MHz, 34 dB gain push-pull
amplifier

Product specification
Supersedes data of 1999 Mar 30

2001 Oct 25



860 MHz, 34 dB gain push-pull amplifier

BGY888

FEATURES

- Excellent linearity
- Extremely low noise
- High gain
- Excellent return loss properties.

APPLICATIONS

- Single module line extender in CATV systems operating over a frequency range of 40 to 860 MHz.

DESCRIPTION

Hybrid high dynamic range amplifier module operating with a voltage supply of 24 V in a SOT115J package. The high gain module consists of two cascaded stages both in cascode configuration.

PINNING SOT115J

PIN	DESCRIPTION
1	input
2, 3	common
5	+V _B
7, 8	common
9	output

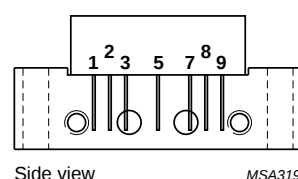


Fig.1 Simplified outline.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 50 MHz	33.5	34.5	dB
		f = 860 MHz	34	—	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	—	340	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V _i	RF input voltage	—	55	dBmV
T _{stg}	storage temperature	−40	+100	°C
T _{mb}	operating mounting base temperature	−20	+100	°C

860 MHz, 34 dB gain push-pull amplifier

BGY888

CHARACTERISTICS

Table 1 Bandwidth 40 to 860 MHz; $V_B = 24\text{ V}$; $T_{\text{case}} = 30\text{ }^{\circ}\text{C}$; $Z_S = Z_L = 75\text{ }\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	$f = 50\text{ MHz}$	33.5	34	34.5	dB
		$f = 860\text{ MHz}$	34	35	–	dB
SL	slope cable equivalent	$f = 40\text{ to }860\text{ MHz}$	0.5	1.1	2.5	dB
FL	flatness of frequency response	$f = 40\text{ to }860\text{ MHz}$	–	± 0.2	± 0.5	dB
S_{11}	input return losses	$f = 40\text{ to }80\text{ MHz}$	20	25	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	28	–	dB
		$f = 320\text{ to }640\text{ MHz}$	15.5	21	–	dB
		$f = 640\text{ to }860\text{ MHz}$	14	18.5	–	dB
S_{22}	output return losses	$f = 40\text{ to }80\text{ MHz}$	20	25.5	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28.5	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	26.5	–	dB
		$f = 320\text{ to }640\text{ MHz}$	15.5	20.5	–	dB
		$f = 640\text{ to }860\text{ MHz}$	14	21	–	dB
S_{21}	phase response	$f = 50\text{ MHz}$	135	–	225	deg
CTB	composite triple beat	49 channels flat; $V_o = 44\text{ dBmV}$; measured at 859.25 MHz	–	–63.5	–60	dB
X_{mod}	cross modulation	49 channels flat; $V_o = 44\text{ dBmV}$; measured at 55.25 MHz	–	–63	–59	dB
CSO	composite second order distortion	49 channels flat; $V_o = 44\text{ dBmV}$; measured at 860.5 MHz	–	–64	–55	dB
d_2	second order distortion	note 1	–	–74	–65	dB
V_o	output voltage	$d_{\text{im}} = -60\text{ dB}$; note 2	58	60	–	dBmV
F	noise figure	$f = 50\text{ MHz}$	–	4	4.5	dB
		$f = 550\text{ MHz}$	–	–	5	dB
		$f = 600\text{ MHz}$	–	–	5	dB
		$f = 650\text{ MHz}$	–	–	5.5	dB
		$f = 750\text{ MHz}$	–	–	6	dB
		$f = 860\text{ MHz}$	–	5.5	7	dB
I_{tot}	total current consumption (DC)	note 3	–	325	340	mA

Notes

- $f_p = 55.25\text{ MHz}$; $V_p = 44\text{ dBmV}$;
 $f_q = 805.25\text{ MHz}$; $V_q = 44\text{ dBmV}$;
measured at $f_p + f_q = 860.5\text{ MHz}$.
- Measured according to DIN45004B:
 $f_p = 851.25\text{ MHz}$; $V_p = V_o$;
 $f_q = 858.25\text{ MHz}$; $V_q = V_o - 6\text{ dB}$;
 $f_r = 860.25\text{ MHz}$; $V_r = V_o - 6\text{ dB}$;
measured at $f_p + f_q - f_r = 849.25\text{ MHz}$.
- The module normally operates at $V_B = 24\text{ V}$, but is able to withstand supply transients up to 30 V.

860 MHz, 34 dB gain push-pull amplifier

BGY888

Table 2 Bandwidth 40 to 860 MHz; $V_B = 24\text{ V}$; $T_{\text{case}} = 30\text{ }^{\circ}\text{C}$; $Z_S = Z_L = 75\text{ }\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	$f = 50\text{ MHz}$	33.5	34	34.5	dB
		$f = 860\text{ MHz}$	34	35	–	dB
SL	slope cable equivalent	$f = 40\text{ to }860\text{ MHz}$	0.5	1.1	2.5	dB
FL	flatness of frequency response	$f = 40\text{ to }860\text{ MHz}$	–	± 0.2	± 0.5	dB
S_{11}	input return losses	$f = 40\text{ to }80\text{ MHz}$	20	25	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	28	–	dB
		$f = 320\text{ to }640\text{ MHz}$	15.5	21	–	dB
		$f = 640\text{ to }860\text{ MHz}$	14	18.5	–	dB
S_{22}	output return losses	$f = 40\text{ to }80\text{ MHz}$	20	25.5	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28.5	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	26.5	–	dB
		$f = 320\text{ to }640\text{ MHz}$	15.5	20.5	–	dB
		$f = 640\text{ to }860\text{ MHz}$	14	21	–	dB
S_{21}	phase response	$f = 50\text{ MHz}$	135	–	225	deg
CTB	composite triple beat	129 channels flat; $V_o = 44\text{ dBmV}$; measured at 859.25 MHz	–	–47.5	–46	dB
X_{mod}	cross modulation	129 channels flat; $V_o = 44\text{ dBmV}$; measured at 55.25 MHz	–	–53.5	–50	dB
CSO	composite second order distortion	129 channels flat; $V_o = 44\text{ dBmV}$; measured at 860.5 MHz	–	–56	–48	dB
d_2	second order distortion	note 1	–	–74	–65	dB
V_o	output voltage	$d_{\text{im}} = -60\text{ dB}$; note 2	58	60	–	dBmV
F	noise figure	see Table 1	–	–	–	dB
I_{tot}	total current consumption (DC)	note 3	–	325	340	mA

Notes

- $f_p = 55.25\text{ MHz}$; $V_p = 44\text{ dBmV}$;
 $f_q = 805.25\text{ MHz}$; $V_q = 44\text{ dBmV}$;
measured at $f_p + f_q = 860.5\text{ MHz}$.
- Measured according to DIN45004B:
 $f_p = 851.25\text{ MHz}$; $V_p = V_o$;
 $f_q = 858.25\text{ MHz}$; $V_q = V_o - 6\text{ dB}$;
 $f_r = 860.25\text{ MHz}$; $V_r = V_o - 6\text{ dB}$;
measured at $f_p + f_q - f_r = 849.25\text{ MHz}$.
- The module normally operates at $V_B = 24\text{ V}$, but is able to withstand supply transients up to 30 V.

860 MHz, 34 dB gain push-pull amplifier

BGY888

Table 3 Bandwidth 40 to 750 MHz; $V_B = 24$ V; $T_{case} = 30$ °C; $Z_S = Z_L = 75 \Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	$f = 50$ MHz	33.5	34	34.5	dB
		$f = 750$ MHz	34	–	–	dB
SL	slope cable equivalent	$f = 40$ to 750 MHz	0.2	–	2.2	dB
FL	flatness of frequency response	$f = 40$ to 750 MHz	–	–	± 0.45	dB
S_{11}	input return losses	$f = 40$ to 80 MHz	20	25	–	dB
		$f = 80$ to 160 MHz	18.5	28	–	dB
		$f = 160$ to 320 MHz	17	28	–	dB
		$f = 320$ to 640 MHz	15.5	21	–	dB
		$f = 640$ to 750 MHz	14	18.5	–	dB
S_{22}	output return losses	$f = 40$ to 80 MHz	20	25.5	–	dB
		$f = 80$ to 160 MHz	18.5	28.5	–	dB
		$f = 160$ to 320 MHz	17	26.5	–	dB
		$f = 320$ to 640 MHz	15.5	20.5	–	dB
		$f = 640$ to 750 MHz	14	21	–	dB
S_{21}	phase response	$f = 50$ MHz	135	–	225	deg
CTB	composite triple beat	110 channels flat; $V_o = 44$ dBmV; measured at 745.25 MHz	–	–52.5	–50	dB
X_{mod}	cross modulation	110 channels flat; $V_o = 44$ dBmV; measured at 55.25 MHz	–	–55.5	–51	dB
CSO	composite second order distortion	110 channels flat; $V_o = 44$ dBmV; measured at 746.5 MHz	–	–61.5	–53	dB
d_2	second order distortion	note 1	–	–	–65	dB
V_o	output voltage	$d_{im} = -60$ dB; note 2	59	–	–	dBmV
F	noise figure	see Table 1	–	–	–	dB
I_{tot}	total current consumption (DC)	note 3	–	325	340	mA

Notes

- $f_p = 55.25$ MHz; $V_p = 44$ dBmV;
 $f_q = 691.25$ MHz; $V_q = 44$ dBmV;
measured at $f_p + f_q = 746.5$ MHz.
- Measured according to DIN45004B:
 $f_p = 740.25$ MHz; $V_p = V_o$;
 $f_q = 747.25$ MHz; $V_q = V_o - 6$ dB;
 $f_r = 749.25$ MHz; $V_r = V_o - 6$ dB;
measured at $f_p + f_q - f_r = 738.25$ MHz.
- The module normally operates at $V_B = 24$ V, but is able to withstand supply transients up to 30 V.

860 MHz, 34 dB gain push-pull amplifier

BGY888

Table 4 Bandwidth 40 to 600 MHz; $V_B = 24\text{ V}$; $T_{\text{case}} = 30\text{ }^{\circ}\text{C}$; $Z_S = Z_L = 75\text{ }\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G_p	power gain	$f = 50\text{ MHz}$	33.5	34	34.5	dB
		$f = 600\text{ MHz}$	34	–	–	dB
SL	slope cable equivalent	$f = 40\text{ to }600\text{ MHz}$	0	–	2	dB
FL	flatness of frequency response	$f = 40\text{ to }600\text{ MHz}$	–	–	± 0.35	dB
S_{11}	input return losses	$f = 40\text{ to }80\text{ MHz}$	20	25	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	28	–	dB
		$f = 320\text{ to }600\text{ MHz}$	16	21	–	dB
S_{22}	output return losses	$f = 40\text{ to }80\text{ MHz}$	20	25.5	–	dB
		$f = 80\text{ to }160\text{ MHz}$	18.5	28.5	–	dB
		$f = 160\text{ to }320\text{ MHz}$	17	26.5	–	dB
		$f = 320\text{ to }600\text{ MHz}$	16	20.5	–	dB
S_{21}	phase response	$f = 50\text{ MHz}$	135	–	225	deg
CTB	composite triple beat	85 channels flat; $V_o = 44\text{ dBmV}$; measured at 595.25 MHz	–	–56.5	–55	dB
X_{mod}	cross modulation	85 channels flat; $V_o = 44\text{ dBmV}$; measured at 55.25 MHz	–	–58	–54	dB
CSO	composite second order distortion	85 channels flat; $V_o = 44\text{ dBmV}$; measured at 596.5 MHz	–	–69.5	–56	dB
d_2	second order distortion	note 1	–	–	–68	dB
V_o	output voltage	$d_{\text{im}} = -60\text{ dB}$; note 2	61	–	–	dBmV
F	noise figure (DC)	see Table 1	–	–	–	dB
I_{tot}	total current consumption	note 3	–	325	340	mA

Notes

- $f_p = 55.25\text{ MHz}$; $V_p = 44\text{ dBmV}$;
 $f_q = 541.25\text{ MHz}$; $V_q = 44\text{ dBmV}$;
measured at $f_p + f_q = 596.5\text{ MHz}$.
- Measured according to DIN45004B:
 $f_p = 590.25\text{ MHz}$; $V_p = V_o$;
 $f_q = 597.25\text{ MHz}$; $V_q = V_o - 6\text{ dB}$;
 $f_r = 599.25\text{ MHz}$; $V_r = V_o - 6\text{ dB}$;
measured at $f_p + f_q - f_r = 588.25\text{ MHz}$.
- The module normally operates at $V_B = 24\text{ V}$, but is able to withstand supply transients up to 30 V.

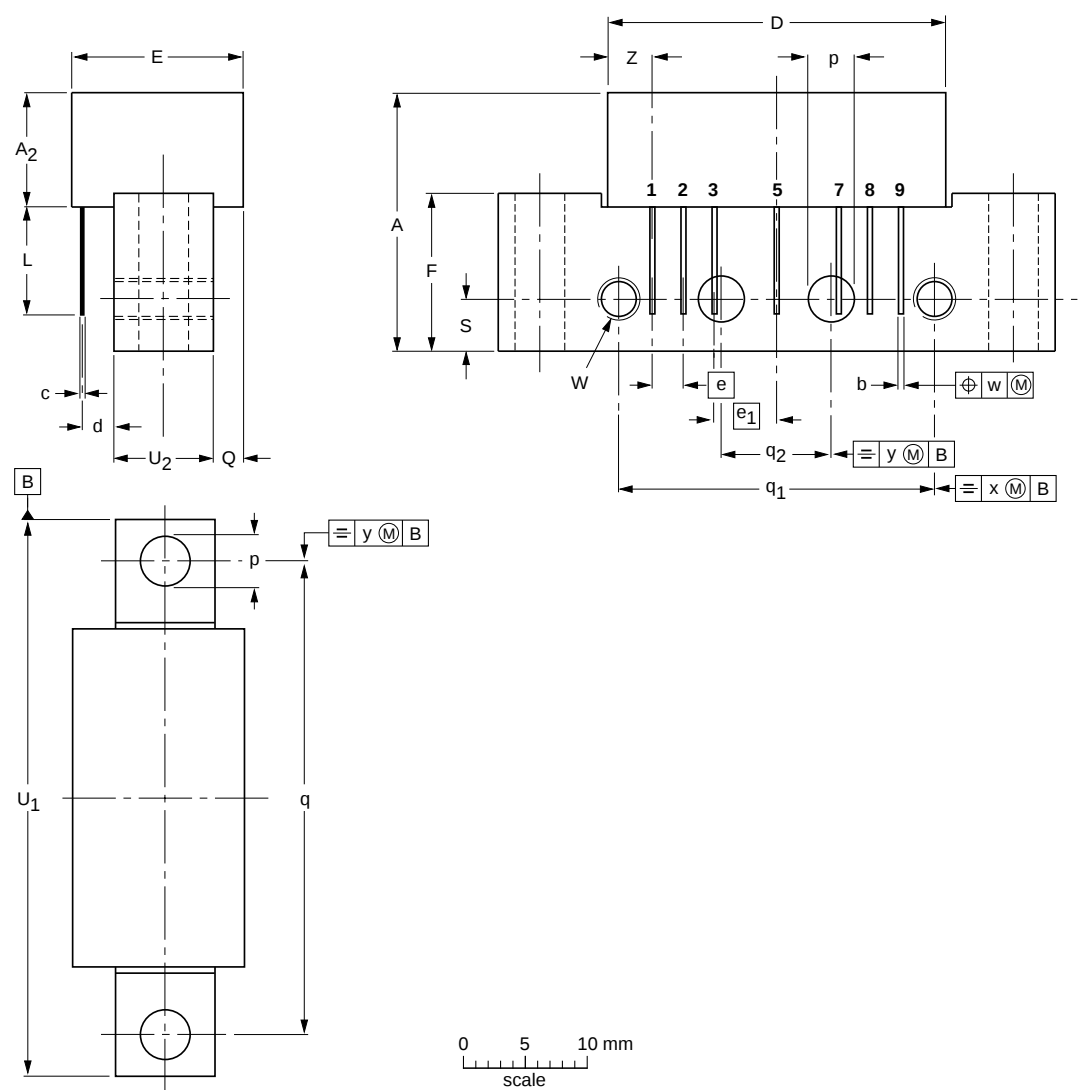
860 MHz, 34 dB gain push-pull amplifier

BGY888

PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes;
2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₂ max.	b	c	D max.	d	E max.	e	e ₁	F	L min.	p	Q max.	q	q ₁	q ₂	S	U ₁	U ₂	W	w	x	y	Z max.
mm	20.8	9.5	0.51 0.38	0.25	27.2	2.04 2.54	13.75	2.54	5.08	12.7	8.8	4.15 3.85	2.4	38.1	25.4	10.2	4.2	44.75 44.25	8.2 7.8	6-32 UNC	0.25	0.7	0.1	3.8

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT115J						04-02-04 10-06-18

860 MHz, 34 dB gain push-pull amplifier

BGY888

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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860 MHz, 34 dB gain push-pull amplifier

BGY888

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This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: **<http://www.nxp.com>**

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