

Lens Motor Driver for Camera Solution

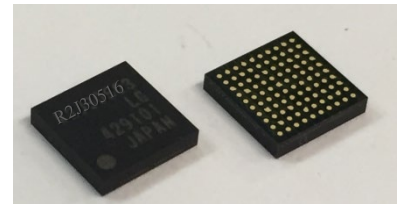
Lens Motor Driver with OIS/ R2J30516LG#U4

Feature

- P/N: R2J30516LG#U4
- Built-in H8S-16bit CPU core
 - Focusing on OIS-servo control
- Built-in own DSP core
 - Possible high-speed operation & Flexibility
 - Easy to implement Digital Filter depending situation
- Multi-channel Lens driver circuits
 - For IS (Image Stabilizer)
 - Available to support high-speed PWM output for VCM driving circuit
 - For Lens × 6ch as usual case
 - × 7ch at exclusive control mode
 - Available to support high-speed PWM output and Silent design by fine 512-steps Micro control
- 12bit-ADC: High resolution and Fast conversion
- Outline: Package "100pin LGA" & 5.5mm²-size
- Others
 - Supporting Digital I/F for connecting Gyro sensor
 - Compliant for 1.8V regulator
 - Necessary to use Debugging tool E10A

Package Outline

100pin LGA 5.5 × 5.5mm
t = 1.05mm
Pin pitch = 0.5mm

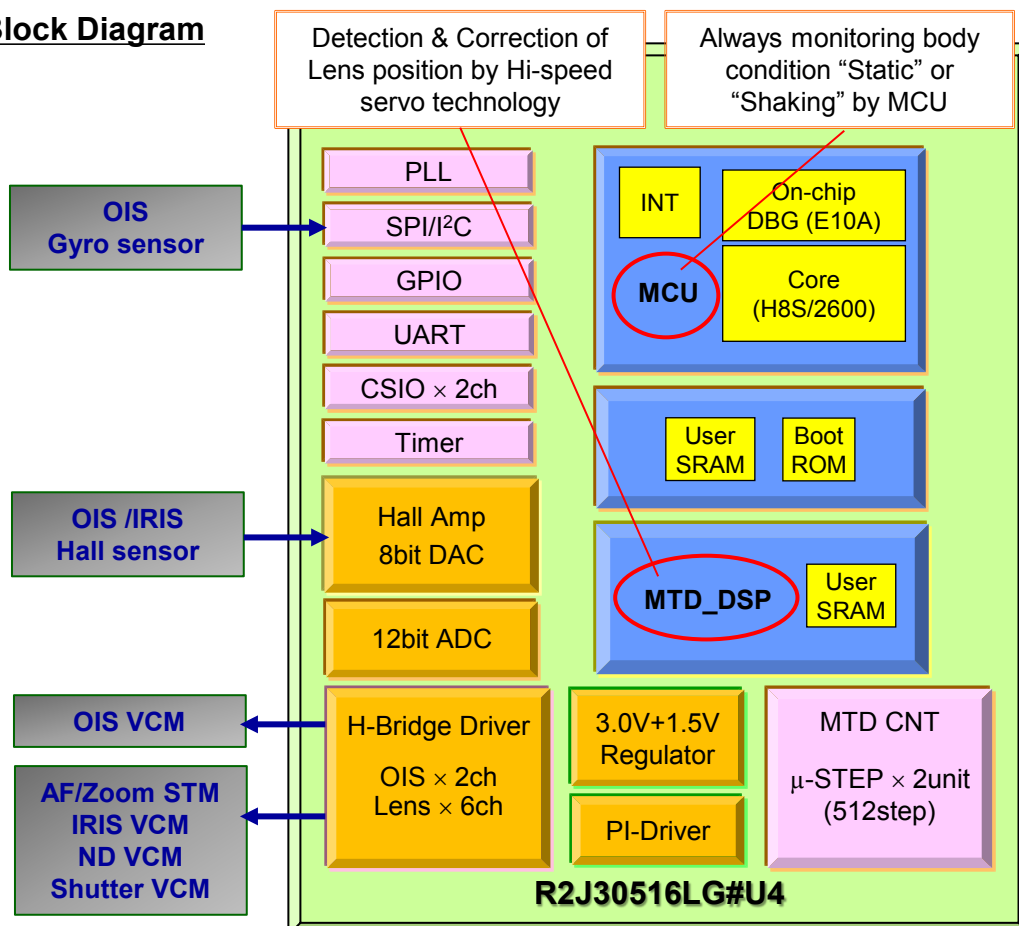


http://resource.renesas.com/lib/package/pdf/outdrawing/ptlg0100ka_a.pdf

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System Block Diagram



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