

## Rail-to-rail high output current quad operational amplifiers with standby mode and adjustable phantom ground

### Features

- Rail-to-rail input and output
- Low noise: 9 nV/√Hz
- Low distortion
- High output current: 80 mA (able to drive 32 Ω loads)
- High-speed: 4 MHz, 1.3 V/μs
- Operating range from 2.7 to 12 V
- Low input offset voltage: 900 μV max. (TS925A)
- Adjustable phantom ground ( $V_{CC}/2$ )
- Standby mode
- ESD internal protection: 2 kV
- Latch-up immunity

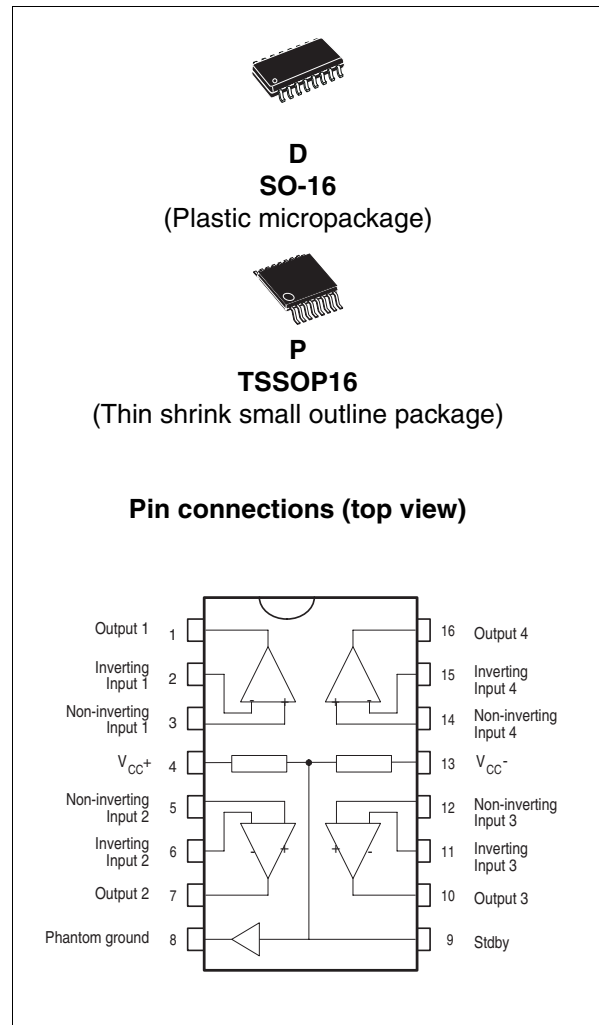
### Applications

- Headphone amplifiers
- Soundcard amplifiers, piezoelectric speakers
- MPEG boards, multimedia systems
- Cordless telephones and portable communication equipment
- Line drivers, buffers
- Instrumentation with low noise as key factor

### Description

The TS925 is a rail-to-rail quad BiCMOS operational amplifier optimized and fully specified for 3- and 5-V operation.

High output current allows low load impedances to be driven. An internal low impedance phantom ground eliminates the need for an external reference voltage or biasing arrangement.



The TS925 exhibits very low noise, low distortion and high output current, making this device an excellent choice for high-quality, low-voltage or battery-operated audio/telecom systems.

The device is stable for capacitive loads up to 500 pF. When the STANDBY mode is enabled, the total consumption drops to 6 μA ( $V_{CC} = 3\text{ V}$ ).

# 1 Absolute maximum ratings and operating conditions

**Table 1. Absolute maximum ratings**

| Symbol            | Parameter                                 | Conditions                             | Value                                        | Unit |
|-------------------|-------------------------------------------|----------------------------------------|----------------------------------------------|------|
| V <sub>CC</sub>   | Supply voltage <sup>(1)</sup>             |                                        | 14                                           | V    |
| V <sub>id</sub>   | Differential input voltage <sup>(2)</sup> |                                        | ±1                                           | V    |
| V <sub>i</sub>    | Input voltage                             |                                        | V <sub>DD</sub> -0.3 to V <sub>CC</sub> +0.3 | V    |
| T <sub>j</sub>    | Maximum junction temperature              |                                        | 150                                          | °C   |
| R <sub>thja</sub> | Thermal resistance junction to ambient    | SO-16<br>TSSOP16                       | 95<br>95                                     | °C/W |
| R <sub>thjc</sub> | Thermal resistance junction to case       | SO-16<br>TSSOP16                       | 30<br>25                                     | °C/W |
| ESD               | Electrostatic discharge                   | HBM<br>Human body model <sup>(3)</sup> | 2                                            | kV   |
|                   |                                           | MM<br>Machine model <sup>(4)</sup>     | 200                                          | V    |
|                   |                                           | CDM<br>Charged device model            | 1                                            | kV   |
|                   | Output short circuit duration             |                                        | See note <sup>(5)</sup>                      |      |
|                   | Latch-up immunity                         |                                        | 200                                          | mA   |
|                   | Soldering temperature                     | 10 sec,<br>Pb-free package             | 260                                          | °C   |

1. All voltage values, except differential voltage are with respect to network ground terminal.
2. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal. If V<sub>id</sub> > ±1 V, the maximum input current must not exceed ±1 mA. In this case (V<sub>id</sub> > ±1 V), an input series resistor must be added to limit input current.
3. Human body model: 100 pF discharged through a 1.5 kΩ resistor into pin of device.
4. Machine model ESD: a 200 pF cap is charged to the specified voltage, then discharged directly into the IC with no external series resistor (internal resistor < 5 Ω), into pin-to-pin of device.
5. There is no short-circuit protection inside the device: short-circuits from the output to V<sub>CC</sub> can cause excessive heating. The maximum output current is approximately 80 mA, independent of the magnitude of V<sub>CC</sub>. Destructive dissipation can result from simultaneous short-circuits on all amplifiers.

**Table 2. Operating conditions**

| Symbol            | Parameter                            | Value                                        | Unit |
|-------------------|--------------------------------------|----------------------------------------------|------|
| V <sub>CC</sub>   | Supply voltage                       | 2.7 to 12                                    | V    |
| V <sub>icm</sub>  | Common mode input voltage range      | V <sub>DD</sub> -0.2 to V <sub>CC</sub> +0.2 | V    |
| T <sub>oper</sub> | Operating free air temperature range | -40 to +125                                  | °C   |

## 2 Electrical characteristics

**Table 3.** Electrical characteristics for  $V_{CC} = 3\text{ V}$  with  $V_{DD} = 0\text{ V}$ ,  $V_{icm} = V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb} = 25^\circ\text{C}$  (unless otherwise specified)

| Symbol                | Parameter                                                | Conditions                                                                                                           | Min.         | Typ.            | Max.                     | Unit                         |
|-----------------------|----------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------|--------------|-----------------|--------------------------|------------------------------|
| <b>DC performance</b> |                                                          |                                                                                                                      |              |                 |                          |                              |
| $V_{io}$              | Input offset voltage                                     | At $T_{amb} = +25^\circ\text{C}$<br>TS925<br>TS925A<br>At $T_{min.} \leq T_{amb} \leq T_{max.}$ :<br>TS925<br>TS925A |              |                 | 3<br>0.9<br><br>5<br>1.8 | mV                           |
| $DV_{io}$             | Input offset voltage drift                               |                                                                                                                      |              | 2               |                          | $\mu\text{V}/^\circ\text{C}$ |
| $I_{io}$              | Input offset current                                     | $V_{out} = 1.5\text{V}$                                                                                              |              | 1               | 30                       | nA                           |
| $I_{ib}$              | Input bias current                                       | $V_{out} = 2.5\text{V}$                                                                                              |              | 15              | 100                      | nA                           |
| CMR                   | Common mode rejection ratio                              | $V_{icm}$ from 0 to 3 V<br>$T_{min} \leq T_{amb} \leq T_{max}$                                                       | 60           | 80              |                          | dB                           |
| $V_{OH}$              | High level output voltage                                | $R_L = 10\text{k}\Omega$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                                                    | 2.90<br>2.87 | 2.63            |                          | V                            |
| $V_{OL}$              | Low level output voltage                                 | $R_L = 10\text{k}\Omega$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                                                    |              | 180             | 50<br>100                | mV                           |
| $A_{vd}$              | Large signal voltage gain                                | $V_{out} = 2V_{pk-pk}$<br>$R_L = 10\text{k}\Omega$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                          |              | 200<br>35<br>16 |                          | V/mV                         |
| SVR                   | Supply voltage rejection ratio                           | $V_{cc} = 2.7$ to $3.3\text{V}$                                                                                      | 60           | 85              |                          | dB                           |
| $I_o$                 | Output short-circuit current                             |                                                                                                                      | 50           | 80              |                          | mA                           |
| $I_{CC}$              | Total supply current                                     | No load, $V_{out} = V_{cc}/2$                                                                                        |              | 5               | 7                        | mA                           |
| $I_{stby}$            | Total supply current in STANDBY                          | Pin 9 connected to $V_{cc-}$                                                                                         |              | 6               |                          | $\mu\text{A}$                |
| $V_{enstby}$          | Pin 9 voltage to enable the STANDBY mode <sup>(1)</sup>  | at $T_{amb} = +25^\circ\text{C}$<br>at $T_{min} \leq T_{amb} \leq T_{max}$                                           |              |                 | 0.3<br>0.4               | V                            |
| $V_{distby}$          | Pin 9 voltage to disable the STANDBY mode <sup>(1)</sup> | at $T_{amb} = +25^\circ\text{C}$<br>at $T_{min} \leq T_{amb} \leq T_{max}$                                           | 1.1<br>1     |                 |                          | V                            |
| <b>AC performance</b> |                                                          |                                                                                                                      |              |                 |                          |                              |
| GBP                   | Gain bandwidth product                                   | $R_L = 600\Omega$                                                                                                    |              | 4               |                          | MHz                          |
| SR                    | Slew rate                                                |                                                                                                                      | 0.7          | 1.3             |                          | V/ $\mu\text{s}$             |
| Pm                    | Phase margin at unit gain                                | $R_L = 600\Omega$ , $C_L = 100\text{pF}$                                                                             |              | 68              |                          | Degrees                      |

**Table 3. Electrical characteristics for  $V_{CC} = 3\text{ V}$  with  $V_{DD} = 0\text{ V}$ ,  $V_{icm} = V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb} = 25^\circ\text{ C}$  (unless otherwise specified) (continued)**

| Symbol                | Parameter                                             | Conditions                                                                                               | Min.              | Typ.            | Max.              | Unit                                 |
|-----------------------|-------------------------------------------------------|----------------------------------------------------------------------------------------------------------|-------------------|-----------------|-------------------|--------------------------------------|
| GM                    | Gain margin                                           | $R_L = 600\Omega$ , $C_L = 100\text{pF}$                                                                 |                   | 12              |                   | dB                                   |
| $e_n$                 | Equivalent input noise voltage                        | $f = 1\text{kHz}$                                                                                        |                   | 9               |                   | $\frac{\text{nV}}{\sqrt{\text{Hz}}}$ |
| THD                   | Total harmonic distortion                             | $V_{out} = 2V_{pk-pk}$ ,<br>$f = 1\text{kHz}$ , $A_v = 1$ ,<br>$R_L = 600\Omega$                         |                   | 0.01            |                   | %                                    |
| $C_s$                 | Channel separation                                    |                                                                                                          |                   | 120             |                   | dB                                   |
| <b>Phantom ground</b> |                                                       |                                                                                                          |                   |                 |                   |                                      |
| $V_{pg}$              | Phantom ground output voltage                         | No output current                                                                                        | $V_{cc}/2$<br>-5% | $V_{cc}/2$      | $V_{cc}/2$<br>+5% | V                                    |
| $I_{pgsc}$            | Phantom ground output short circuit current - sourced |                                                                                                          | 12                | 18              |                   | mA                                   |
| $Z_{pg}$              | Phantom ground impedance                              | DC to 20kHz                                                                                              |                   | 3               |                   | $\Omega$                             |
| $E_{npg}$             | Phantom ground output voltage noise                   | $f = 1\text{kHz}$<br>$C_{dec} = 100\text{pF}$<br>$C_{dec} = 1\text{nF}$<br>$C_{dec} = 10\text{nF}^{(2)}$ |                   | 200<br>40<br>17 |                   | $\frac{\text{nV}}{\sqrt{\text{Hz}}}$ |
| $I_{pgsk}$            | Phantom ground output short circuit current - sinked  |                                                                                                          | 12                | 18              |                   | mA                                   |

1. The STANDBY mode is enabled when pin 9 is GROUNDED and disabled when pin 9 is left OPEN.
2.  $C_{dec}$  is the decoupling capacitor on pin 9.

**Table 4. Electrical characteristics for  $V_{CC} = 5\text{ V}$ ,  $V_{DD} = 0\text{ V}$ ,  $V_{icm} = V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb} = 25^\circ\text{C}$  (unless otherwise specified)**

| Symbol                | Parameter                                                | Conditions                                                                                                             | Min.         | Typ.            | Max.                     | Unit                         |
|-----------------------|----------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------|--------------|-----------------|--------------------------|------------------------------|
| <b>DC performance</b> |                                                          |                                                                                                                        |              |                 |                          |                              |
| $V_{io}$              | Input offset voltage                                     | At $T_{amb} = +25^\circ\text{C}$ :<br>TS925<br>TS925A<br>At $T_{min.} \leq T_{amb} \leq T_{max.}$ :<br>TS925<br>TS925A |              |                 | 3<br>0.9<br><br>5<br>1.8 | mV                           |
| $DV_{io}$             | Input offset voltage drift                               |                                                                                                                        |              | 2               |                          | $\mu\text{V}/^\circ\text{C}$ |
| $I_{io}$              | Input offset current                                     | $V_{out} = 2.5\text{V}$                                                                                                |              | 1               | 30                       | nA                           |
| $I_{ib}$              | Input bias current                                       | $V_{out} = 2.5\text{V}$                                                                                                |              | 15              | 100                      | nA                           |
| CMR                   | Common mode rejection ratio                              | $V_{icm}$ from 0 to 5 V<br>$T_{min} \leq T_{amb} \leq T_{max}$                                                         | 60           | 80              |                          | dB                           |
| $V_{OH}$              | High level output voltage                                | $R_L = 10\text{k}\Omega$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                                                      | 4.90<br>4.85 | 4.4             |                          | V                            |
| $V_{OL}$              | Low level output voltage                                 | $R_L = 10\text{k}\Omega$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                                                      |              | 300             | 50<br>120                | mV                           |
| $A_{vd}$              | Large signal voltage gain                                | $V_{out} = 2V_{pk-pk}$<br>$R_L = 10\text{k}$<br>$R_L = 600\Omega$<br>$R_L = 32\Omega$                                  |              | 200<br>40<br>17 |                          | V/mV                         |
| SVR                   | Supply voltage rejection ratio                           | $V_{CC} = 3$ to $5\text{V}$                                                                                            | 60           | 85              |                          | dB                           |
| $I_o$                 | Output short-circuit current                             |                                                                                                                        | 50           | 80              |                          | mA                           |
| $I_{CC}$              | Total supply current                                     | No load, $V_{out} = V_{CC}/2$                                                                                          |              | 6               | 8                        | mA                           |
| $I_{stby}$            | Total supply current in STANDBY                          | Pin 9 connected to $V_{CC-}$                                                                                           |              | 6               |                          | $\mu\text{A}$                |
| $V_{enstby}$          | Pin 9 Voltage to enable the STANDBY mode <sup>(1)</sup>  | at $T_{amb} = +25^\circ\text{C}$<br>at $T_{min} \leq T_{amb} \leq T_{max}$                                             |              |                 | 0.3<br>0.4               | V                            |
| $V_{distby}$          | Pin 9 voltage to disable the STANDBY mode <sup>(1)</sup> | at $T_{amb} = +25^\circ\text{C}$<br>at $T_{min} \leq T_{amb} \leq T_{max}$                                             | 1.1<br>1     |                 |                          | V                            |
| <b>AC performance</b> |                                                          |                                                                                                                        |              |                 |                          |                              |
| GBP                   | Gain bandwidth product                                   | $R_L = 600\Omega$                                                                                                      |              | 4               |                          | MHz                          |
| SR                    | Slew rate                                                |                                                                                                                        | 0.7          | 1.3             |                          | V/ $\mu\text{s}$             |
| Pm                    | Phase margin at unit gain                                | $R_L = 600\Omega$ , $C_L = 100\text{pF}$                                                                               |              | 68              |                          | Degrees                      |
| GM                    | Gain margin                                              | $R_L = 600\Omega$ , $C_L = 100\text{pF}$                                                                               |              | 12              |                          | dB                           |

**Table 4. Electrical characteristics for  $V_{CC} = 5\text{ V}$ ,  $V_{DD} = 0\text{ V}$ ,  $V_{icm} = V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb} = 25^\circ\text{ C}$  (unless otherwise specified) (continued)**

| Symbol                | Parameter                                             | Conditions                                                                                                   | Min.              | Typ.            | Max.              | Unit                                 |
|-----------------------|-------------------------------------------------------|--------------------------------------------------------------------------------------------------------------|-------------------|-----------------|-------------------|--------------------------------------|
| $e_n$                 | Equivalent input noise voltage                        | $f = 1\text{ kHz}$                                                                                           |                   | 9               |                   | $\frac{\text{nV}}{\sqrt{\text{Hz}}}$ |
| THD                   | Total harmonic distortion                             | $V_{out} = 2V_{pk-pk}$ ,<br>$f = 1\text{ kHz}$ , $A_v = 1$ ,<br>$R_L = 600\Omega$                            |                   | 0.01            |                   | %                                    |
| $C_s$                 | Channel separation                                    |                                                                                                              |                   | 120             |                   | dB                                   |
| <b>Phantom ground</b> |                                                       |                                                                                                              |                   |                 |                   |                                      |
| $V_{pg}$              | Phantom ground output voltage                         | No output current                                                                                            | $V_{cc/2}$<br>-5% | $V_{cc/2}$      | $V_{cc/2}$<br>+5% | V                                    |
| $I_{pgsc}$            | Phantom ground output short circuit current - sourced |                                                                                                              | 12                | 18              |                   | mA                                   |
| $Z_{pg}$              | Phantom ground impedance                              | DC to 20kHz                                                                                                  |                   | 3               |                   | $\Omega$                             |
| $E_{npg}$             | Phantom ground output voltage noise                   | $f = 1\text{ kHz}$<br>$C_{dec} = 100\text{ pF}$<br>$C_{dec} = 1\text{ nF}$<br>$C_{dec} = 10\text{ nF}^{(2)}$ |                   | 200<br>40<br>17 |                   | $\frac{\text{nV}}{\sqrt{\text{Hz}}}$ |
| $I_{pgsk}$            | Phantom ground output short circuit current - sinked  |                                                                                                              | 12                | 18              |                   | mA                                   |

1. The STANDBY mode is enabled when pin 9 is GROUNDED and disabled when pin 9 is left OPEN.
2.  $C_{dec}$  is the decoupling capacitor on pin 9.

Figure 1. Input offset voltage distribution

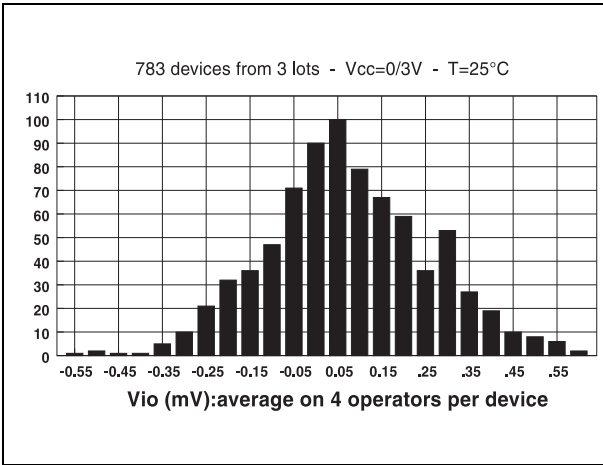


Figure 2. Total supply current vs. supply voltage with no load

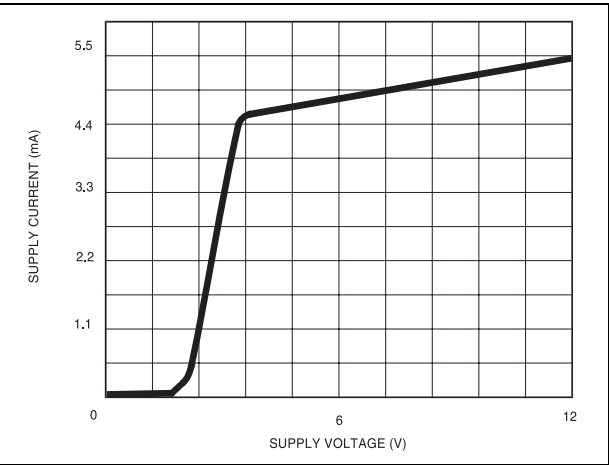


Figure 3. Supply current/amplifier vs. temperature

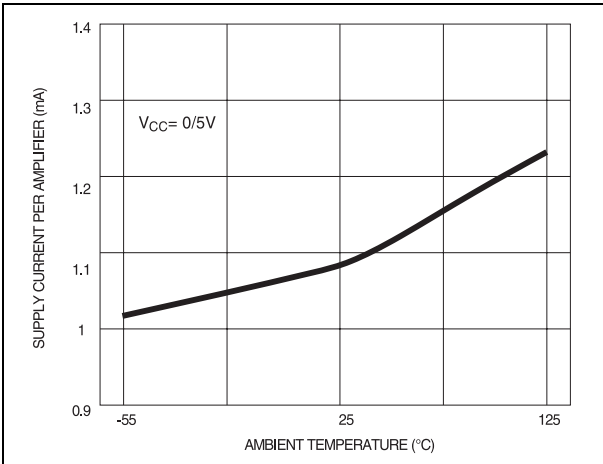


Figure 4. Output short circuit current vs. output voltage

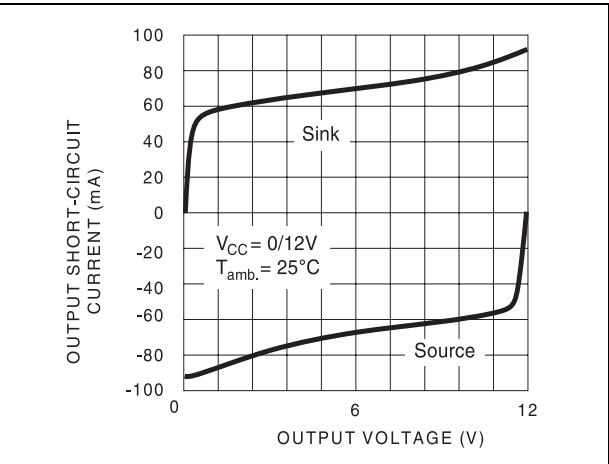


Figure 5. Output short circuit current vs. output voltage

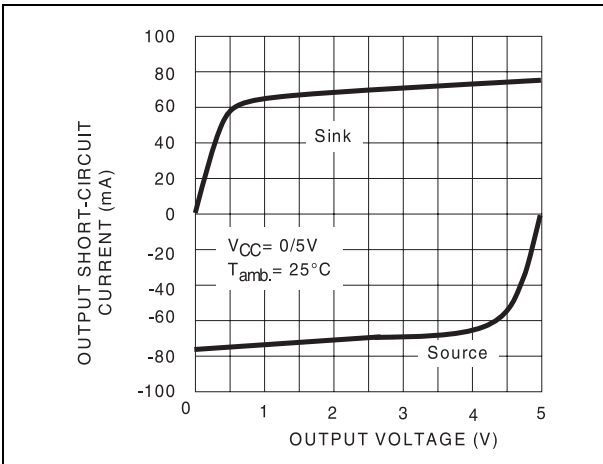


Figure 6. Output short circuit current vs. output voltage

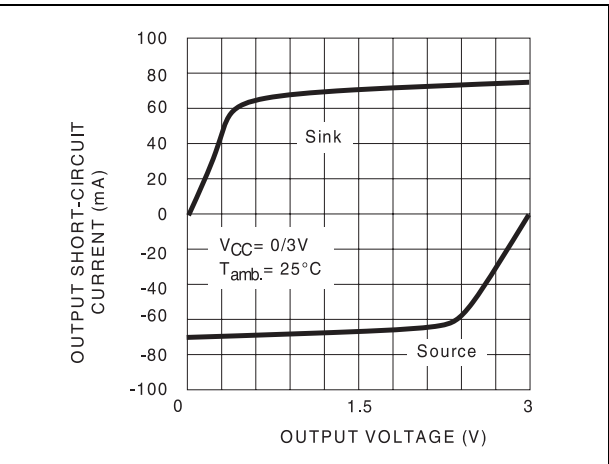


Figure 7. Output short circuit current vs. temperature

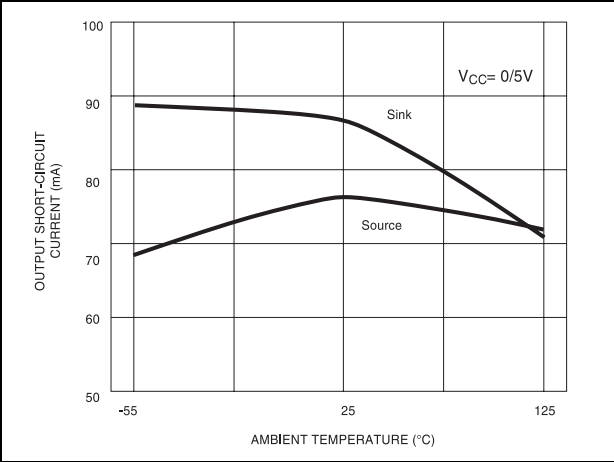


Figure 8. Voltage gain and phase vs. frequency

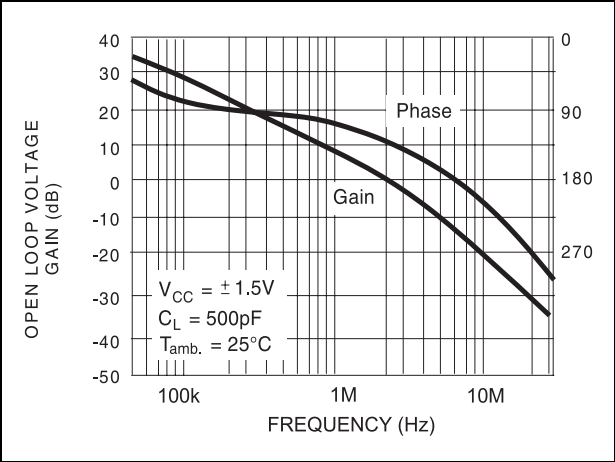


Figure 9. Distortion + noise vs. frequency

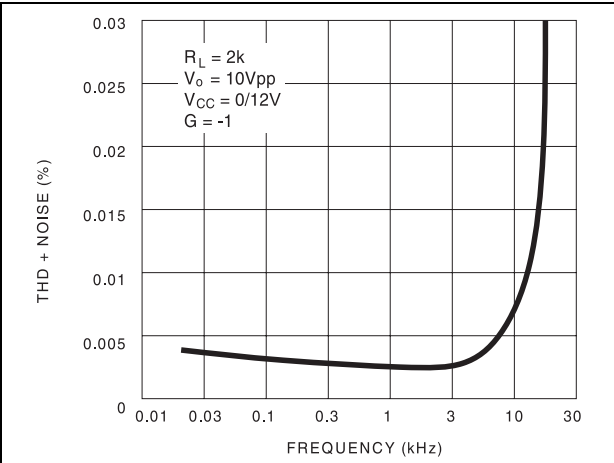


Figure 10. THD + noise vs. frequency

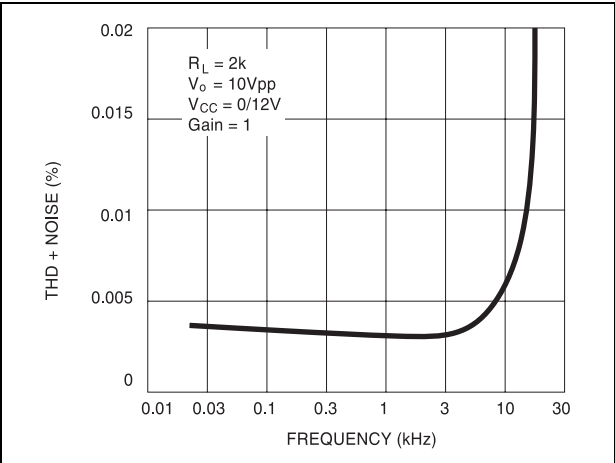


Figure 11. THD + noise vs. frequency

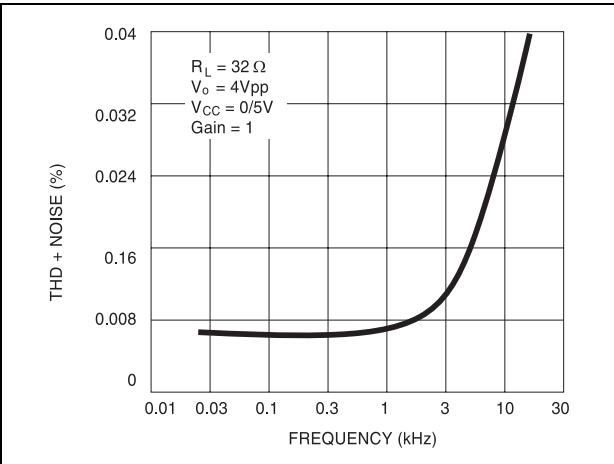


Figure 12. THD + noise vs. frequency

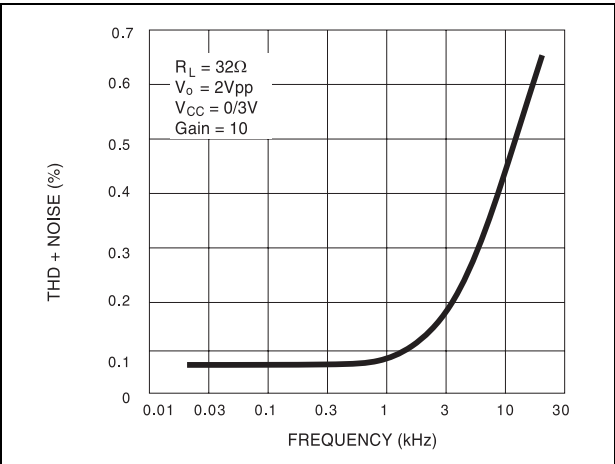


Figure 13. Equivalent input noise vs. frequency

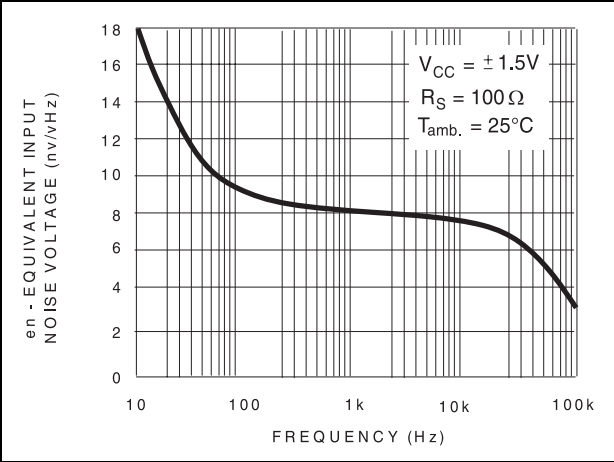


Figure 14. Total supply current vs. standby input voltage

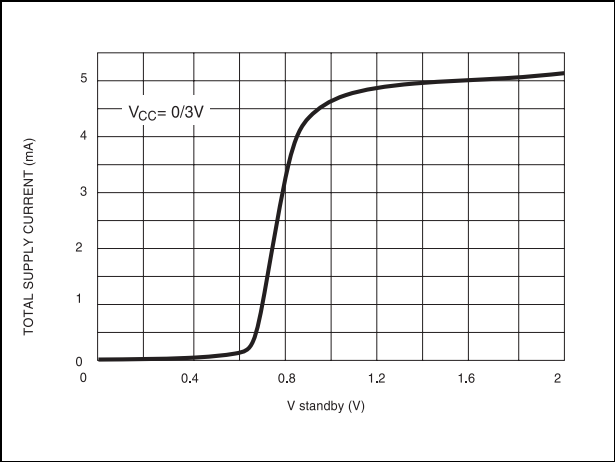
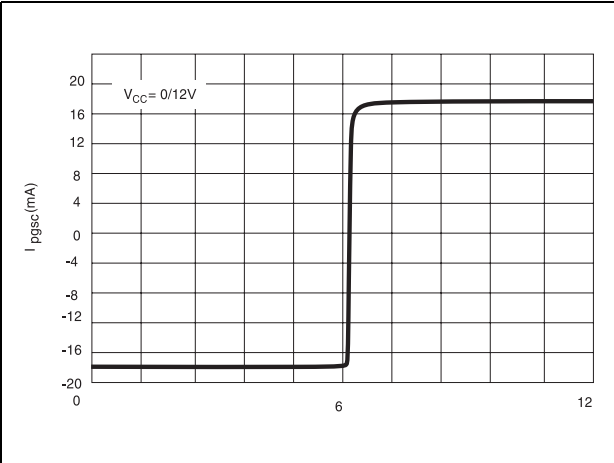


Figure 15. Phantom ground short circuit output current vs. phantom ground output voltage



### 3 Using the TS925 as a preamplifier and speaker driver

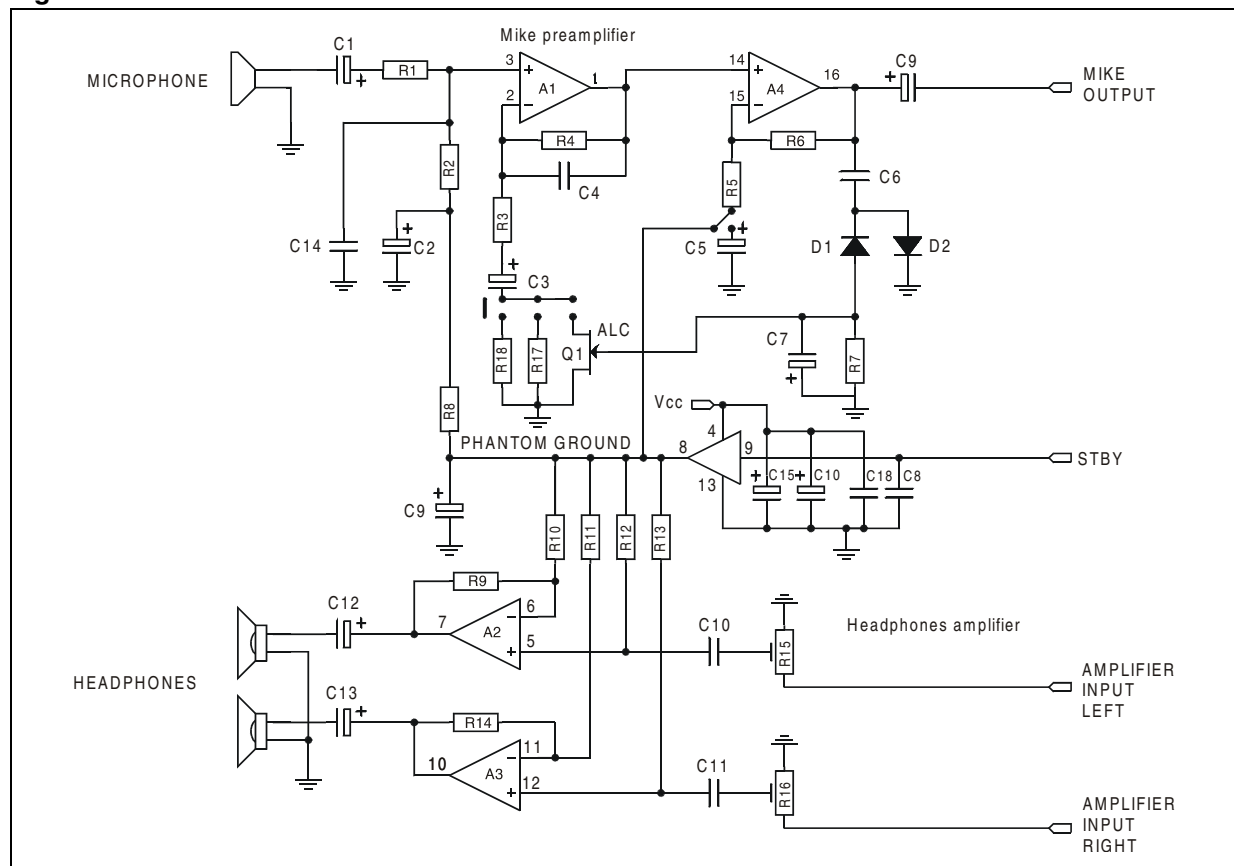
The TS925 is an input/output rail-to-rail quad BiCMOS operational amplifier. It can operate with low supply voltages (2.7 V) and drive output loads as low as  $32\ \Omega$ .

This section illustrates these features by providing an example of how the device can be used as a preamplifier and speaker driver.

The application circuit is shown in [Figure 16](#).

- Operators A1 and A4 are used in a preamplifier configuration.
- Operators A2 and A3 are used in a push-pull configuration driving a headset.
- The phantom ground is used as a common reference level ( $V_{CC}/2$ ).
- The power supply is delivered by two LR6 batteries (2 x 1.5 V nominal).

**Figure 16. Electrical schematic**



### 3.1 Preamplifier configuration

The operators A1 and A4 are wired with a non-inverting gain of respectively:

- A1#  $(R4/(R3+R17))$
- A4#  $R6/R5$

With the following values:

- $R4 = 22\text{ k}\Omega$  -  $R3 = 50\text{ }\Omega$  -  $R17 = 1.2\text{ k}\Omega$
- $R6 = 47\text{ k}\Omega$  -  $R5 = 1.2\text{ k}\Omega$

The gain of the preamplifier chain is therefore equal to 58 dB.

Alternatively, the gain of A1 can be adjusted by choosing a JFET transistor Q1 instead of R17. This JFET voltage controlled resistor arrangement forms an automatic level control (ALC) circuit, useful in many microphone preamplifier applications. The mean rectified peak level of the output signal envelope is used to control the preamplifier gain.

### 3.2 Headphone amplifier

The operators A2 and A3 are organized in a push-pull configuration with a gain of 5. The stereo inputs can be connected to a CD player and the TS925 can directly drive the headphone speakers. This configuration shows the ability of the circuit to drive a  $32\text{ }\Omega$  load with a maximum output swing and high fidelity suitable for sound and music.

*Figure 19* shows the available signal swing at the headset outputs: two other rail-to-rail competitor parts are employed in the same circuit for comparison (note the much-reduced clipping level and crossover distortion).

Figure 17. Frequency response of the global preamplifier chain

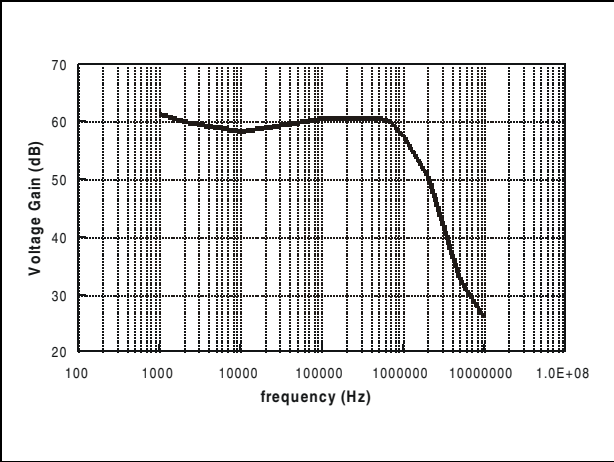


Figure 18. Voltage noise density vs. frequency at preamplifier output

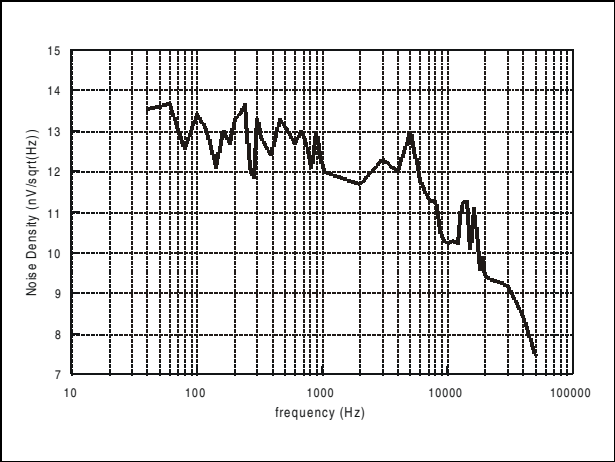


Figure 19. Maximum voltage swing at headphone outputs ( $R_L = 32 \Omega$ )

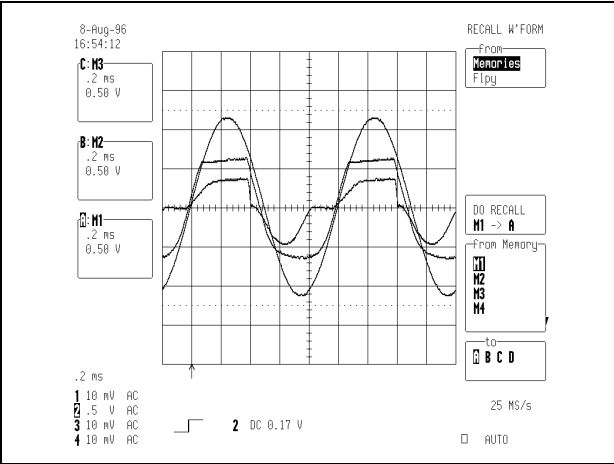
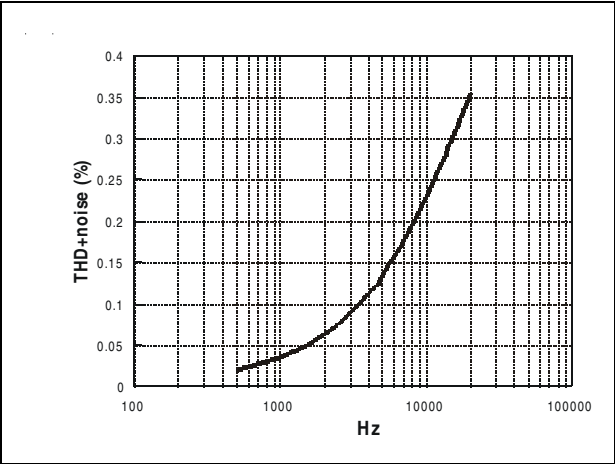


Figure 20. THD + noise vs. frequency (headphone outputs)



## 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK<sup>®</sup> is an ST trademark.

## 4.1 SO-16 package information

Figure 21. SO-16 package mechanical drawing

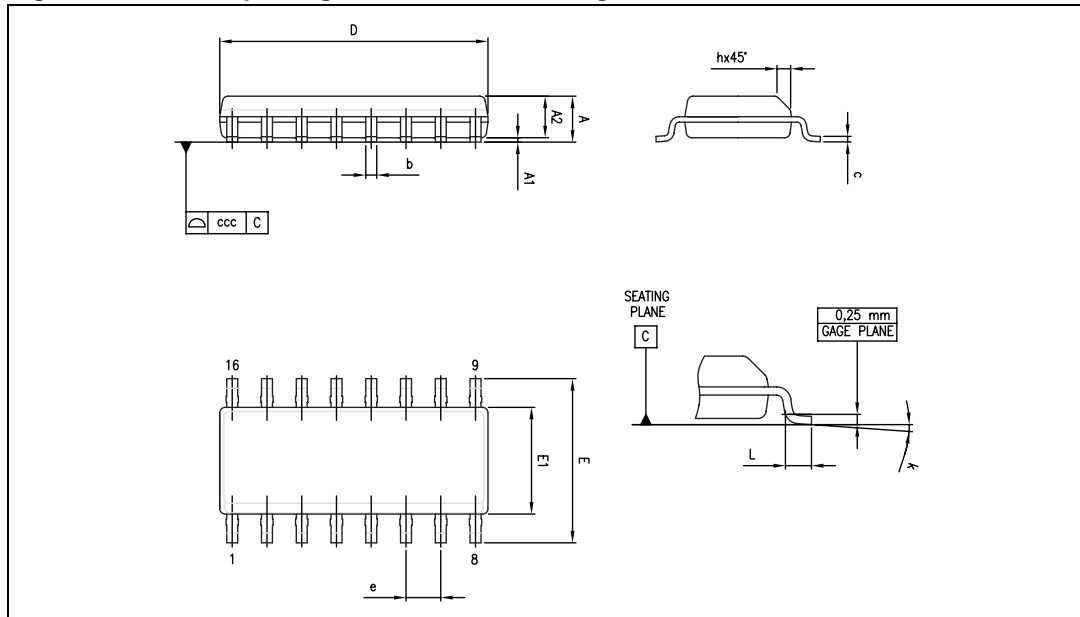


Table 5. SO-16 package mechanical data

| Dimensions        |             |      |       |        |       |       |
|-------------------|-------------|------|-------|--------|-------|-------|
| Ref.              | Millimeters |      |       | Inches |       |       |
|                   | Min.        | Typ. | Max.  | Min.   | Typ.  | Max.  |
| A                 |             |      | 1.75  |        |       | 0.069 |
| A1                | 0.10        |      | 0.25  | 0.004  |       | 0.010 |
| A2                | 1.25        |      |       | 0.049  |       |       |
| b                 | 0.31        |      | 0.51  | 0.012  |       | 0.020 |
| c                 | 0.17        |      | 0.25  | 0.007  |       | 0.010 |
| D <sup>(1)</sup>  | 9.80        | 9.90 | 10.00 | 0.386  | 0.390 | 0.394 |
| E                 | 5.80        | 6.00 | 6.20  | 0.228  | 0.236 | 0.244 |
| E1 <sup>(2)</sup> | 3.80        | 3.90 | 4.00  | 0.150  | 0.154 | 0.157 |
| e                 |             | 1.27 |       |        | 0.050 |       |
| h                 | 0.25        |      | 0.50  | 0.010  |       | 0.020 |
| L                 | 0.40        |      | 1.27  | 0.016  |       | 0.050 |
| k                 | 0           |      | 8     |        |       |       |
| ccc               |             |      | 0.10  |        |       | 0.004 |

1. Does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs not to exceed 0.15 mm in total.
2. Does not include interlead flash or protrusions. Interlead flash or protrusions not to exceed 0.25 mm per side.

## 4.2 TSSOP16 package information

Figure 22. TSSOP16 package mechanical drawing

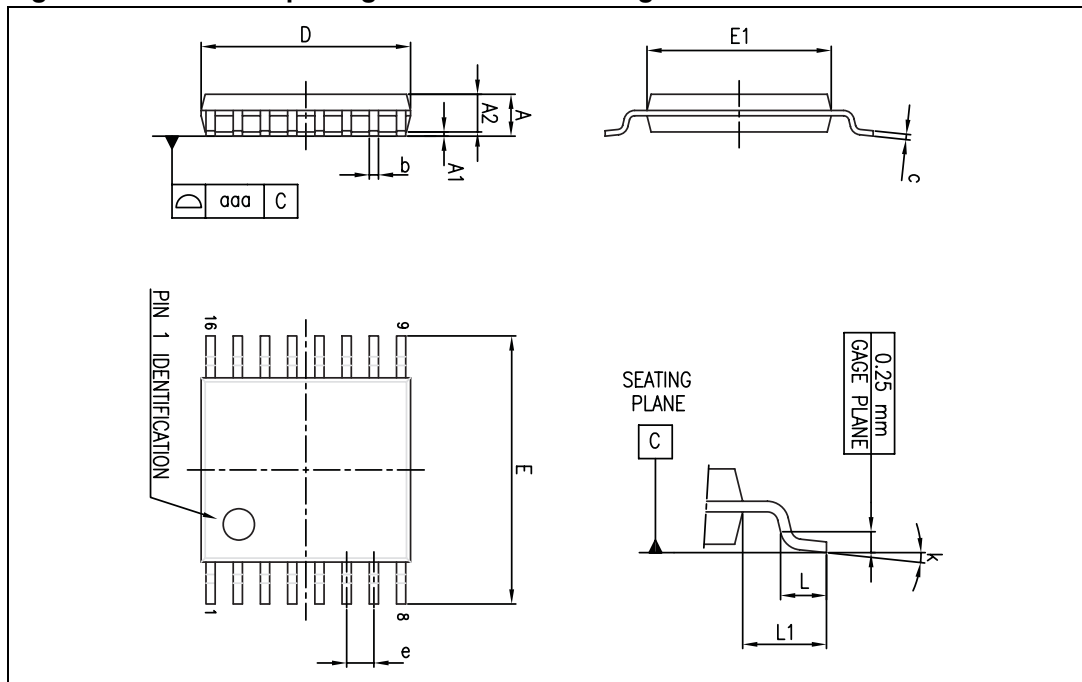


Table 6. TSSOP16 package mechanical data

| Ref. | Dimensions  |      |      |        |        |       |
|------|-------------|------|------|--------|--------|-------|
|      | Millimeters |      |      | Inches |        |       |
|      | Min.        | Typ. | Max. | Min.   | Typ.   | Max.  |
| A    |             |      | 1.20 |        |        | 0.047 |
| A1   | 0.05        |      | 0.15 | 0.002  |        | 0.006 |
| A2   | 0.80        | 1.00 | 1.05 | 0.031  | 0.039  | 0.041 |
| b    | 0.19        |      | 0.30 | 0.007  |        | 0.012 |
| c    | 0.09        |      | 0.20 | 0.004  |        | 0.008 |
| D    | 4.90        | 5.00 | 5.10 | 0.193  | 0.197  | 0.201 |
| E    | 6.20        | 6.40 | 6.60 | 0.244  | 0.252  | 0.260 |
| E1   | 4.30        | 4.40 | 4.50 | 0.169  | 0.173  | 0.177 |
| e    |             | 0.65 |      |        | 0.0256 |       |
| k    | 0°          |      | 8°   | 0°     |        | 8°    |
| L    | 0.45        | 0.60 | 0.75 | 0.018  | 0.024  | 0.030 |
| L1   |             | 1.00 |      |        | 0.039  |       |
| aaa  |             |      | 0.10 |        |        | 0.004 |

## 5 Ordering information

| Order code    | Temperature range | Package | Packing              | Marking |
|---------------|-------------------|---------|----------------------|---------|
| TS925ID/IDT   | -40°C to +125°C   | SO-16   | Tube and tape & reel | 925I    |
| TS925IPT      |                   | TSSOP16 | Tape & reel          |         |
| TS925AID/AIDT |                   | SO-16   | Tube and tape & reel | 925AI   |
| TS925AIPT     |                   | TSSOP16 | Tape & reel          |         |

## 6 Revision history

**Table 7. Document revision history**

| Date        | Revision | Changes                                                                                                                                                                                                                                                                                                                                                                                          |
|-------------|----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 01-Feb-2001 | 1        | Initial release. Product in full production.                                                                                                                                                                                                                                                                                                                                                     |
| 01-Nov-2005 | 2        | The following changes were made in this revision: <ul style="list-style-type: none"><li>– Chapter on Macromodels removed from the datasheet.</li><li>– Data updated in <a href="#">Table 3. on page 3</a>.</li><li>– Data in tables in <a href="#">Electrical characteristics on page 3</a> reformatted for easier use.</li><li>– Minor grammatical and formatting changes throughout.</li></ul> |
| 10-Mar-2009 | 3        | Document reformatted.<br>Removed DIP package information in <a href="#">Chapter 4</a> and associated order codes in <a href="#">Chapter 5</a> .<br>Updated SO-16 and TSSOP16 package drawings and dimensions in <a href="#">Chapter 4</a> .                                                                                                                                                      |
| 28-Apr-2011 | 4        | Modified CMR conditions in <a href="#">Table 3</a> and <a href="#">Table 4</a> .                                                                                                                                                                                                                                                                                                                 |

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