

## **Wireless Power Transmitter for 15W Solution**

### **General Description**

The RT3181C is a highly integrated and scalable solution for wireless power transmitter applications. The RT3181C is designed to support the major wireless power standards - WPC (Qi). The RT3181C can be configured to various types of transmitter according to WPC specification.

The RT3181C supports fully integrated design for WPC Base Power Profile A11 transmitter which could be powered from a 5V output of USB or AC adaptor in low power applications. The RT3181C is able to support WPC MP-A5/MP-A11 transmitter design with external power stage for medium power applications.

The RT3181C can be paired with a Smart AC adapter from selected suppliers to provide reduced system cost and optimal efficiency over the range of power usage.

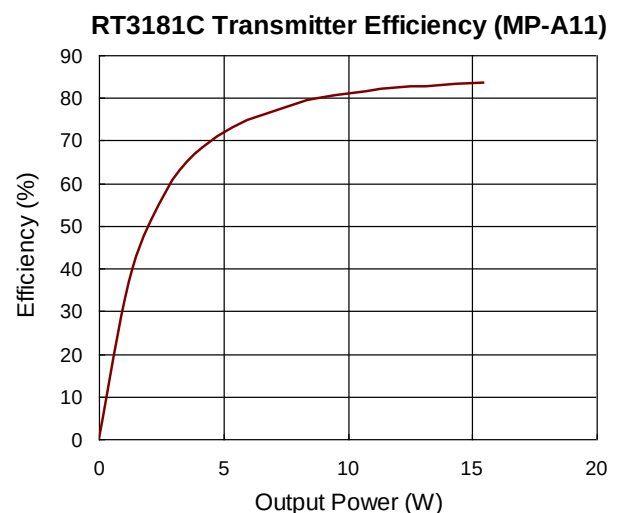
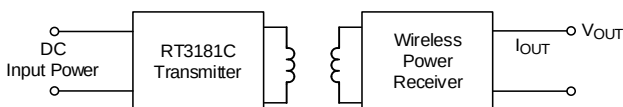
### **Applications**

- Mobile Phones
- Phablets, Tablets
- Wearable Devices
- Medical and Industrial

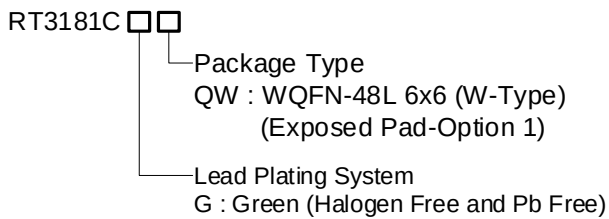
### **Features**

- **Wireless Power Transmitter Compliant**
  - ▶ WPC qi V1.3
- **High Integration with Internal H-Bridge Power Stage and Current Sense Amplifier, Optimized for LP-A11 Solution**
- **High Power with External Power Stage, Optimized for MP-A5/MP-A11 Solution**
- **No Current Transformer Required**
- **Low Quiescent Current Plus**
  - ▶ Selectable Ping Cycle in Custom Applications
  - ▶ Low Current Sleep Mode Options including "Off" State with Wake Up I/O Pin
- **Programmable Thermal Protection**
- **Support Foreign Object Detection**
- **Support Fan Speed Control**
- **Notifications**
  - ▶ 1 Buzzer
  - ▶ 2 LEDs, Resistor Programmable
- **I<sup>2</sup>C Host Applications Interface (Option)**
- **Internal Watchdog Timer**
- **WQFN-48L 6x6mm Package**

### **Simplified Application Circuit**



## Ordering Information

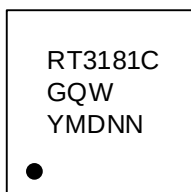


Note :

Richtek products are :

- ▶ RoHS compliant and compatible with the current requirements of IPC/JEDEC J-STD-020.
- ▶ Suitable for use in SnPb or Pb-free soldering processes.

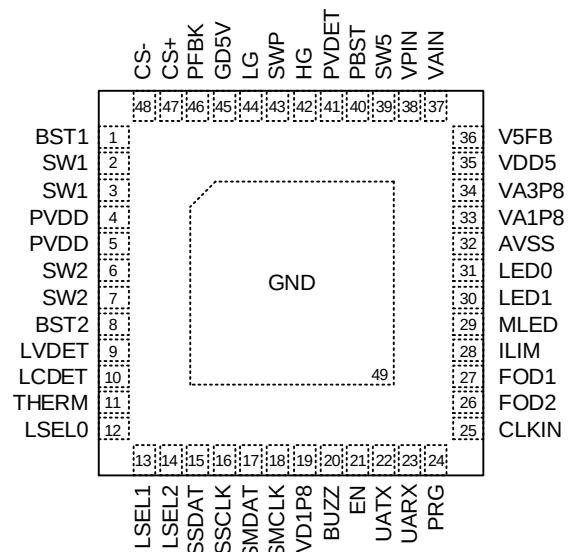
## Marking Information



RT3181CGQW : Product Code  
YMDNN : Date Code

## Pin Configuration

(TOP VIEW)

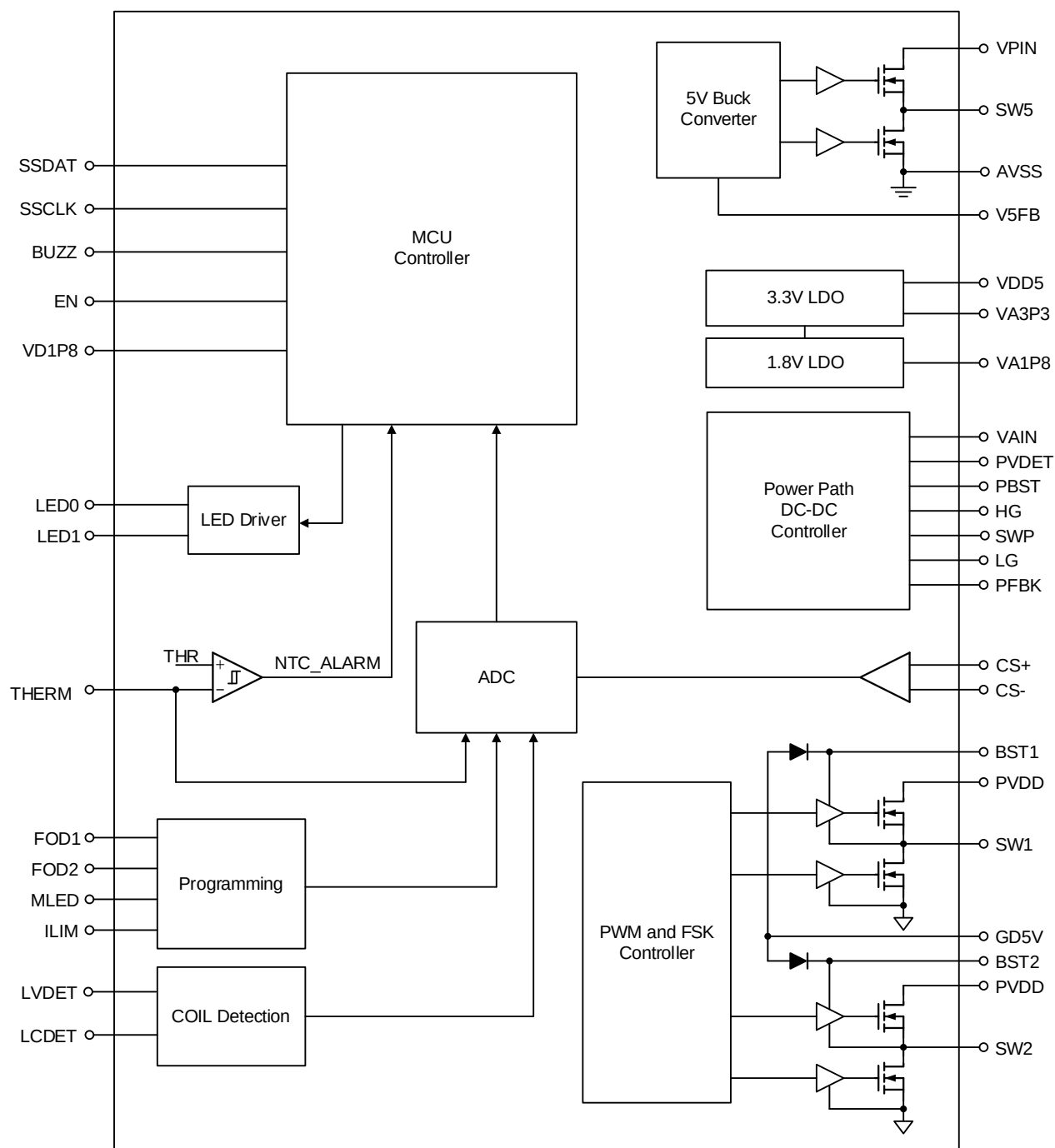


## Functional Pin Description

| Pin No. | Pin Name | Pin Function                                                  |
|---------|----------|---------------------------------------------------------------|
| 1       | BST1     | Bootstrap supply input of H-Bridge power inverter.            |
| 2, 3    | SW1      | First Switch Node of H-Bridge power inverter.                 |
| 4, 5    | PVDD     | Power supply input of H-Bridge power inverter.                |
| 6, 7    | SW2      | Second Switch Node of H-Bridge power inverter.                |
| 8       | BST2     | Bootstrap supply input of H-Bridge power inverter.            |
| 9       | LVDET    | Coil voltage detection input.                                 |
| 10      | LCDET    | Coil current sense input.                                     |
| 11      | THERM    | Temperature detection input.                                  |
| 12      | LSEL0    | Coil select signal 0.                                         |
| 13      | LSEL1    | Coil select signal 1.                                         |
| 14      | LSEL2    | Coil select signal 2.                                         |
| 15      | SSDAT    | Slave I <sup>2</sup> C data.                                  |
| 16      | SSCLK    | Slave I <sup>2</sup> C clock.                                 |
| 17      | SMDAT    | Master I <sup>2</sup> C data.                                 |
| 18      | SMCLK    | Master I <sup>2</sup> C clock.                                |
| 19      | VD1P8    | Digital core/IO power supply input.                           |
| 20      | BUZZ     | Output for buzzer.                                            |
| 21      | EN       | Enable control input.                                         |
| 22      | UATX     | UART TX.                                                      |
| 23      | UARX     | UART RX.                                                      |
| 24      | PRG      | Reserved pin. Do not connect this pin to any power or ground. |
| 25      | CLKIN    | Crystal oscillator input.                                     |
| 26      | FOD2     | Input for FOD threshold setting.                              |
| 27      | FOD1     | Input for FOD threshold setting.                              |
| 28      | ILIM     | Input for reserved mode setting.                              |
| 29      | MLED     | Input for LED mode selection.                                 |
| 30      | LED1     | Open drain LED driver.                                        |
| 31      | LED0     | Open drain LED driver.                                        |
| 32      | AVSS     | Reference ground.                                             |
| 33      | VA1P8    | 1.8V LDO output.                                              |
| 34      | VA3P3    | 3.3V LDO output.                                              |
| 35      | VDD5     | Power supply input for analog blocks.                         |
| 36      | V5FB     | Feedback input of 5V Buck converter.                          |
| 37      | VAIN     | Main power input for analog blocks.                           |
| 38      | VPIN     | Power input of 5V buck converter.                             |

| Pin No.             | Pin Name | Pin Function                                                                                                |
|---------------------|----------|-------------------------------------------------------------------------------------------------------------|
| 39                  | SW5      | Switch node of 5V buck converter.                                                                           |
| 40                  | PBST     | Bootstrap supply input for DC-DC controller.                                                                |
| 41                  | PVDET    | HS sense input for DC-DC controller.                                                                        |
| 42                  | HG       | HS gate-driver output for DC-DC controller.                                                                 |
| 43                  | SWP      | Switch node for DC-DC controller.                                                                           |
| 44                  | LG       | LS Gate-driver output for DC-DC controller.                                                                 |
| 45                  | GD5V     | Gate driver supply input for H-Bridge driver.                                                               |
| 46                  | PFBK     | Feedback Input for DC-DC controller.                                                                        |
| 47                  | CS+      | Current sense positive input.                                                                               |
| 48                  | CS-      | Current sense negative input.                                                                               |
| 49<br>(Exposed Pad) | GND      | Ground. The exposed pad must be soldered to a large PCB and connected to GND for maximum power dissipation. |

# Functional Block Diagram



## Operation

### Internal Power Management

The internal power management system provides a 5V supply from Buck converter, a 3.3V supply from LDO and a 1.8V digital supply from LDO.

### Power Path DC-DC Controller

The DC-DC power path converts the input voltage to a DC supply voltage for the power inverter to drive power transmitting coil. The DC-DC power path could be configured as a Buck converter or a Boost converter. It includes a current limit to prevent overloading of the wireless power receive chain.

### H-Bridge Power Inverter

The H-Bridge power inverter converts DC input power to AC output power for driving coil. The power inverter is able to operate in a wide switching frequency range from 100kHz to 500kHz for various applications.

### Microcontroller

The RT3181C device integrates a 32bit-microprocessor which allows designer to configure the transmitter to meet application requirements. The microprocessor monitors the input signals from ADC and control the output power via power path DC-DC controller and PWM controller.

### Current Sense Amplifier

The current sense amplifier detects the voltage between CS+ pin and CS- pin which connected to an external current sense resistor. In general, the current sense resistor is connected to the input of power inverter.

### OTP (Over-Temperature Protection)

The OTP function shuts down the power stage when the junction temperature exceeds a certain threshold. Once the junction temperature cools down, the receiver will automatically resume operating.

## Absolute Maximum Ratings (Note 1)

- Input Voltage, VAIN, VPIN, SW5, PVDET, SWP, LG, PFBK, CS+, CS- ----- -0.3V to 20V
- Input Voltage, HG----- -0.3V to 25V
- Input Voltage, PVDD, SW1, SW2----- -0.3V to 13.2V
- Input Voltage, LVDET, LCDDET, THERM, CLKIN, FOD1, FOD2,  
ILIM, MLED, LED0, LED1, VDD5, V5FB, GD5V ----- -0.3V to 5.5V
- Input Voltage, PBST-SWP, BST1-SW1, BST2-SW2 ----- -0.3V to 5.5V
- Input Voltage, LSEL0, LSEL1, LSEL2, SSDAT, SSCLK,  
SMDAT, SMCLK, VD1P8, BUZZ, EN, UATX, UARX ----- -0.3V to 1.98V
- Input Current, PVDD ----- 2A
- Output Current, SW1, SW2 ----- 2A
- Power Dissipation, PD @ TA = 25°C  
WQFN-48L 6x6----- 3.73W
- Package Thermal Resistance (Note 2)  
WQFN-48L 6x6,  $\theta_{JA}$  ----- 26.8°C/W  
WQFN-48L 6x6,  $\theta_{JC}$  ----- 1.3°C/W
- Junction Temperature ----- 150°C
- Lead Temperature (Soldering, 10sec.)----- 260°C
- Storage Temperature Range ----- -65°C to 150°C
- ESD Susceptibility (Note 3)  
HBM (Human Body Model) ----- 2kV

## Recommended Operating Conditions (Note 4)

- Supply Input Voltage Range, VAIN ----- 5V to 19V
- Input Voltage, PVDD----- 5V to 12V
- Junction Temperature Range----- -40°C to 125°C
- Ambient Temperature Range----- -40°C to 85°C

## Electrical Characteristics

(VAIN = VPIN = VDD5 = PVDD = GD5V = 5V, VD1P8 = 1.8V, TA = 25°C, unless otherwise specified)

| Parameter                            | Symbol    | Test Conditions                       | Min | Typ  | Max | Unit |
|--------------------------------------|-----------|---------------------------------------|-----|------|-----|------|
| VAIN Under-Voltage Lockout Threshold | VUVLO     | VAIN rising : $0 \geq 5V$             | 3   | 3.15 | 3.3 | V    |
| VAIN UVLO Hysteresis                 | VUVLO_HYS | VAIN falling : $5V \geq 0V$           | --  | 300  | --  | mV   |
| Quiescent Current                    | Iq        | No coil, 200kHz switching at SW1, SW2 | --  | 2    | --  | mA   |
| Standby Current (Pinging)            | ISTB      |                                       | --  | 15   | --  | mA   |
| Sleep Mode Current (No Pinging)      | ISLEEP    |                                       | --  | 0.35 | --  | mA   |

| Parameter                                       | Symbol               | Test Conditions                                  | Min  | Typ  | Max  | Unit |
|-------------------------------------------------|----------------------|--------------------------------------------------|------|------|------|------|
| <b>Internal Regulator</b>                       |                      |                                                  |      |      |      |      |
| VDD5 Input Voltage                              | VDD5                 |                                                  | 4.5  | 5    | 5.5  | V    |
| 5V Buck Converter Feedback Reference            | V <sub>5FB</sub>     |                                                  | --   | 5    | --   | V    |
| VA3P3 Output Voltage                            | VA3P3                | LOAD = 20mA                                      | 2.97 | 3.3  | 3.63 | V    |
| 3.3V Output Current                             | I <sub>33V</sub>     |                                                  | 0    | --   | 50   | mA   |
| VA1P8 Output Voltage                            | VA1P8                | LOAD = 40mA                                      | 1.62 | 1.8  | 1.96 | V    |
| 1.8V Output Current                             | I <sub>18V</sub>     |                                                  | 0    | --   | 100  | mA   |
| <b>Power Path DC-DC Controller (Buck Mode)</b>  |                      |                                                  |      |      |      |      |
| Quiescent Supply Current                        | I <sub>Q_BUCK</sub>  | Not connect to external pass device (Note 5)     | --   | --   | 2    | mA   |
| Shut Down Supply Current                        | I <sub>SD_BUCK</sub> | (Note 5)                                         | --   | 1    | 10   | μA   |
| Feedback Reference Voltage                      | V <sub>FB1</sub>     | V <sub>AIN</sub> = 5V to 19V (Note 5)            | 0.3  | 1.2  | 1.9  | V    |
| Feedback Voltage DC Accuracy                    | V <sub>FB1</sub>     | (Note 5)                                         | --   | 5    | --   | %    |
| Buck Switching Frequency                        | f <sub>BUCK</sub>    | F/W programmable, default 500kHz (Note 5)        | 400  | 500  | 1200 | kHz  |
| Buck Maximum Duty Cycle                         | D <sub>BUCK</sub>    | (Note 5)                                         | --   | 95   | --   | %    |
| Driver Pull-Up On Resistance                    | R <sub>DRV</sub>     | (Note 5)                                         | --   | 1    | --   | Ω    |
| <b>Power Path DC-DC Controller (Boost Mode)</b> |                      |                                                  |      |      |      |      |
| Feedback Reference Voltage                      | V <sub>FB2</sub>     | V <sub>AIN</sub> = 5V to 19V (Note 5)            | 0.3  | 1.2  | 1.9  | V    |
| Feedback Voltage DC Accuracy                    | V <sub>FB2</sub>     | (Note 5)                                         | --   | 5    | --   | %    |
| Boost Switching Frequency                       | f <sub>BOOST</sub>   | F/W programmable, default 500kHz (Note 5)        | 400  | 500  | 1200 | kHz  |
| Boost Maximum Duty Cycle                        | D <sub>BOOST</sub>   | (Note 5)                                         | --   | 95   | --   | %    |
| <b>Power Path DC-DC Controller (PWM Mode)</b>   |                      |                                                  |      |      |      |      |
| PWM Frequency Adjustment Range                  | f <sub>PWM</sub>     | Duty = 50% (Note 5)                              | 110  | --   | 300  | kHz  |
| PWM Duty Cycle Adjustment Range                 | D <sub>PWM</sub>     | (Note 5)                                         | 1    | --   | 50   | %    |
| <b>H-Bridge Power Inverter</b>                  |                      |                                                  |      |      |      |      |
| FET On Resistance                               | R <sub>ON</sub>      | Internal power inverter mode                     | 0.14 | 0.16 | 0.18 | Ω    |
|                                                 |                      | Gate drive mode for driving external power stage | --   | 1.2  | --   |      |
| Over-Current Protection Adjustment Range        | I <sub>OC</sub>      | Resolution = 200mA (Note 5)                      | 0.2  | --   | 6    | A    |
| Over-Current Protection Accuracy                | I <sub>OC</sub>      | (Note 5)                                         | -40  | --   | 40   | %    |
| PWM Frequency Adjustment Range                  | f <sub>PWM</sub>     | Duty = 50% (Note 5)                              | 110  | --   | 300  | kHz  |
| PWM Frequency Adjustment Resolution             | f <sub>PWM</sub>     | (Note 5)                                         | --   | 0.74 | --   | kHz  |



| Parameter                                                                                                      | Symbol              | Test Conditions                                             | Min  | Typ | Max | Unit |
|----------------------------------------------------------------------------------------------------------------|---------------------|-------------------------------------------------------------|------|-----|-----|------|
| PWM Duty Cycle Adjustment Range                                                                                | DPWM                | (Note 5)                                                    | 1    | --  | 50  | %    |
| PWM Phase Shift Adjustment Range                                                                               | PHPWM               | (Note 5)                                                    | 0    | --  | 180 | DEG  |
| <b>COIL V/I Detection</b>                                                                                      |                     |                                                             |      |     |     |      |
| LVDET Input Voltage Range                                                                                      | LVDET               |                                                             | 0    | --  | 2   | V    |
| LCDET Input Voltage Range                                                                                      | LCDET               | Buffer gain = 1                                             | -0.9 | --  | 0.9 | V    |
|                                                                                                                |                     | Buffer gain = 4                                             | -0.3 | --  | 0.3 |      |
| Operating Frequency Range                                                                                      | f                   |                                                             | 100  | --  | 300 | kHz  |
| <b>Over-Temperature Protection</b>                                                                             |                     |                                                             |      |     |     |      |
| Thermal Shut Down Threshold                                                                                    | T <sub>J_SD</sub>   | Shut down power path when die temp reach threshold (Note 5) | --   | 125 | --  | °C   |
| Hysteresis                                                                                                     | T <sub>J_HYS</sub>  | F/W programmable (Note 5)                                   | --   | 10  | --  | °C   |
| <b>Digital Input/Output<br/>(LSEL0, LSEL1, LSEL2, SSDAT, SSCLK, SMDAT, SMCLK, VD1P8, BUZZ, EN, UATX, UARX)</b> |                     |                                                             |      |     |     |      |
| Input Voltage                                                                                                  | Low-Level           | V <sub>IL</sub>                                             | 0    | --  | 0.5 | V    |
|                                                                                                                | High-Level          | V <sub>IH</sub>                                             | 1.4  | --  | 1.8 |      |
| Output Voltage                                                                                                 | Low-Level           | V <sub>OL</sub>                                             | 0    | --  | 0.2 | V    |
|                                                                                                                | High-Level          | V <sub>OH</sub>                                             | 1.4  | --  | 1.9 |      |
| Output High Source Current                                                                                     | I <sub>OH_MAX</sub> | V <sub>OH</sub> = VD1P8 - 0.2V                              | --   | --  | 200 | μA   |
| Output Low Sink Current                                                                                        | I <sub>OL_MAX</sub> | V <sub>OL</sub> = 0.2V                                      | --   | --  | 200 | μA   |

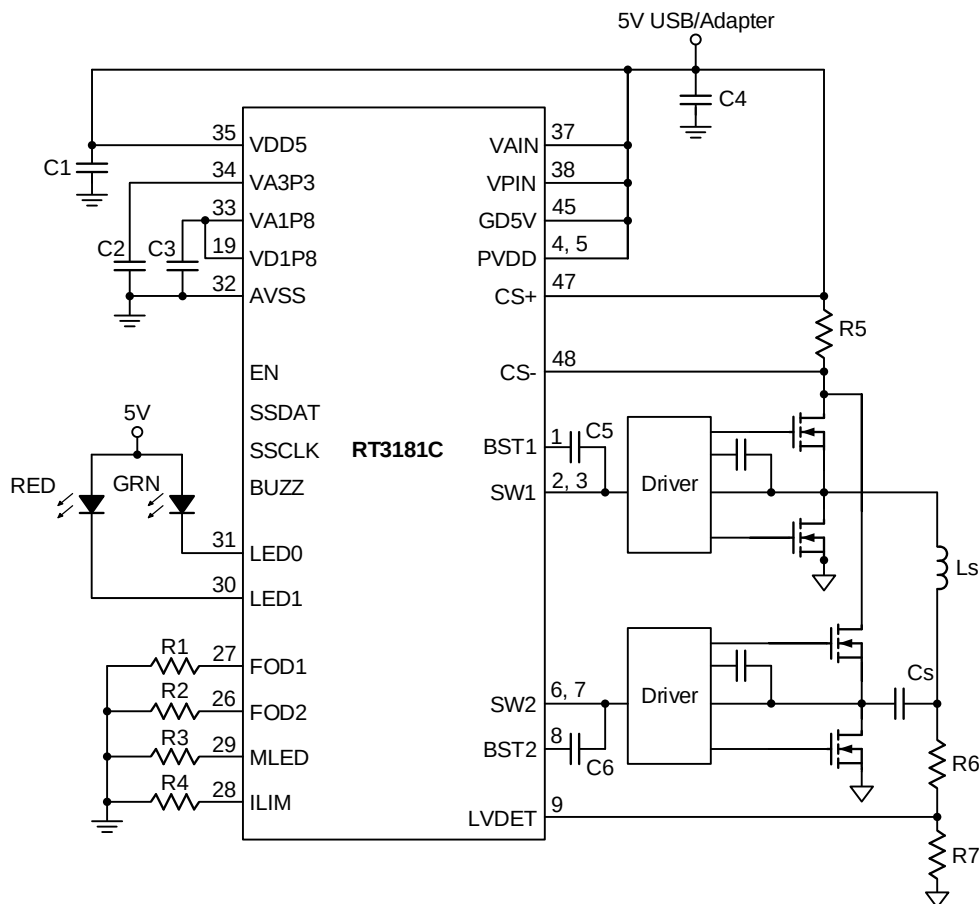
**Note 1.** Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions may affect device reliability.

**Note 2.**  $\theta_{JA}$  is measured under natural convection (still air) at T<sub>A</sub> = 25°C with the component mounted on a high effective-thermal-conductivity four-layer test board on a JEDEC 51-7 thermal measurement standard.  $\theta_{JC}$  is measured at the exposed pad of the package.

**Note 3.** Devices are ESD sensitive. Handling precautions are recommended.

**Note 4.** The device is not guaranteed to function outside its operating conditions.

**Note 5.** Specification is guaranteed by design and/or correlation with statistical process control.



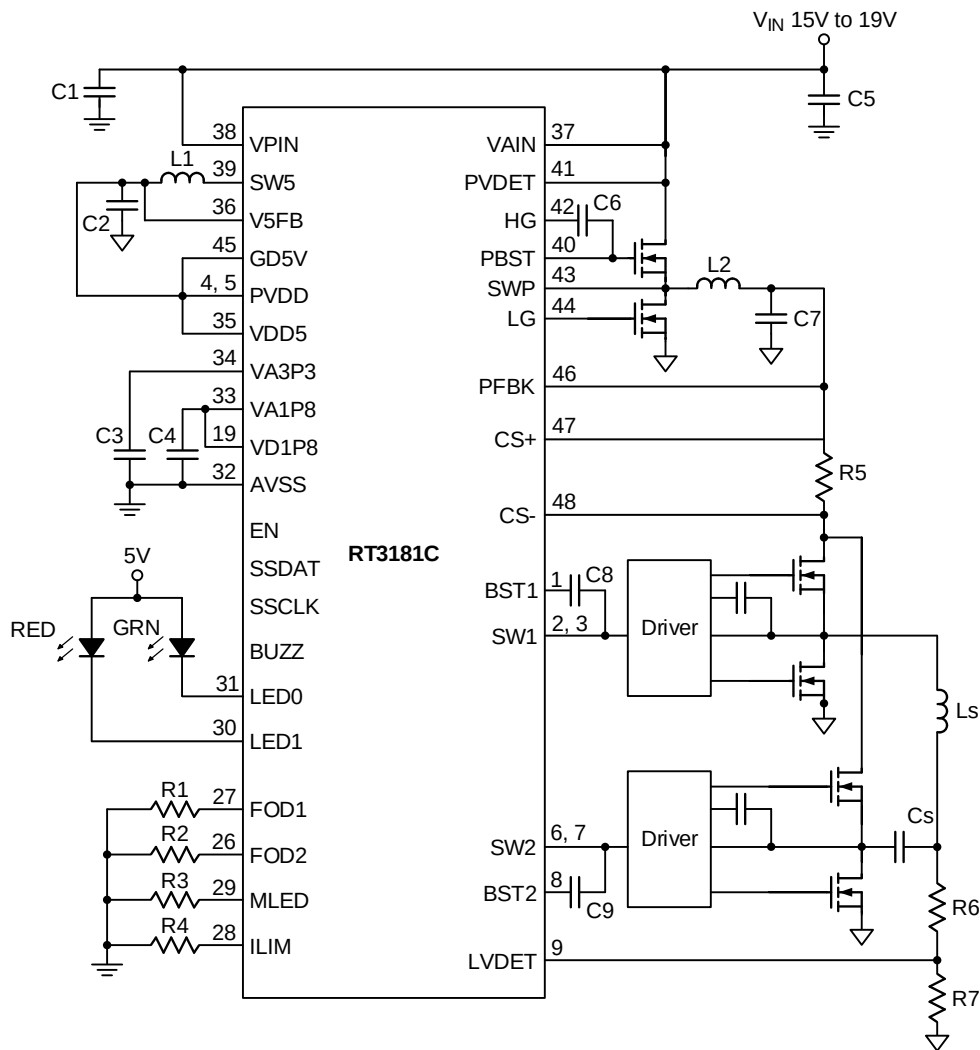


Figure 2. WPC MP-A5/MP-A11 Transmitter Application Circuit

## Application Information

**Richtek's component specification does not include the following information in the Application Information section. Thereby no warranty is given regarding its validity and accuracy. Customers should take responsibility to verify their own designs and to ensure the functional suitability of their components and systems.**

The RT3181C supports fully integrated design at low power 5W transmitter which is able to be powered from a USB or a 5V adapter power supply. Figure 1 shows a typical application circuit of WPC LP A11 design transmitter with minimized BOM. The RT3181C provides an internal H-Bridge Power Inverter to drive the power transmitting coil.

Figure 2 shows another application circuit of WPC LP A11 design transmitter for high performance applications. An external H-Bridge Power Inverter is adopted to drive the power transmitting coil.

The RT3181C is able to support WPC medium power 15W solution. A typical application circuit of WPC MP-A5 design transmitter for high performance applications shown as Figure 3. The RT3181C provides an internal DC-DC Buck controller to converter input voltage to the required supply voltage for the H-Bridge Power Inverter to drive its power transmitting coil.

### Current Sense

The RT3181C provides an internal amplifier for sensing the input current of power stage. Select a suitable current sense resistor and connect CS+ and CS- pins between the resistor.

### Indicator

The RT3181C provides two open-drain output pins, LED0 and LED1, to indicate the status of the wireless power transmitter. The LED0/LED1 is programmable depending system design requirements.

The RT3181C also provides a BUZZ pin for sound notification. The BUZZ is programmable based on system design requirements.

### I<sup>2</sup>C Interface

The RT3181C provides I<sup>2</sup>C interface for data communication. The I<sup>2</sup>C interface can be used to allow host control of RT3181C. The host can control and monitor various operating conditions of RT3181C. The I<sup>2</sup>C supports a programmable 7 bits device address up to 400kb/s.

### External Thermal Management

The RT3181C provides an external device thermal management function with an external NTC thermistor connected between THERM pin and GND pin shown as Figure 4. The Thermistor Interface measures the resistance of an external NTC thermistor. It provides the thermistor voltage to the ADC for monitoring by the MCU and asserts the NTC\_ALARM signal if the thermistor voltage exceeds a programmed threshold.

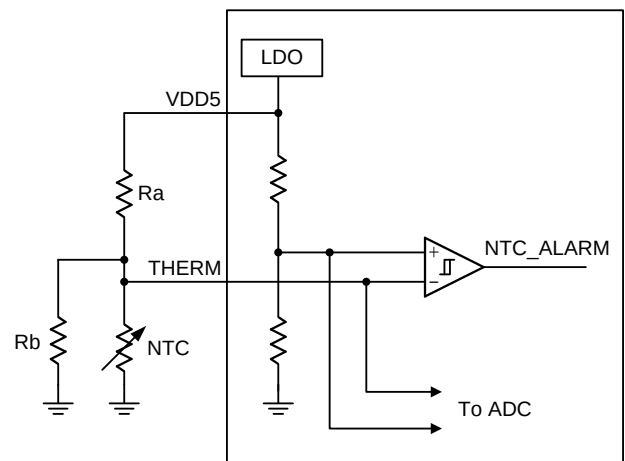


Figure 3. External Temperature Sense Circuit

## Internal Over-Temperature Protection

The RT3181C provides an over-temperature protection (OTP) feature to prevent excessive power dissipation from overheating the device. The OTP function shuts down the linear regulator operation when the junction temperature exceeds 125°C. Once the junction temperature cools down by around 10°C, the receiver will automatically resume operating.

## Thermal Considerations

The junction temperature should never exceed the absolute maximum junction temperature  $T_{J(MAX)}$ , listed under Absolute Maximum Ratings, to avoid permanent damage to the device. The maximum allowable power dissipation depends on the thermal resistance of the IC package, the PCB layout, the rate of surrounding airflow, and the difference between the junction and ambient temperatures. The maximum power dissipation can be calculated using the following formula :

$$P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$$

where  $T_{J(MAX)}$  is the maximum junction temperature,  $T_A$  is the ambient temperature, and  $\theta_{JA}$  is the junction-to-ambient thermal resistance.

For continuous operation, the maximum operating junction temperature indicated under Recommended Operating Conditions is 125°C. The junction-to-ambient thermal resistance,  $\theta_{JA}$ , is highly package dependent. For a WQFN-48L 6x6 package, the thermal resistance,  $\theta_{JA}$ , is 26.8°C/W on a standard JEDEC 51-7 high effective-thermal-conductivity four-layer test board. The maximum power dissipation at  $T_A = 25^\circ\text{C}$  can be calculated as below :

$$P_{D(MAX)} = (125^\circ\text{C} - 25^\circ\text{C}) / (26.8^\circ\text{C/W}) = 3.73\text{W for a WQFN-48L 6x6 package.}$$

The maximum power dissipation depends on the operating ambient temperature for the fixed  $T_{J(MAX)}$  and the thermal resistance,  $\theta_{JA}$ . The derating curves in Figure 4 allows the designer to see the effect of rising ambient temperature on the maximum power dissipation.

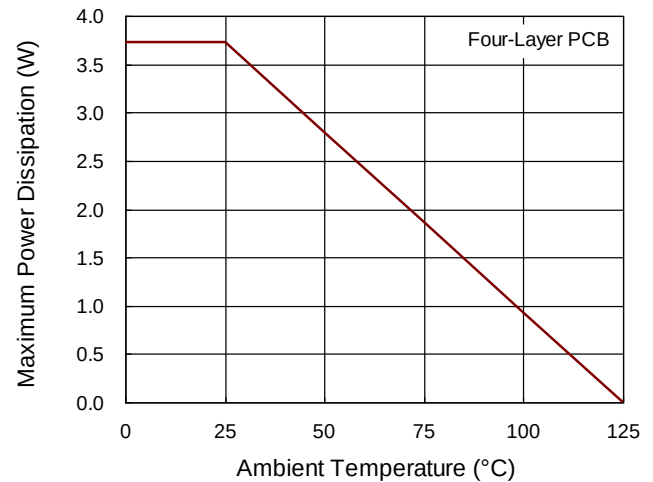


Figure 4. Derating Curve of Maximum Power Dissipation

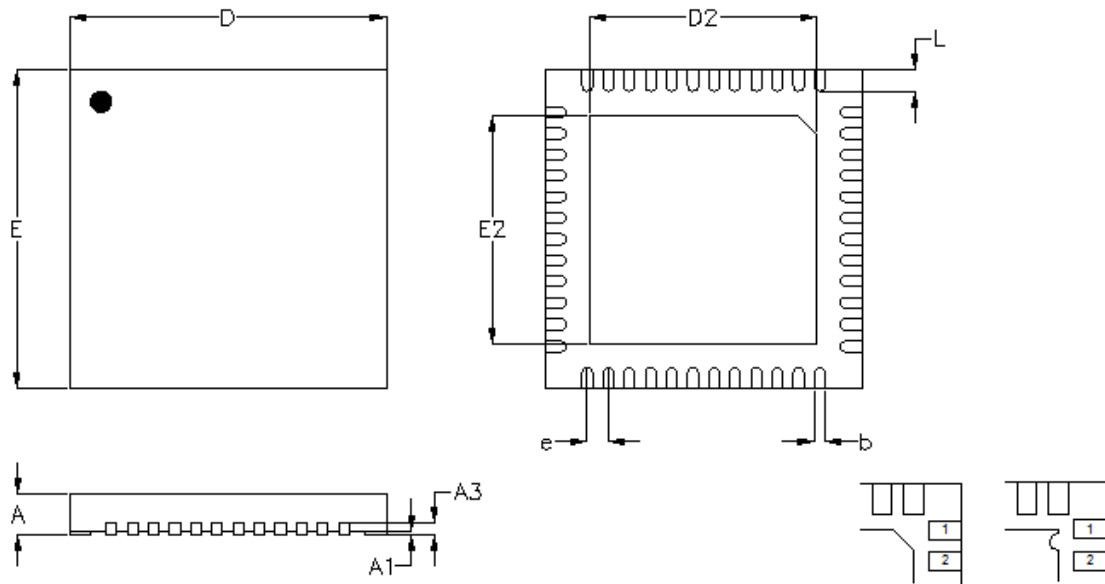
## Layout Considerations

Follow the PCB layout guidelines for optimal performance of the IC.

- ▶ Keep the traces of main current paths as short and wide as possible.
- ▶ Place the capacitors as close as possible to the IC.
- ▶ Power ground should be as large as possible and connected to a power plane for thermal dissipation.

For further design information, please contact Richtek.

## Outline Dimension



DETAIL A

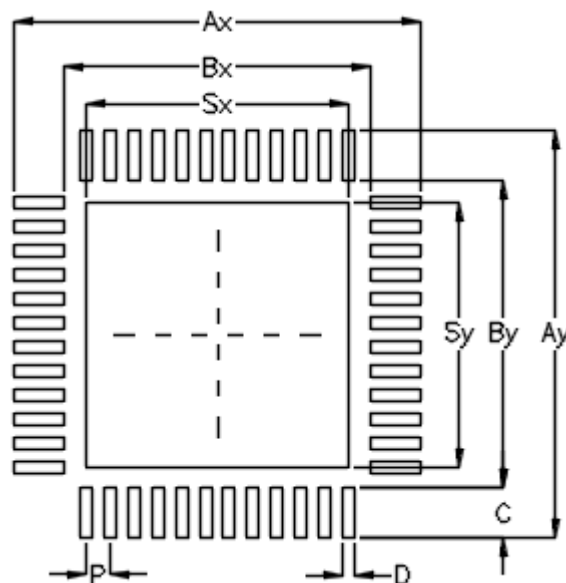
Pin #1 ID and Tie Bar Mark Options

Note : The configuration of the Pin #1 identifier is optional, but must be located within the zone indicated.

| Symbol |          | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|----------|---------------------------|-------|----------------------|-------|
|        |          | Min                       | Max   | Min                  | Max   |
| A      |          | 0.700                     | 0.800 | 0.028                | 0.031 |
| A1     |          | 0.000                     | 0.050 | 0.000                | 0.002 |
| A3     |          | 0.175                     | 0.250 | 0.007                | 0.010 |
| b      |          | 0.150                     | 0.250 | 0.006                | 0.010 |
| D      |          | 5.950                     | 6.050 | 0.234                | 0.238 |
| D2     | Option 1 | 4.250                     | 4.350 | 0.167                | 0.171 |
|        | Option 2 | 4.350                     | 4.450 | 0.171                | 0.175 |
|        | Option 3 | 4.650                     | 4.750 | 0.183                | 0.187 |
|        | Option 4 | 4.450                     | 4.550 | 0.175                | 0.179 |
| E      |          | 5.950                     | 6.050 | 0.234                | 0.238 |
| E2     | Option 1 | 4.250                     | 4.350 | 0.167                | 0.171 |
|        | Option 2 | 4.350                     | 4.450 | 0.171                | 0.175 |
|        | Option 3 | 4.650                     | 4.750 | 0.183                | 0.187 |
|        | Option 4 | 4.450                     | 4.550 | 0.175                | 0.179 |
| e      |          | 0.400                     |       | 0.016                |       |
| L      |          | 0.350                     | 0.450 | 0.014                | 0.018 |

## W-Type 48L QFN 6x6 Package

## Footprint Information



| Package          |         | Number of Pin | Footprint Dimension (mm) |      |      |      |      |      |      |      | Tolerance |       |
|------------------|---------|---------------|--------------------------|------|------|------|------|------|------|------|-----------|-------|
|                  |         |               | P                        | Ax   | Ay   | Bx   | By   | C    | D    | Sx   |           | Sy    |
| V/W/U/XQFN6x6-48 | Option1 | 48            | 0.40                     | 6.80 | 6.80 | 5.10 | 5.10 | 0.85 | 0.20 | 4.40 | 4.40      | ±0.05 |
|                  | Option2 |               |                          |      |      |      |      |      |      | 4.50 | 4.50      |       |
|                  | Option3 |               |                          |      |      |      |      |      |      | 4.70 | 4.70      |       |
|                  | Option4 |               |                          |      |      |      |      |      |      | 4.60 | 4.60      |       |

## Richtek Technology Corporation

14F, No. 8, Tai Yuen 1<sup>st</sup> Street, Chupei City

Hsinchu, Taiwan, R.O.C.

Tel: (8863)5526789

**RICHTEK**

Richtek products are sold by description only. Richtek reserves the right to change the circuitry and/or specifications without notice at any time. Customers should obtain the latest relevant information and data sheets before placing orders and should verify that such information is current and complete. Richtek cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Richtek product. Information furnished by Richtek is believed to be accurate and reliable. However, no responsibility is assumed by Richtek or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Richtek or its subsidiaries.

Copyright © 2022 Richtek Technology Corporation. All rights reserved. **RICHTEK** is a registered trademark of Richtek Technology Corporation

DS3181C-02 December 2022

www.richtek.com

## Datasheet Revision History

| Version | Date       | Description | Item                                                                                                                                                                 |
|---------|------------|-------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 02      | 2022/12/13 | Modify      | Features on P1<br>Simplified Application Circuit on P1<br>Note 3 on P9<br>Application Information on P12<br>Outline Dimension on P14<br>Footprint Information on P15 |