



4-BIT BIDIRECTIONAL UNIVERSAL SHIFT REGISTER

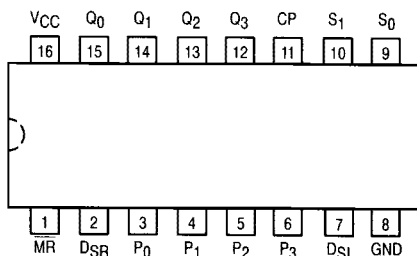
The MC74F194 is a high-speed 4-bit bidirectional universal shift register. As a high-speed multifunctional, sequential building block, it is useful in a wide variety of applications. It may be used in serial-serial, shift left, shift right, serial-parallel, parallel-serial, and parallel-parallel data register transfers. The F194 is similar in operation to the S195 universal shift register, with added features of shift left without external connections and hold (do nothing) modes of operation.

- Typical Shift Frequency of 150 MHz
- Asynchronous Master Reset
- Hold (Do Nothing) Mode
- Fully Synchronous Serial or Parallel Data Transfers

FUNCTIONAL DESCRIPTION

The F194 contains four edge-triggered D flip-flops and the necessary interstage logic to synchronously perform shift right, shift left, parallel load and hold operations. Signals applied to the Select (S_0 , S_1) inputs determine the type of operation, as shown in the Function Table. Signals on the Select, Parallel data (P_0 – P_3) and Serial data (DSR , DSL) inputs can change when the clock is in either state, provided only that the recommended setup and hold times, with respect to the clock rising edge, are observed. A LOW signal on Master Reset (MR) overrides all other inputs and forces the outputs LOW.

CONNECTION DIAGRAM



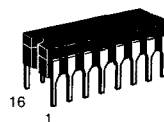
FUNCTION TABLE

Operating Mode	Inputs						Outputs			
	MR	S ₁	S ₀	DSR	DSL	P _n	Q ₀	Q ₁	Q ₂	Q ₃
Reset	L	X	X	X	X	X	L	L	L	L
Hold	H	I	I	X	X	X	q ₀	q ₁	q ₂	q ₃
Shift Left	H	h	I	X	I	X	q ₁	q ₂	q ₃	L
	H	h	I	X	h	X	q ₁	q ₂	q ₃	H
Shift Right	H	I	h	I	X	X	L	q ₀	q ₁	q ₂
	H	I	h	h	X	X	H	q ₀	q ₁	q ₂
Parallel Load	H	h	h	X	X	P _n	P ₀	P ₁	P ₂	P ₃

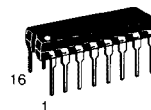
I = LOW voltage level one setup time prior to the LOW-to-HIGH clock transition.
h = HIGH voltage level one setup time prior to the LOW-to-HIGH clock transition.
P_n, q_n = Lower case letters indicate the state of the referenced input or output one setup time prior to the LOW-to-HIGH clock transition.
H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial

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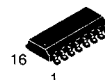
4-BIT BIDIRECTIONAL UNIVERSAL SHIFT REGISTER FAST™ SCHOTTKY TTL



J SUFFIX
CERAMIC
CASE 620-09



N SUFFIX
PLASTIC
CASE 648-08

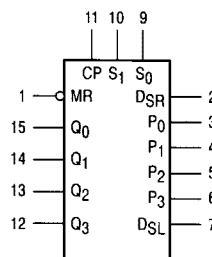


D SUFFIX
SOIC
CASE 751B-03

ORDERING INFORMATION

MC74FXXXJ Ceramic
MC74FXXXN Plastic
MC74FXXXD SOIC

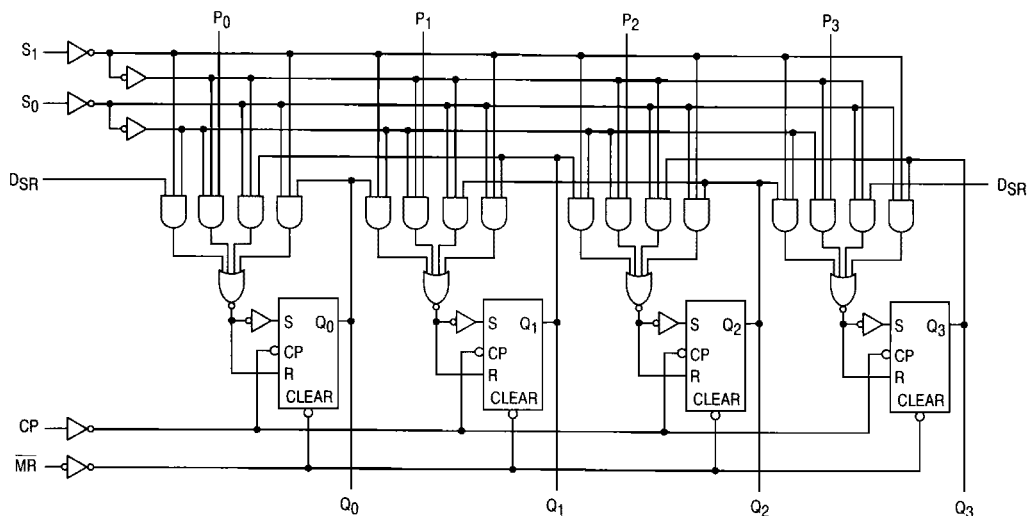
LOGIC SYMBOL



VCC = PIN 16
GND = PIN 8

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LOGIC DIAGRAM



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Typ	Max	Unit
V _{CC}	Supply Voltage	74	4.5	5.0	5.5	V
T _A	Operating Ambient Temperature Range	74	0	25	70	°C
I _{OH}	Output Current — High	74			–1.0	mA
I _{OL}	Output Current — Low	74			20	mA

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter	Limits			Unit	Test Conditions
		Min	Typ	Max		
V _{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage
V _{IL}	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage
V _{IK}	Input Clamp Diode Voltage			–1.2	V	I _{IN} = –18 mA, V _{CC} = MIN
V _{OH}	Output HIGH Voltage	74	2.5	3.4	V	I _{OH} = –1.0 mA, V _{CC} = 4.5 V
		74	2.7	3.4	V	I _{OH} = –1.0 mA, V _{CC} = 4.75 V
V _{OL}	Output LOW Voltage		0.35	0.5	V	I _{OL} = 20 mA, V _{CC} = MIN
I _{IH}	Input HIGH Current			20	μA	V _{IN} = 2.7 V, V _{CC} = MAX
				100	μA	V _{IN} = 7.0 V, V _{CC} = MAX
I _{IL}	Input LOW Current			–0.6	mA	V _{IN} = 0.5 V, V _{CC} = MAX
I _{OS}	Output Short Circuit Current (Note 2)	–60		–150	mA	V _{OUT} = 0 V, V _{CC} = MAX
I _{CC}	Power Supply Current		33	46	mA	S _n , MR, DSR, D _{SL} = 4.5 V, P _n = Gnd, CP = J, V _{CC} = MAX

NOTES:

- For conditions such as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.
- Not more than one output should be shorted at a time, nor for more than 1 second.

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AC CHARACTERISTICS

Symbol	Parameter	74F		74F		Unit
		$T_A = +25^{\circ}\text{C}$ $V_{CC} = +5.0\text{ V}$ $C_L = 50\text{ pF}$		$T_A = 0\text{ to }+70^{\circ}\text{C}$ $V_{CC} = 5.0\text{ V} \pm 10\%$ $C_L = 50\text{ pF}$		
		Min	Max	Min	Max	
f _{max}	Maximum Shift Frequency	105		90		MHz
t _{PLH}	Propagation Delay CP to Q _N	3.0	7.0	3.5	8.0	ns
t _{PHL}		3.5	7.5	3.5	8.0	
t _{PHL}	Propagation Delay MR to Q _N	4.5	12	4.5	14	ns

AC OPERATING REQUIREMENTS

Symbol	Parameter	74F		74F		Unit
		$T_A = +25^{\circ}\text{C}$ $V_{CC} = +5.0\text{ V}$		$T_A = 0\text{ to }+70^{\circ}\text{C}$ $V_{CC} = 5.0\text{ V} \pm 10\%$		
		Min	Max	Min	Max	
$t_S(\text{H})$ $t_S(\text{L})$	Set up Time, HIGH or LOW P_n or DSR or DSL to CP	4.0 4.0		4.0 4.0		ns
$t_H(\text{H})$ $t_H(\text{L})$	Hold Time, HIGH or LOW P_n or DSR or DSL to CP	0 0		1.0 1.0		
$t_S(\text{H})$ $t_S(\text{L})$	Set up Time, HIGH or LOW S_n to CP	8.0 8.0		9.0 8.0		
$t_H(\text{H})$ $t_H(\text{L})$	Hold Time, HIGH or LOW S_n to CP	0 0		0 0		
$t_W(\text{H})$	CP Pulse Width HIGH	5.0		5.5		ns
$t_W(\text{L})$	$\overline{\text{MR}}$ Pulse Width LOW	5.0		5.0		ns
t_{rec}	Recovery Time $\overline{\text{MR}}$ to CP	7.0		8.0		ns