

---

## *Three-Phase Sensorless Fan Driver*

---

### **Discontinued Product**

This device is no longer in production. The device should not be purchased for new design applications. Samples are no longer available.

Date of status change: October 31, 2011

#### **Recommended Substitutions:**

*For existing customer transition, and for new customers or new applications, refer to the [A4941](#).*

---

NOTE: For detailed information on purchasing options, contact your local Allegro field applications engineer or sales representative.

---

*Allegro MicroSystems, Inc. reserves the right to make, from time to time, revisions to the anticipated product life cycle plan for a product to accommodate changes in production capabilities, alternative product availabilities, or market demand. The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringements of patents or other rights of third parties which may result from its use.*

---

## Three-Phase Sensorless Fan Driver

### Features and Benefits

- Sensorless (no Hall sensors required)
- Soft switching for reduced audible noise
- Minimal external components
- PWM speed input
- FG speed output
- Low power standby mode
- Lock detection
- Optional overcurrent protection

**Package: 16-pin TSSOP with exposed thermal pad (suffix LP)**



Not to scale

### Description

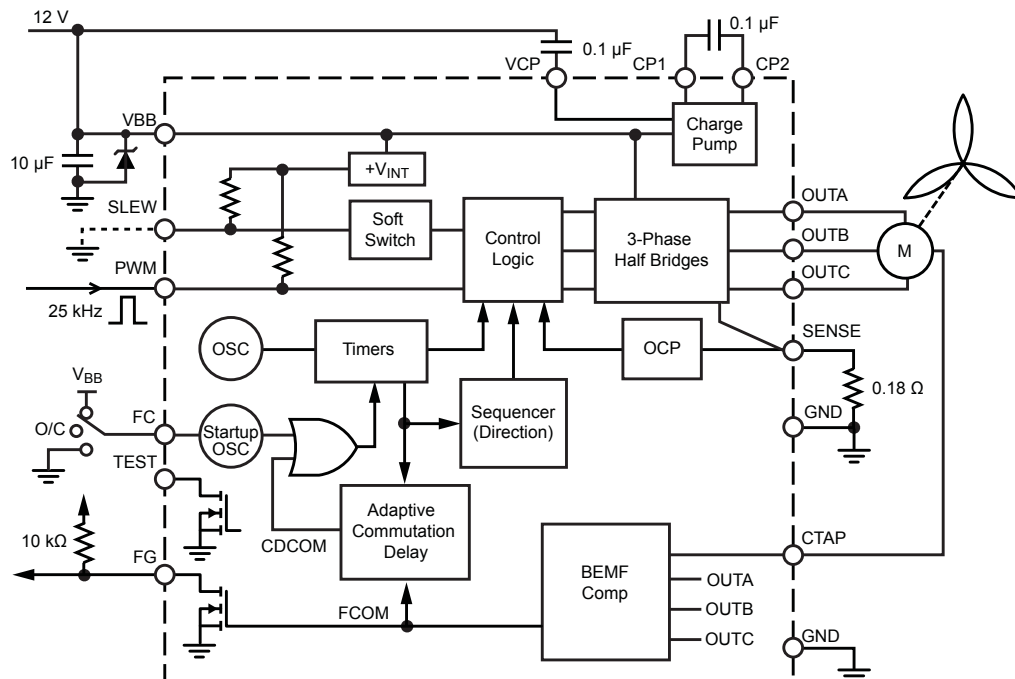
The A4934 three-phase motor driver incorporates BEMF sensing to eliminate the requirement for Hall sensors in fan applications.

A pulse wave modulated (PWM) input is provided to control motor speed, allowing system cost savings by eliminating external variable power supply. PWM input can also be used as an on/off switch to disable motor operation and place the IC into a low power standby mode.

The A4934 soft switching settings are designed for lower inductance or lower speed motors. For higher inductance or higher speed motors consider using the pin-compatible A4941.

The A4934 is provided in a 16-pin TSSOP package (suffix LP) with an exposed thermal pad. It is lead (Pb) free, with 100% matte tin leadframe plating.

### Functional Block Diagram



## Selection Guide

Part Number	Packing
A4934GLPTR-T	4000 pieces per 13-in. reel

## Absolute Maximum Ratings

Characteristic	Symbol	Notes	Rating	Unit
Supply Voltage	$V_{BB}$		20	V
Logic Input Voltage Range	$V_{IN}$	PWM, SLEW	-0.3 to 5.5	V
		FC	-0.3 to $V_{BB}$	V
Logic Output Voltage	$V_{OUT}$	FG	$V_{BB}$	V
Output Current	$I_{OUT}$	Peak (startup and lock rotor)	1.25	A
		Duty cycle = 100%	800	mA
Operating Ambient Temperature	$T_A$	G temperature range	-40 to 105	°C
Maximum Junction Temperature	$T_{J(max)}$		150	°C
Storage Temperature	$T_{stg}$		-55 to 150	°C

## Recommended Operating Conditions

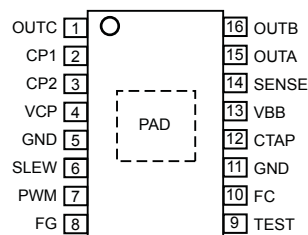
Characteristic	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	$V_{BB}$		8	—	16	V
Output Current	$I_{OUT}$	Peak (startup and lock rotor)	—	—	800	mA
		Run current	—	<500	—	mA

**Thermal Characteristics** may require derating at maximum conditions

Characteristic	Symbol	Test Conditions*	Value	Unit
Package Thermal Resistance	$R_{\theta JA}$	On 4-layer PCB based on JEDEC standard	34	°C/W
		On 2-layer PCB with 1 in. <sup>2</sup> of copper area each side	52	°C/W

\*Additional thermal information available on the Allegro website

## Pin-out Diagram



## Terminal List Table

Name	Number	Function
CP1	2	Charge pump
CP2	3	Charge pump
CTAP	12	Motor terminal center tap
FC	10	Logic input
FG	8	Speed output signal
GND	5, 11	Ground
OUTA	15	Motor terminal A
OUTB	16	Motor terminal B
OUTC	1	Motor terminal C
PWM	7	Logic input
SENSE	14	Sense resistor connection
SLEW	6	Logic input
TEST	9	Test use only, leave open circuit
VBB	13	Input supply
VCP	4	Charge pump

**ELECTRICAL CHARACTERISTICS** Valid at  $T_A = 25^\circ\text{C}$ ,  $V_{BB} = 12\text{ V}$ ; unless otherwise noted

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
VBB Supply Current	$I_{BB}$		–	2.5	5	mA
	$I_{BBST}$	Standby mode, PWM = 0 V, SLEW = FC = O/C	–	25	50	$\mu\text{A}$
Total Driver $R_{DS(on)}$ (Sink + Source)	$R_{DS(on)}$	$I = 800\text{ mA}$ , $T_J = 25^\circ\text{C}$	–	750		m $\Omega$
Overcurrent Threshold	$V_{OCL}$		180	200	220	mV
PWM Low Level	$V_{IL}$		–	–	2	V
PWM High Level	$V_{IH}$		0.8	–	–	V
Input Hysteresis	$V_{HYS}$		–	300	–	mV
Logic Input Current	$I_{IN}$	PWM, FC $V_{IN} = 0\text{ V}$	–	–20	–	$\mu\text{A}$
		SLEW	–	–50	–	$\mu\text{A}$
Output Saturation Voltage	$V_{SAT}$	$I = 5\text{ mA}$	–	–	0.3	V
FG Output Leakage	$I_{FG}$	$V = 16\text{ V}$	–	–	1	$\mu\text{A}$
<b>Protection Circuitry</b>						
Lock Protection	$t_{on}$		–	2	–	s
	$t_{off}$		–	5	–	s
Thermal Shutdown Temperature	$T_{JTSD}$	Temperature increasing	150	165	180	$^\circ\text{C}$
Thermal Shutdown Hysteresis	$T_{JHYS}$	Recovery = $T_{JTSD} - \Delta T_J$	–	15	–	$^\circ\text{C}$
VBB Undervoltage Lockout (UVLO)	$V_{UVLO}$	$V_{BB}$ rising	–	6.3	–	V
VBB Undervoltage Lockout (UVLO) Hysteresis	$V_{UVLOHYS}$		–	0.56	–	V

## Functional Description

The driver system is a three-phase, BEMF sensing motor controller and driver. Commutation is controlled by a proprietary BEMF sensing technique.

The motor drive system consists of three half bridge NMOS outputs, BEMF sensing circuits, adaptive commutation control, and state sequencer. The sequencer determines which output devices are active. The BEMF sensing circuits and adaptive commutation circuits determine when the state sequencer advances to the next state.

A complete self-contained BEMF sensing commutation scheme is provided. The three half-bridge outputs are controlled by a state machine with six possible states, shown in figure 1. Motor BEMF is sensed at the tri-stated output for each state.

BEMF sensing motor commutation relies on the accurate comparison of the voltage on the tri-stated output to the voltage at the center tap of the motor. The BEMF zero crossing, the point where the tri-stated motor winding voltage crosses the center tap voltage, is used as a positional reference. The zero crossing occurs roughly halfway through one commutation cycle.

Adaptive commutation circuitry and programmable timers determine the optimal commutation points with minimal external components. The major blocks within this system are: the BEMF zero crossing detector, Commutation Delay timer, and the Blank timer.

### BEMF Zero Cross Detection

BEMF zero crossings are detected by comparing the voltage at the tri-stated motor winding to the voltage at the motor center tap. Zero crossings are indicated by the FCOM signal, which goes high at each valid zero crossing and low at the beginning of the next commutation. In each state, the BEMF detector looks for the first correct polarity zero crossing and latches it until the next state. This latching action, along with precise comparator hysteresis, makes for a robust sensing system. At the beginning of each commutation event, the BEMF detectors are inhibited for a period of time set by the Blank timer. This is done so that commutation transients do not disturb the BEMF sensing system.

### Commutation Event

See figure 1 for timing relationships. The commutation sequence is started by a CDCOM pulse or a valid XCOM at startup. After

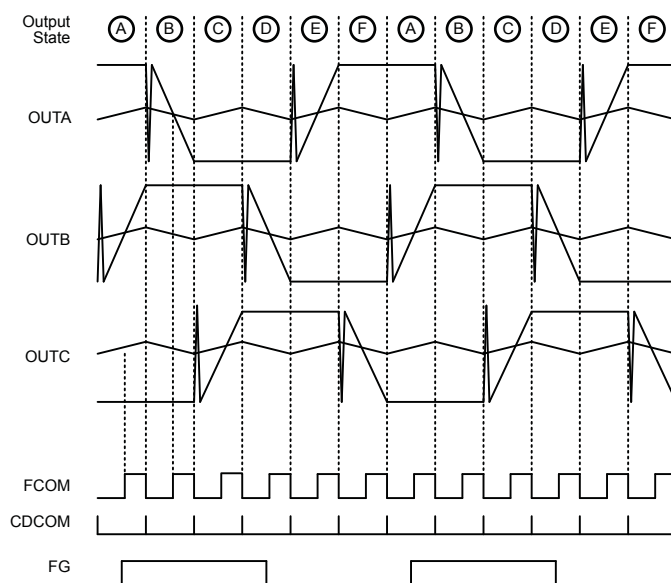


Figure 1. Motor Terminal Output States

the commutation delay period, a CDCOM is asserted, starting the Blank timer. The Blank signal disables the BEMF detector so the comparator is not active during the commutation transients. The next zero crossing, detected on the tri-stated output, causes FCOM to go high. This triggers the Commutation Delay timer and the sequence repeats.

### Startup

At startup, commutations are provided by an onboard oscillator. These commutations are part of the startup scheme, to step the motor to generate BEMF until legitimate BEMF zero crossings are detected and normal BEMF sensing commutation is achieved. Until an appropriate number of FCOM pulses are achieved (96), 100% PWM will be applied to the motor windings.

### Standby Mode

Driving PWM low for 500  $\mu$ s causes the IC to enter a low power standby mode.

### Lock Detect

Valid FCOM signals must be detected to ensure the motor is not stalled. If a valid FG is not detected for 2 s, the outputs will be disabled for 5 s before an auto-restart is attempted.

### FG Output

The FG output provides fan speed information to the system. FG is an open drain output.

### PWM Input

The duty cycle applied to the PWM pin is translated directly to an average duty cycle applied across the motor windings to control speed.

- For voltage controlled applications, where  $V_{BB}$  controls the speed, PWM can be left open circuit. PWM is internally pulled-up to logic high level.
- PWM also can be used as a control input to start and stop the motor.

- For PWM applications, input frequencies in the range 15 to 30 kHz are applied directly to the motor windings. If the PWM duty cycle is very small, then the IC will apply a minimum pulse width of typically 6  $\mu$ s. This minimum pulse width effects the minimum speed. As a result of having a minimum pulse width, the IC can startup and operate down to very short duty cycles.

### SLEW Input

Controls the level of soft switching:

SLEW Pin Connection	Soft Switch Status
GND	Less
Open	More

### FC Input

This is the logic input to set force commutation time at startup, by connection as follows:

FC Pin Connection	Startup Commutation Time (ms)
GND	100
VBB	50
Open	200

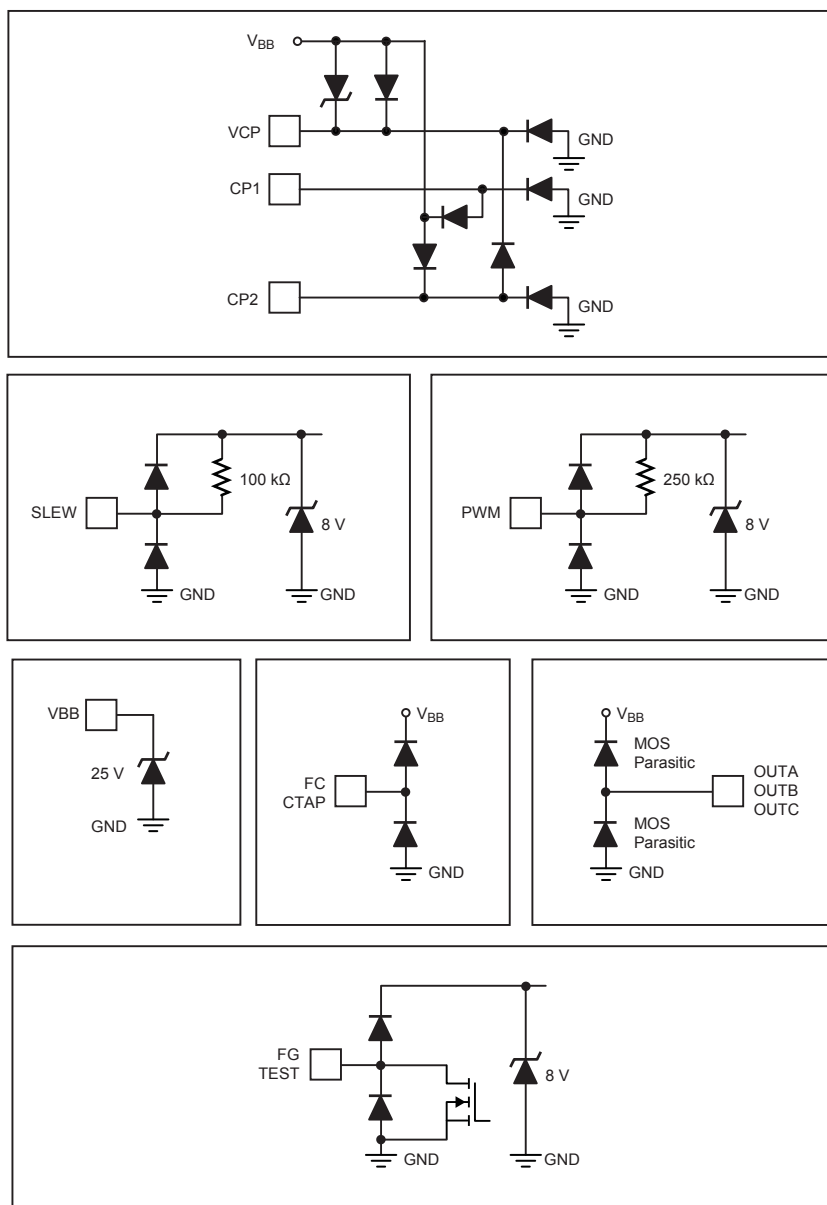
### Overcurrent Protection

If needed, a sense resistor can be installed to limit current. (See Applications Information section for more details.) The current limit trip point would be set by:

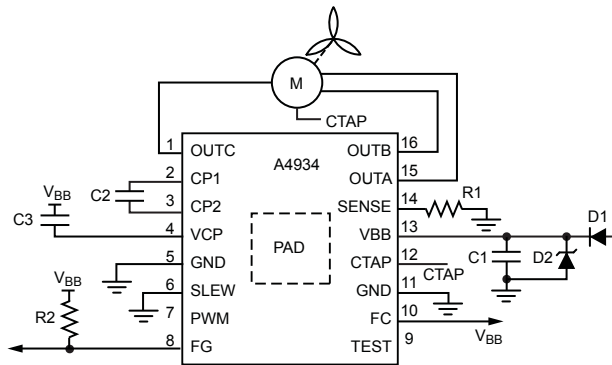
$$I_{OCL} = 200 \text{ mV} / R_S$$

When the trip point is reached, if the threshold voltage,  $V_{OCL}$ , is exceeded, the drivers will be disabled for 25  $\mu$ s.

## Input/Output Structures



## Application Information



Typical Application Circuit; speed adjusted via VBB

Name	Typical Value	Description
C1	10 $\mu$ F / 25 V	VBB supply capacitor, minimum 10 $\mu$ F, electrolytic can be used
C2,C3	0.1 $\mu$ F / 25 V	Charge pump ceramic capacitors
R2	10 k $\Omega$	FG pull-up resistor, can be pulled-up to VBB if required
D1	>1.5 A rated	Optional blocking diode for supply reverse polarity protection
D2	17 V	Transient voltage suppressor (TVS)
R1	0.18 $\Omega$ / 0.25 W	Current limiting sense resistor, required for low resistance motors

### Startup Oscillator Setting (FC)

Typically, the 50 ms setting is optimum for motors appropriate for use with the A4934. If the motor does not produce a proper BEMF signal at startup when power is applied, a longer setting may be required.

### SLEW Setting

For some motors, soft switching will reduce audible noise. The soft switching function can result in motor stall for some motors, specifically motors with large inductance that run at higher speeds. For this situation, there are two potential solutions:

- Limit the motor speed by lowering the maximum demand, by reducing either  $V_{motor(max)}$  or the PWM duty applied.
- Consider the pin-to-pin compatible IC A4941 that allows disabling of the soft switching function.

### Current Limiting

Use of the current limit circuit is not required. If motor resistance (phase-to-phase) will limit the current below the rating in the Absolute Maximum table, then simply connect the SENSE pin to

ground. That is:

- If  $(V_{BB(max)} / R_{motor}) < 1.25$  A, eliminate  $R_S$ .
- If  $(V_{BB(max)} / R_{motor}) > I_{OUT(max)}$ , the choice of  $R_S$  determines the current limit setting; recommended range is  $167 \text{ m}\Omega < R_S < 250 \text{ m}\Omega$ .

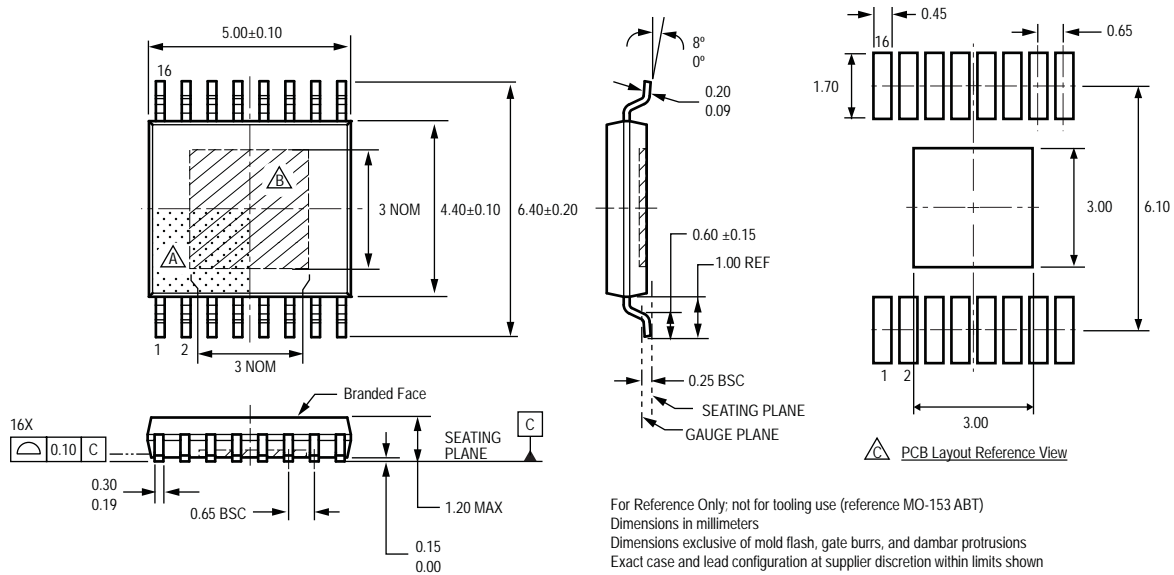
Note: For some motor types, use of the current limit circuit may prevent proper startup due to the effect of the chopping on the BEMF voltage appearing on the tri-stated winding.

### Layout Notes

- Connect GND pins (5,11) to exposed pad ground area under package.
- Add thermal vias from exposed pad to bottom side ground plane.
- Place  $V_{BB}$  decoupling capacitor as close to the IC as possible.
- Place sense resistor, (if used), as close to the IC as possible.



## Package LP, 16-Pin TSSOP with Exposed Thermal Pad



For Reference Only; not for tooling use (reference MO-153 ABT)  
 Dimensions in millimeters  
 Dimensions exclusive of mold flash, gate burrs, and dambar protrusions  
 Exact case and lead configuration at supplier discretion within limits shown

- △ Terminal #1 mark area
- △ Exposed thermal pad (bottom surface); dimensions may vary with device
- △ Reference land pattern layout (reference IPC7351 SOP65P640X110-17M):  
 All pads a minimum of 0.20 mm from all adjacent pads; adjust as necessary to meet application process requirements and PCB layout tolerances; when mounting on a multilayer PCB, thermal vias at the exposed thermal pad land can improve thermal dissipation (reference EIA/JEDEC Standard JESD51-5)

Copyright ©2010, Allegro MicroSystems, Inc.

Allegro MicroSystems, Inc. reserves the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the performance, reliability, or manufacturability of its products. Before placing an order, the user is cautioned to verify that the information being relied upon is current.

Allegro's products are not to be used in life support devices or systems, if a failure of an Allegro product can reasonably be expected to cause the failure of that life support device or system, or to affect the safety or effectiveness of that device or system.

The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.

For the latest version of this document, visit our website:

[www.allegromicro.com](http://www.allegromicro.com)