

Features

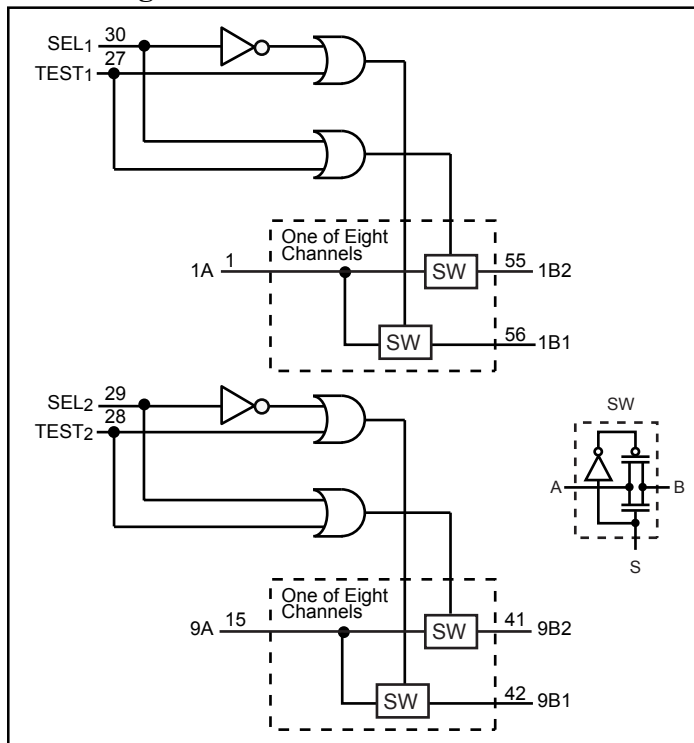
- 5-ohm Switch Connection Between Two Ports
- Near-Zero Propagation Delay
- Fast Switching Speed: 4.5ns (max.)
- Ultra-Low Quiescent Power (0.2mA typical)
- ideal for notebook applications.
- Industrial Operating Temperature: -40°C to 85°C
- Package (Pb-free & Green available):
- 56-pin 240-mil wide plastic TSSOP(A)
- 56-pin 300-mil wide plastic SSOP(V)

Description

Pericom Semiconductor's PI3B16233 is a 16-bit to 32-bit mux/demux switch. This device can be used for memory bank interleaving. The PI3B16233 can be used as two 8-bit to 16-bit multiplexers or as one 16-bit to 32-bit multiplexer.

Two select inputs (SEL1 and SEL2) control the data flow. When the TEST inputs are asserted HIGH, the A port is connected to both the B1 and the B2 ports.

Block Diagram



Pin Configuration

1A	1	56	1B1
2B1	2	55	1B2
2B2	3	54	2A
3A	4	53	3B1
4B1	5	52	3B2
4B2	6	51	4A
5A	7	50	5B1
6B1	8	49	5B2
6B2	9	48	6A
7A	10	47	7B1
8B1	11	46	7B2
8B2	12	45	8A
GND	13	44	GND
VCC	14	43	VCC
9A	15	42	9B1
10B1	16	41	9B2
10B2	17	40	10A
11A	18	39	11B1
12B1	19	38	11B2
12B2	20	37	12A
13A	21	36	13B1
14B1	22	35	13B2
14B2	23	34	14A
15A	24	33	15B1
16B1	25	32	15B2
16B2	26	31	16A
TEST1	27	30	SEL1
TEST2	28	29	SEL2

Function Table

Inputs		Function
SEL	TEST	
L	L	A = B1
H	L	A = B2
X	H	A = B1 and A = B2

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage Range	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 3.0\text{V}$ to 3.6V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic High Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic Low Level	–0.5		0.8	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}; V_{IN} = V_{CC}$			± 1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}; V_{IN} = \text{GND}$			± 1	
I_{OZH}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$			± 1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$		–0.7	–1.2	V
R_{ON}	Switch ON Resistance ⁽³⁾	$V_{CC} = \text{Min.}, V_{IN} = 0.0\text{V}, I_{ON} = 48\text{mA}$ or 64mA $V_{CC} = \text{Min.}, V_{IN} = 2.4\text{V}, I_{ON} = 15\text{mA}$		5 10	8 15	Ω

Capacitance ($T_A = 25^\circ\text{C}$, $f = 1\text{MHz}$)

Parameters ⁽⁵⁾	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.0	pF
C_{ON}	A/B Capacitance, Switch On		25.0	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^\circ\text{C}$ ambient and maximum loading.
- Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$			10	μA
ΔI_{CC}	Supply Current per Input @ TTL High	$V_{CC} = \text{Max.}$	$V_{IN} = 3.0\text{V}(3)$			750	
I_{CCD}	Supply Current per Input per MHz ⁽⁴⁾	$V_{CC} = \text{Max.}$ A & B Pin Open Control Input Toggling 50% Duty Cycle				0.25	mA/MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $+25^\circ\text{C}$ ambient.
3. Per TTL driven input (control inputs only); A and B pins do not contribute to I_{CC} .
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

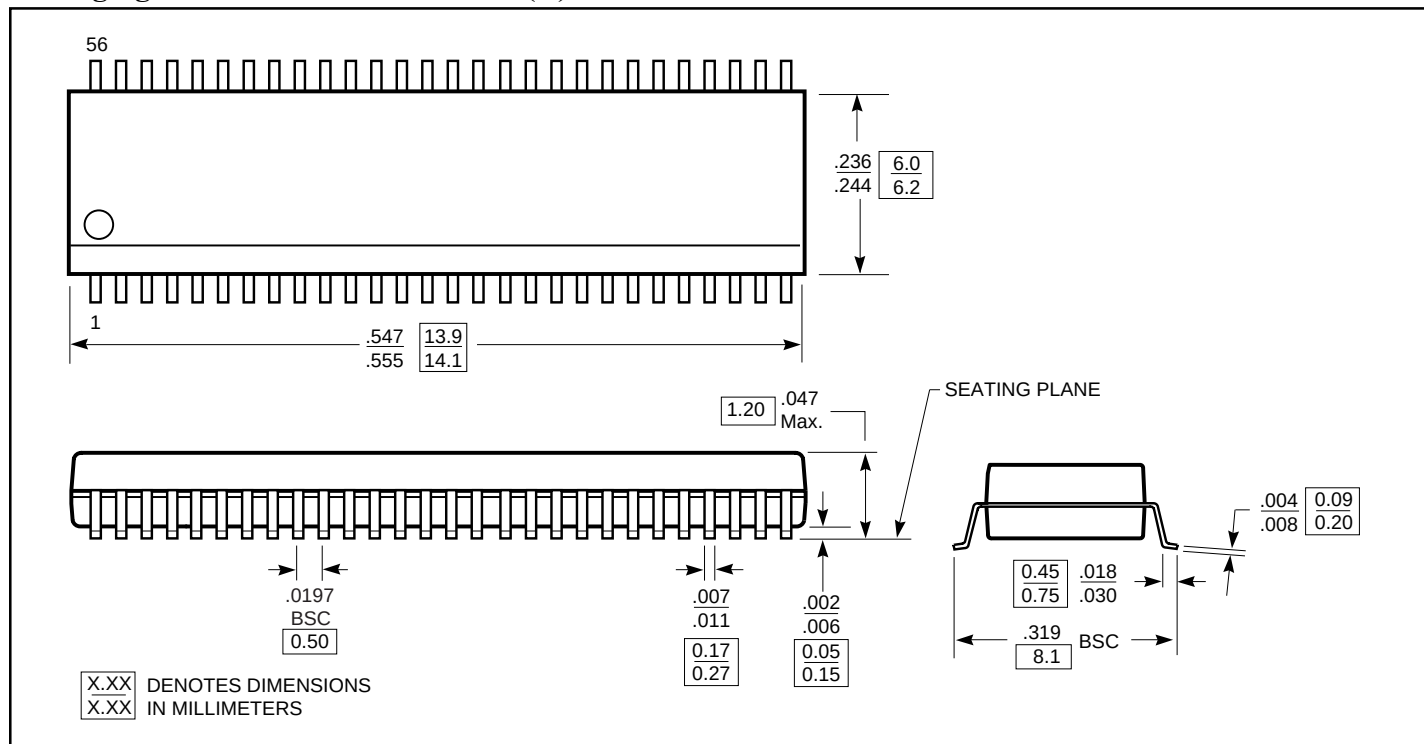
Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	Com.		Units
			Max.	Min.	
t_{PLH} t_{PHL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	$C_L = 50\text{pF}$, $R_L = 500\Omega$		0.25	ns
t_{PZH} t_{PZL}	Bus Enable Time SEL_X to Ax or Bx	$C_L = 50\text{pF}$, $R_L = 500\Omega$, $R = 500\Omega$	1	4.5	
t_{PHZ} t_{PLZ}	Bus Disable Time SEL_X to Ax or Bx	$C_L = 50\text{pF}$, $R_L = 500\Omega$	1	5.0	

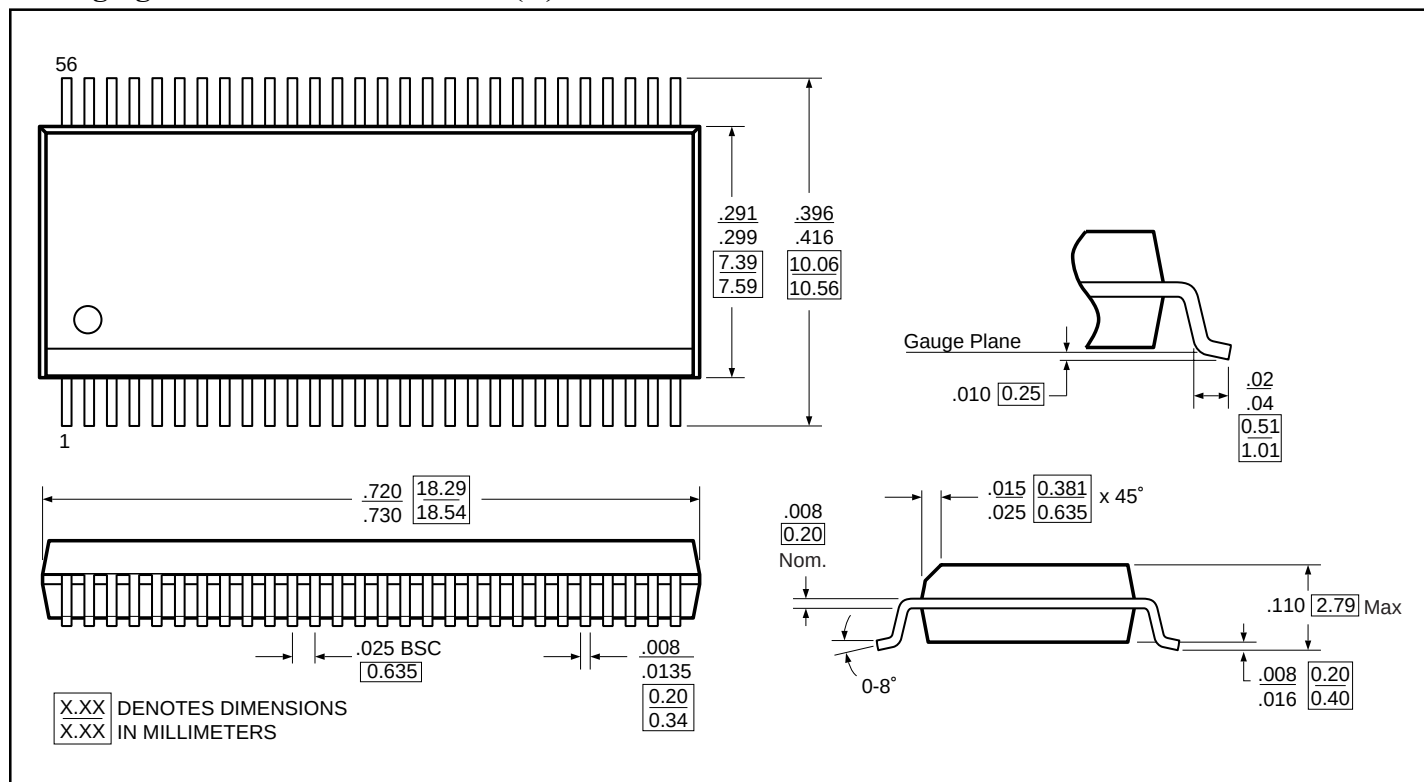
Notes:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Packaging Mechanical: 56-Pin TSSOP (A)



Packaging Mechanical: 56-Pin SSOP (V)



Applications Information

Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a + 3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Power-Supply Sequencing and Hot-Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Ordering Information

Ordering Code	Package Code	Package Type
PI3B16233A	A	56-pin 240-mil wide TSSOP
PI3B16233AE	A	Pb-free & Green, 56-pin 240-mil wide TSSOP
PI3B16233V	V	56-pin 300-mil wide SSOP
PI3B16233VE	V	Pb-free & Green, 56-pin 300-mil wide SSOP

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/