

absolute maximum ratings over operating temperature ranges (unless otherwise noted)[†]

Supply voltage range, V_{CC}	–0.5 V to 4.6 V
Supply voltage range, V_{CCP} , V_{CCA} , V_{CCB} , V_{CCZ} , V_{CCI}	–0.5 V to 6 V
Input voltage range, V_I : PCI	–0.5 V to $V_{CCP} + 0.5$ V
Card A	–0.5 to $V_{CCA} + 0.5$ V
Card B	–0.5 to $V_{CCB} + 0.5$ V
ZV	–0.5 to $V_{CCZ} + 0.5$ V
MISC	–0.5 to $V_{CCI} + 0.5$ V
Fail safe	–0.5 V to $V_{CC} + 0.5$ V
Output voltage range, V_O : PCI	–0.5 V to $V_{CCP} + 0.5$ V
Card A	–0.5 to $V_{CCA} + 0.5$ V
Card B	–0.5 to $V_{CCB} + 0.5$ V
ZV	–0.5 to $V_{CCZ} + 0.5$ V
MISC	–0.5 to $V_{CCI} + 0.5$ V
Fail safe	–0.5 V to $V_{CC} + 0.5$ V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$) (see Note 1)	±20 mA
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) (see Note 2)	±20 mA
Storage temperature range, T_{stg}	–65°C to 150°C
Virtual junction temperature, T_J	150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES:
1. Applies for external input and bidirectional buffers. $V_I > V_{CC}$ does not apply to fail-safe terminals. PCI terminals are measured with respect to V_{CCP} instead of V_{CC} . PC Card terminals are measured with respect to V_{CCA} or V_{CCB} . ZV terminals are measured with respect to V_{CCZ} , and miscellaneous signals are measured with respect to V_{CCI} . The limit specified applies for a dc condition.
 2. Applies for external output and bidirectional buffers. $V_O > V_{CC}$ does not apply to fail-safe terminals. PCI terminals are measured with respect to V_{CCP} instead of V_{CC} . PC Card terminals are measured with respect to V_{CCA} or V_{CCB} . ZV terminals are measured with respect to V_{CCZ} , and miscellaneous signals are measured with respect to V_{CCI} . The limit specified applies for a dc condition.

PRODUCT PREVIEW