

DS75107

Dual Line Receiver

General Description

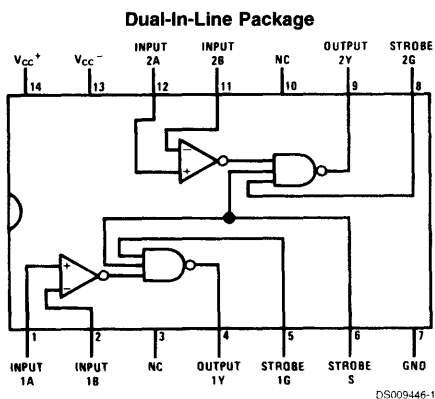
The product described herein is a TTL compatible dual high speed circuit intended for sensing in a broad range of system applications. While the primary usage will be for line receivers of MOS sensing, the product may effectively be used as a voltage comparator, level translator, window detector, transducer preamplifier, and in other sensing applications. As a digital line receiver the product is applicable with the SN55109/SN75109 and μ A75110/DS75110 companion drivers, or may be used in other balanced or unbalanced party-line data transmission systems.

Input protection diodes are incorporated in series with the collectors of the differential input stage. These diodes are useful in certain applications that have multiple V_{CC+} supplies or V_{CC+} supplies that are turned off.

Features

- Diode protected input stage for power "OFF" condition
- 17 ns typ high speed
- TTL compatible
- ± 10 mV or ± 25 mV input sensitivity
- ± 3 V input common-mode range
- High input impedance with normal V_{CC} , or $V_{CC} = 0$ V
- Strobes for channel selection
- Dual circuits
- Sensitivity gntd. over full common-mode range
- Logic input clamp diodes—meets both "A" and "B" version specifications
- ± 5 V standard supply voltages

Connection Diagram



Top View

Order Number DS75107M, DS75107N
See NS Package Number M14A or N14A
For Complete Military 883 Specifications, see RETS Datasheet.
Order Number DS55107AJ/883
See NS Package Number J14A

Selection Guide

Temperature→	$0^{\circ}\text{C} \leq T_A \leq +70^{\circ}\text{C}$	
Package→	Cavity or Molded Dip	
Input Sensitivity→	± 25 mV	± 10 mV
Output Logic↓		
TTL Active Pull-Up	DS75107	
TTL Open Collector		