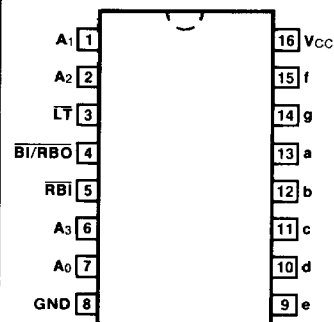
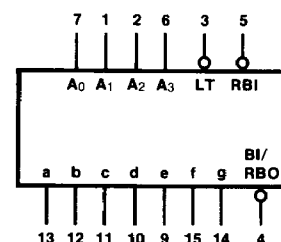


54LS/74LS248**BCD TO 7-SEGMENT DECODER**(With 2 k Ω Pull-up Resistors)**CONNECTION DIAGRAM**
PINOUT A

DESCRIPTION — The 'LS248 has active HIGH outputs with internal 2 k Ω pull-up resistors. It has the same electrical characteristics and pin connections as the 'LS48. The only difference is that the 'LS248 will light the top bar (segment a) for numeral 6 and the bottom bar (segment d) for numeral 9. For detailed description and specifications please refer to the 'LS48 data sheet.

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0\text{ V} \pm 5\%$, $T_A = 0^\circ\text{C to } +70^\circ\text{C}$	$V_{CC} = +5.0\text{ V} \pm 10\%$, $T_A = -55^\circ\text{C to } +125^\circ\text{C}$	
Plastic DIP (P)	A	74LS248PC		9B
Ceramic DIP (D)	A	74LS248DC	54LS248DM	6B
Flatpak (F)	A	74LS248FC	54LS248FM	4L

LOGIC SYMBOL
 $V_{CC} = \text{Pin } 16$
 $GND = \text{Pin } 8$
INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74LS (U.L.) HIGH/LOW
$A_0 - A_3$	BCD Inputs	0.5/0.25
$\overline{RBI}$	Ripple Blanking Input (Active LOW)	0.5/0.25
$\overline{LT}$	Lamp Test Input (Active LOW)	0.5/0.25
$\overline{BI/RBO}$	Blanking Input (Active LOW) or Ripple Blanking Output (Active LOW)	0.5/0.25 1.25/2.0 (1.0)
a — g	Segment Outputs (Active HIGH)	2.5/3.75 (1.25)