

DM74164

8-Bit Serial In/Parallel Out Shift Registers

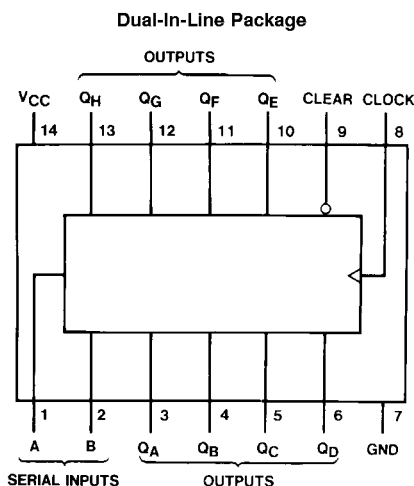
General Description

These 8-bit shift registers feature gated serial inputs and an asynchronous clear. A low logic level at either serial input inhibits entry of the new data, and resets the first flip-flop to the low level at the next clock pulse, thus providing complete control over incoming data. A high logic level on either input enables the other input, which will then determine the state of the first flip-flop. Data at the serial inputs may be changed while the clock is high or low, but only information meeting the setup and hold time requirements will be entered. Clocking occurs on the low-to-high level transition of the clock input. All inputs are diode-clamped to minimize transmission-line effects.

Features

- Gated (enable/disable) serial inputs
- Fully buffered clock and serial inputs
- Asynchronous clear
- Typical clock frequency 36 MHz
- Typical power dissipation 185 mW

Connection Diagram



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Order Number 54164DMQB, 54164FMQB or DM74164N
See Package Number J14A, N14A or W14B

Function Table

Inputs				Outputs			
Clear	Clock	A	B	Q _A	Q _B	...	Q _H
L	X	X	X	L	L	...	L
H	L	X	X	Q _{A0}	Q _{B0}	...	Q _{H0}
H	↑	H	H	H	Q _{An}	...	Q _{Gn}
H	↑	L	X	L	Q _{An}	...	Q _{Gn}
H	↑	X	L	L	Q _{An}	...	Q _{Gn}

H = High Level (steady state), L = Low Level (steady state)

X = Don't Care (any input, including transitions)

↑ = Transition from low to high level

Q_{A0}, Q_{B0}, Q_{H0} = The level of Q_A, Q_B, or Q_H, respectively, before the indicated steady-state input conditions were established.

Q_{An}, Q_{Gn} = The level of Q_A or Q_G before the most recent ↑ transition of the clock; indicates a one-bit shift.

Absolute Maximum Ratings (Note 1)

Supply Voltage

7V

Input Voltage

5.5V

Operating Free Air Temperature Range

54

DM74

Storage Temperature Range

–55°C to +125°C

0°C to +70°C

–65°C to +150°C

Recommended Operating Conditions

Symbol	Parameter	54164			DM74164			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
I _{OH}	High Level Output Current			–0.4			–0.4	mA
I _{OL}	Low Level Output Current			8			8	mA
f _{CLK}	Clock Frequency (Note 5)			25	0		25	MHz
t _W	Pulse Width (Note 5)	Clock		20	20			ns
		Clear		20	20			
t _{SU}	Data Setup Time (Note 5)	15			15			ns
t _H	Data Hold Time (Note 5)	0			5			ns
T _A	Free Air Operating Temperature	–55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = –14 mA			–1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max, V _{IH} = Min	2.4	3.2		V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max V _{IH} = Min, V _{IL} = Max		0.2	0.4	V
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V			1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V			40	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			–1.6	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max	54		–27.5	mA
		(Note 3) DM74	–9		–27.5	
I _{CC}	Supply Current	V _{CC} = Max (Note 4)		37	54	mA

Note 2: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 3: Not more than one output should be shorted at a time.

Note 4: I_{CC} is measured with all outputs open, SERIAL inputs grounded, the CLOCK input at 2.4V, and a momentary ground, then 4.5V, applied to the CLEAR input.

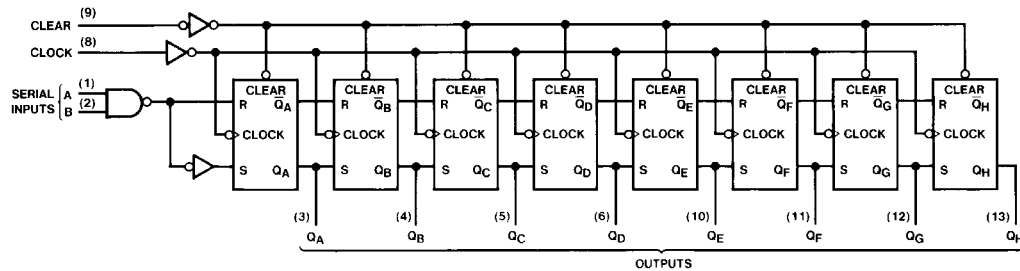
Note 5: T_A = 25°C and V_{CC} = 5V.

Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^\circ C$

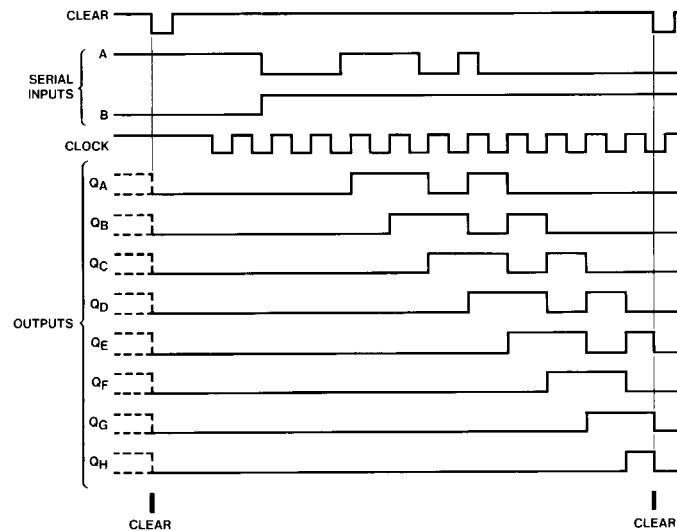
Symbol	Parameter	From (Input) To (Output)	R _L = 800Ω				Units
			C _L = 15 pF		C _L = 50 pF		
			Min	Max	Min	Max	
f _{MAX}	Maximum Clock Frequency		25				MHz
t _{PLH}	Propagation Delay Time Low to High Level Output	Clock to Output		27		30	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Clock to Output		32		37	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Clear to Output		36		42	ns

Logic Diagram

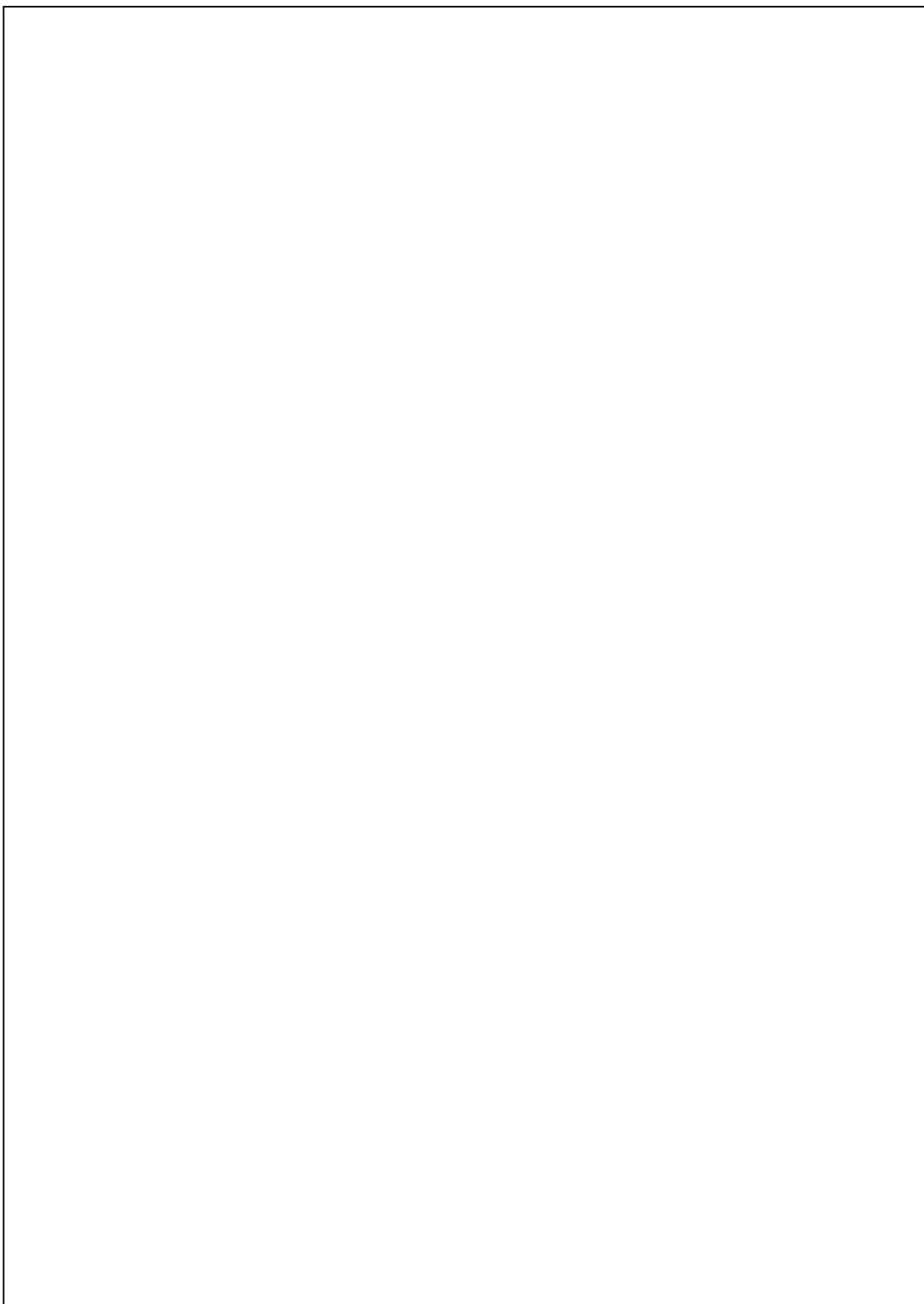


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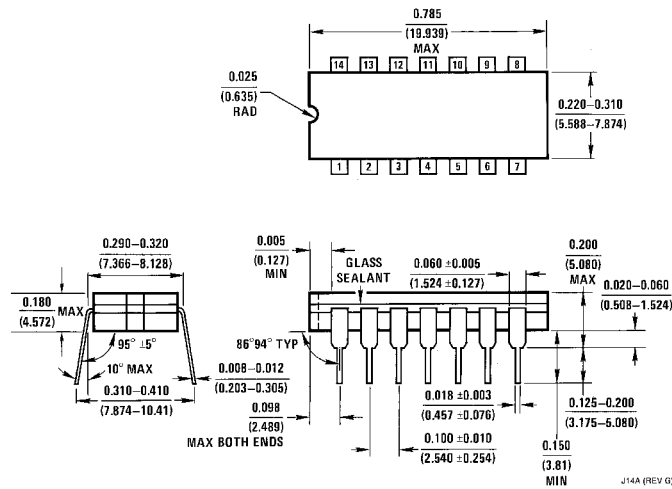
Timing Diagram



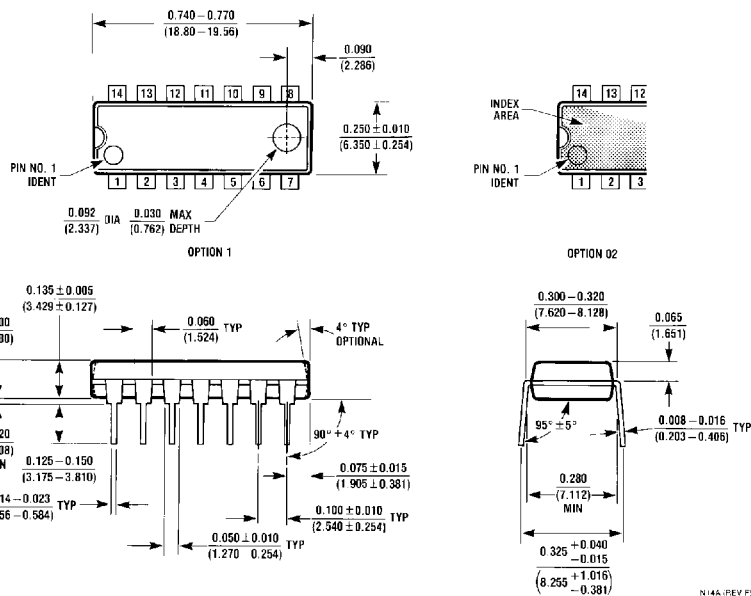
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Physical Dimensions inches (millimeters) unless otherwise noted

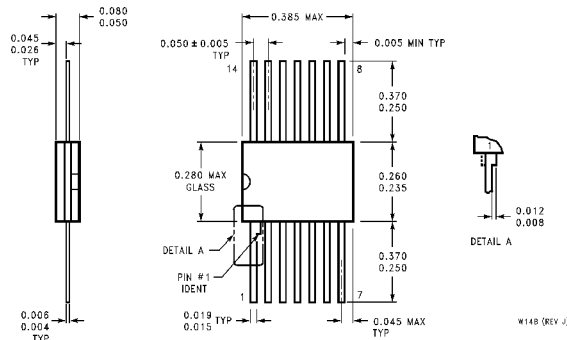


14-Lead Ceramic Dual-In-Line Package (J)
Order Number 54164DMQB
Package Number J14A



14-Lead Molded Dual-In-Line Package (N)
Order Number DM74164N
Package Number N14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Ceramic Flat Package (W)
Order Number 54164FMQB
Package Number W14B

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