

NLAS5157

Ultra-Low 0.4 Ω SPDT Analog Switch

The NLAS5157 is Single Pole Double Throw (SPDT) switch designed for audio systems in portable applications. The NLAS5157 features Ultra-Low R_{ON} of 0.4 Ω typical at $V_{CC} = 3.0$ V and 0.15 Ω R_{ON} Flatness for +3.0 V supply across temperature. This device also has a broad V_{CC} operating range of 1.65 V to 4.5 V, ideal for battery-powered devices.

The NLAS5157 is protected on all pins with 8 kV Human Body Model ESD protection. This allows the device to be placed in a variety of locations, including near the interface, without risk of damage.

Features

- $R_{ON} = 0.4 \Omega$ Typical @ $V_{CC} = 3.0$ V
- V_{CC} Range: 1.65 V to 4.5 V
- 8 kV Human Body Model ESD on All Pins
- These are Pb-Free Devices

Typical Applications

- Mobile Phones
- Portable Devices



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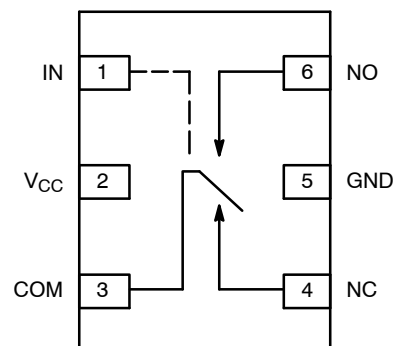
**UDFN6
CASE 517AQ**

MARKING DIAGRAM



A = Specific Device Code*
(Rotated 90°)
M = Date Code

PIN ASSIGNMENTS



(Top View)

ORDERING INFORMATION

Device	Package	Shipping†
NLAS5157MUTCG	UDFN6 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NLAS5157

PIN DESCRIPTION

PIN #	Name	Direction	Description
1	IN	Input	Control Input Select Line
2	V _{CC}	Power	Voltage Supply
3	COM	I/O	Common Signal Line
4	NC	I/O	Normally Closed Signal Line
5	GND	Power	Ground
6	NO	I/O	Normally Open Signal Line

TRUTH TABLE

Control Input	Function
L	NC Connected to COM
H	NO Connected to COM

MAXIMUM RATINGS

Symbol	Pins	Rating	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	0.5 to +5.5		V
V _{IS}	NO, NC, or COM	Analog Signal Voltage	−0.5 to V _{CC} + 0.5		V
V _{IN}	IN	Control Input Voltage	−0.5 to +5.5		V
I _{IS_CON}	NO, NC, or COM	Analog Signal Continuous Current	± 300	Closed Switch	mA
I _{IS_PK}	NO, NC, or COM	Analog Signal Peak Current	± 500	10% Duty Cycle	mA
I _{IN}	IN	Control Input Current	± 20		mA
T _{STG}		Storage Temperature Range	−65 to 150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	1.65 to 4.5		V
V _{IS}	NO, NC, or COM	Analog Signal Voltage	GND to V _{CC}		V
V _{IN}	IN	Control Input Voltage (OVT) Overvoltage Tolerance	GND to 4.5		V
T _A		Operating Temperature Range	−40 to +85		°C
t _r , t _f		Input Rise or Fall Time	20	V _{CC} = 1.6 V – 2.7 V	ns/V
			10	V _{CC} = 3.0 V – 4.5 V	

Minimum and maximum values are guaranteed through test or design across the **Recommended Operating Conditions**, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for each section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.

ESD PROTECTION

Pins	Description	Minimum Voltage
All Pins	Human Body Model	7 kV

DC ELECTRICAL CHARACTERISTICS

CONTROL INPUT (Typical: T = 25°C; V_{CC} = 3.0 V)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
V _{IH}	IN	Control Input High		1.8 – 2.7 3.0 4.5	1.0 1.3 1.6			V
V _{IL}	IN	Control Input Low		1.8 – 2.7 3.0 4.5			0.4 0.5 0.6	V
I _{IN}	IN	Control Input Leakage	0 ≤ V _{IN} ≤ V _{CC}	4.5		±0.1	±0.5	μA

SUPPLY CURRENT AND LEAKAGE (Typical: T = 25°C; V_{CC} = 3.0 V)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
I _{NO/NC} (OFF)	NC, NO	OFF State Leakage	V _{IN} = V _{IL} or V _{IH} V _{NC/NO} = 0.3 V V _{COM} = 4.0 V	4.5		±10	±100	nA
I _{COM} (ON)	COM	ON State Leakage	V _{IN} = V _{IL} or V _{IH} V _{NO} = 0.3 V or 4.0 V with V _{NC} Floating or V _{NC} = 0.3 V or 4.0 V with V _{NO} Floating V _{COM} = 0.3 V or 4.0 V	4.5		±20	±100	nA
I _{CC}	V _{CC}	Quiescent Supply	V _{IN} and V _{IS} = V _{CC} or GND I _D = 0 A	1.65 – 4.5		±0.1	±1.0	μA
I _{OFF}	IN	Power Off Leakage	V _{IN} = 4.5 V or GND	0		±0.5	±1.0	μA

ON RESISTANCE (Typical: T = 25°C; V_{CC} = 3.0 V)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
R _{ON}	NO, NC COM	ON Resistance	I _{ON} = -100 mA V _{IS} = 0 to V _{CC}	3.0 4.5		0.4 0.35	0.6 0.5	Ω
R _{FLAT}	NO, NC COM	R _{ON} Flatness	I _{ON} = -100 mA V _{IS} = 0 to V _{CC}	3.0 4.5		0.12 0.15	0.16 0.17	Ω
ΔR _{ON}	NO, NC COM	R _{ON} Matching	I _{ON} = -100 mA V _{IS} = 0 to V _{CC}	3.0 4.5		0.08 0.08	0.1 0.1	Ω

AC ELECTRICAL CHARACTERISTICS

TIMING/FREQUENCY (Typical: T = 25°C; V_{CC} = 3.0 V, R_L = 50 Ω, C_L = 35 pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
t _{ON}	IN to NC or NO	Turn On Time		2.3 – 4.3		30	40	ns
t _{OFF}	IN to NC or NO	Turn Off Time		2.3 – 4.53		18	25	ns
t _{BBM}	IN to NC or NO	Break Before Make		3.0	2	15		ns
BW		-3dB Bandwidth	C _L = 5 pF	1.65 – 4.5		36		MHz

ISOLATION AND THD (Typical: T = 25°C; V_{CC} = 3.0 V, R_L = 50 Ω, C_L = 5 pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
Q		Charge Injection	V _{IN} = V _{CC} to GND R _{IS} = 0 Ω, C _L = 1.0 nF Q = C _L - ΔV _{OUT}	1.65 – 4.3		38		pC
THD		Total Harmonic Distortion	F _{IS} = 20 Hz to 20 kHz R _L = R _{gen} = 600 Ω, C _L = 1.0 pF V _{IS} = 1.0 V _{PP}	3.0		0.02		%
O _{IRR}	NO	Off Isolation	V _{IN} = 0 V _{NO} or V _{NC} (pk-pk) = 1.0 V	1.65 – 4.5		-54		dB
Xtalk	COM to COMy	Non-Adjacent Channel	V _{NO} or V _{NC} (pk-pk) = 1.0 V	1.65 – 4.5		-54		dB

CAPACITANCE (Typical: T = 25°C; V_{CC} = 3.0 V, R_L = 50 Ω, C_L = 5 pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions	V _{CC} (V)	-40°C to +85°C			Unit
					Min	Typ	Max	
C _{IN}	IN	Control Input		0 V		3.5		pF
C _{ON}	NC to COM	Through Switch	V _{IN} = 0V	3.0 V		95		pF
C _{OFF}	NC, NC	Unselected Port	V _{IS} = 3.0V, V _{IN} = 3.0 V	3.0 V		47		pF

TYPICAL CHARACTERISTICS

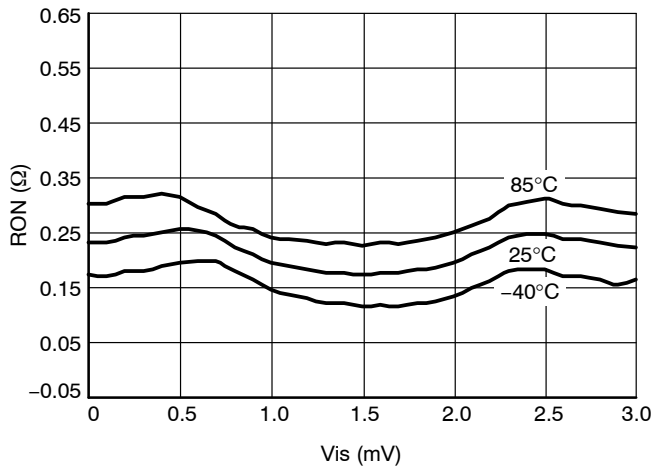


Figure 1. RON vs. Vin @ Vcc = 3.0 V, All Temps

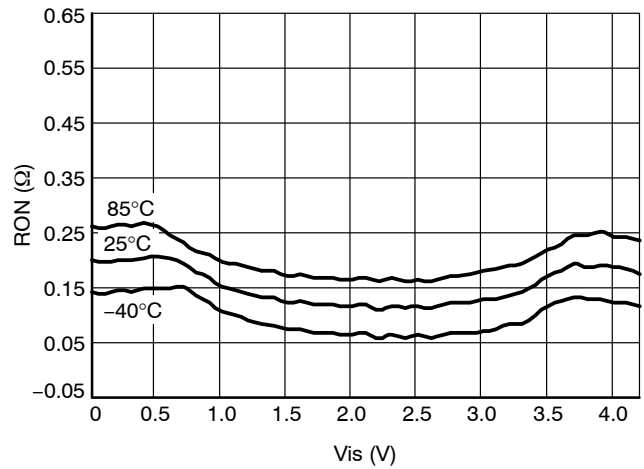


Figure 2. RON vs. Vin @ Vcc = 4.3 V, All Temps

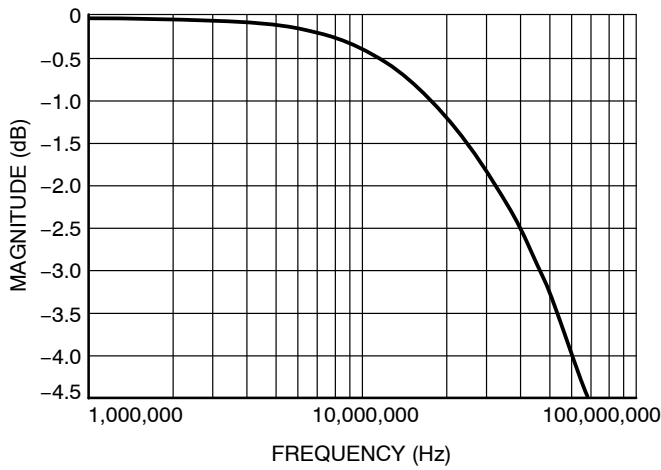


Figure 3. Bandwidth vs. Frequency

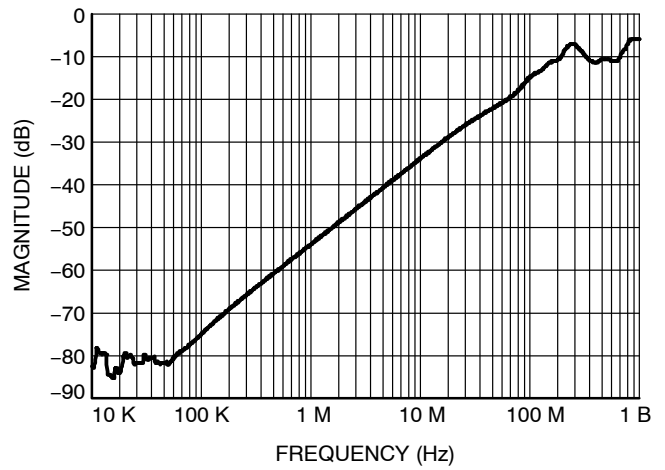


Figure 4. Cross-Talk vs. Frequency @ 25°C

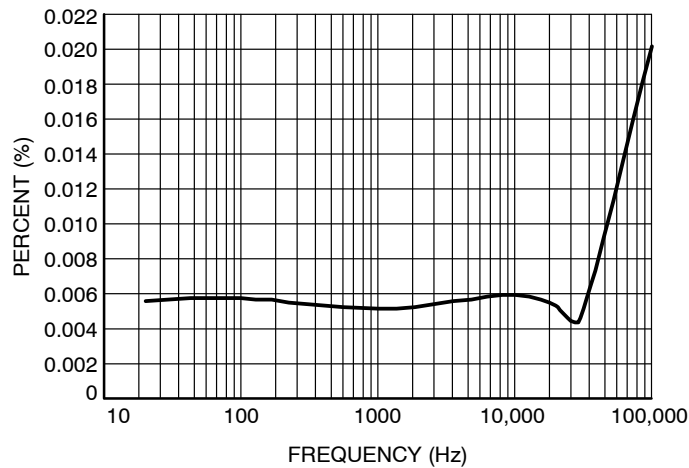


Figure 5. THD vs. Frequency @ Vin = 1 Vpp

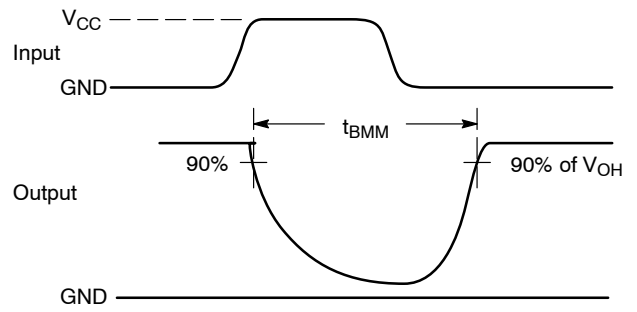
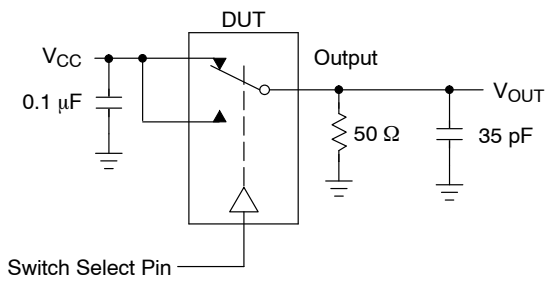


Figure 6. t_{BMM} (Time Break-Before-Make)

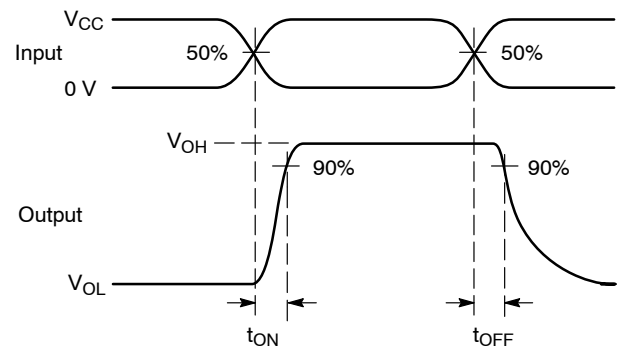
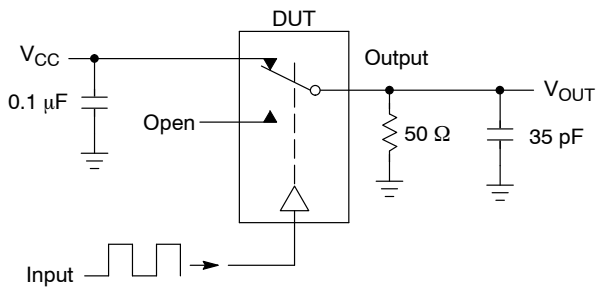


Figure 7. t_{ON}/t_{OFF}

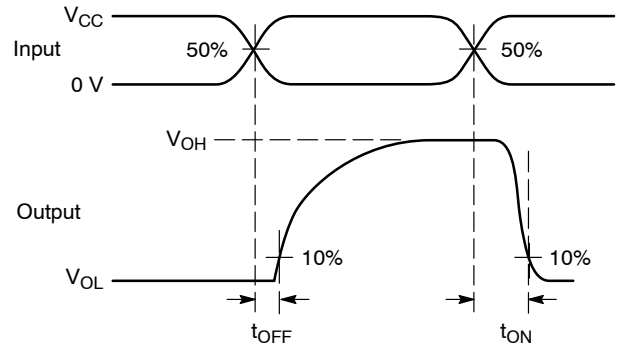
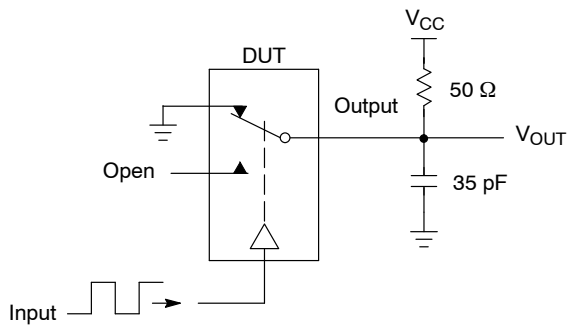
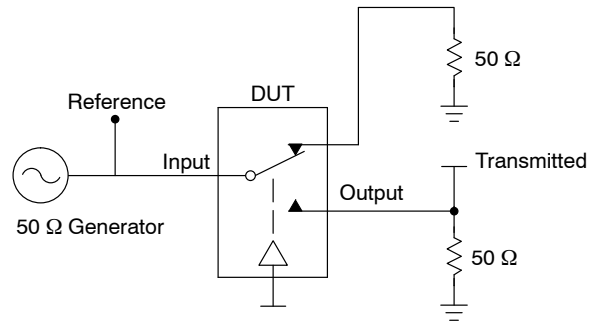


Figure 8. t_{ON}/t_{OFF}



Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch. V_{ISO} , Bandwidth and V_{ONL} are independent of the input signal direction.

$$V_{ISO} = \text{Off Channel Isolation} = 20 \text{ Log} \left(\frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz}$$

$$V_{ONL} = \text{On Channel Loss} = 20 \text{ Log} \left(\frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz to } 50 \text{ MHz}$$

Bandwidth (BW) = the frequency 3 dB below V_{ONL}

V_{CT} = Use V_{ISO} setup and test to all other switch analog input/outputs terminated with 50 Ω

Figure 9. Off Channel Isolation/On Channel Loss (BW)/Crosstalk (On Channel to Off Channel)/ V_{ONL}

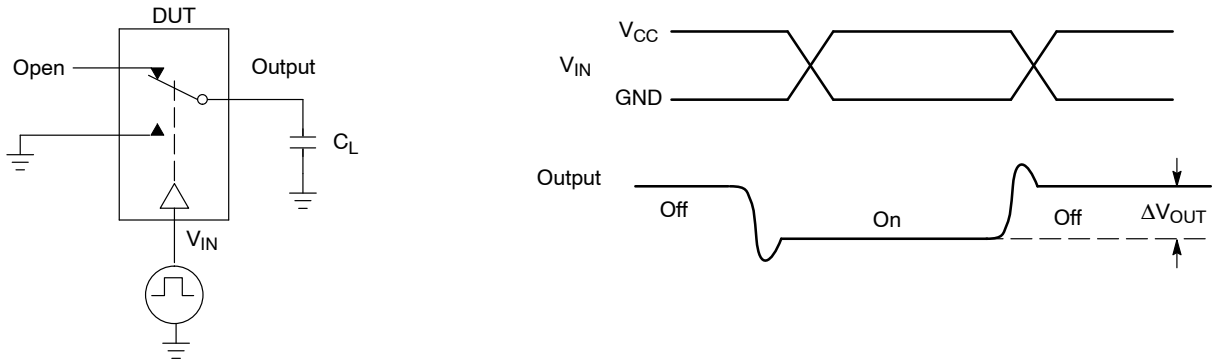


Figure 10. Charge Injection: (Q)

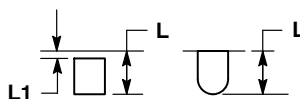
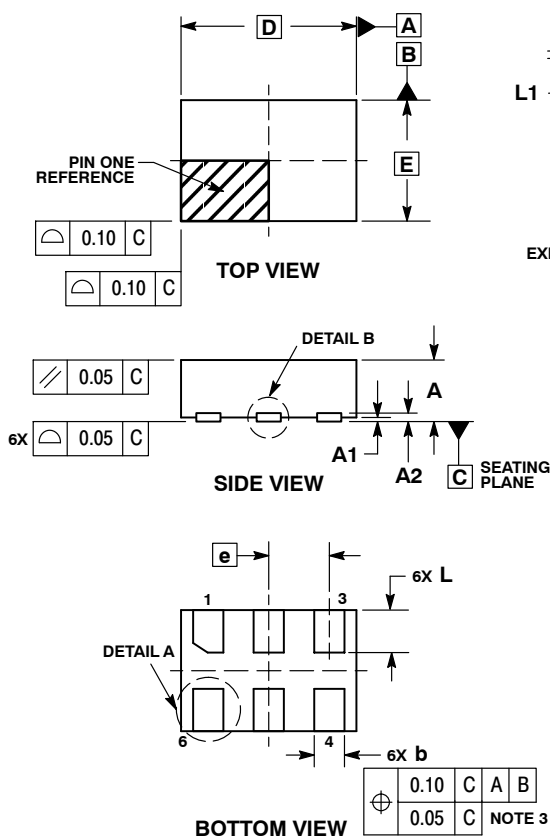
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



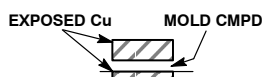
SCALE 4:1

UDFN6, 1.45x1.0, 0.5P
CASE 517AQ
ISSUE O

DATE 15 MAY 2008



DETAIL A
OPTIONAL
CONSTRUCTIONS



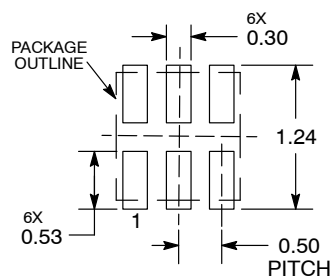
DETAIL B
OPTIONAL
CONSTRUCTIONS

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A2	0.07	REF
b	0.20	0.30
D	1.45	BSC
E	1.00	BSC
e	0.50	BSC
L	0.30	0.40
L1	---	0.15

MOUNTING FOOTPRINT



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



X = Specific Device Code
M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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