

74LVC827A

10-bit buffer/line driver with 5 V tolerant inputs/outputs;
3-state

Rev. 4 — 25 November 2011

Product data sheet

1. General description

The 74LVC827A is a 10-bit buffer/line driver with 3-state outputs. The 3-state outputs are controlled by the output enable pins $\overline{OE}1$ and $\overline{OE}2$. A HIGH on pin $\overline{OEn}$ causes the outputs to assume a high-impedance OFF-state.

Inputs can be driven from either 3.3 V or 5 V devices. When disabled, up to 5.5 V can be applied to the outputs. These features allow the use of these devices as translators in mixed 3.3 V and 5 V applications.

2. Features and benefits

- 5 V tolerant inputs/outputs for interfacing with 5 V logic
- Wide supply voltage range from 1.2 V to 3.6 V
- CMOS low power consumption
- Direct interface with TTL levels
- Complies with JEDEC standard:
 - ◆ JESD8-7A (1.65 V to 1.95 V)
 - ◆ JESD8-5A (2.3 V to 2.7 V)
 - ◆ JESD8-C/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - ◆ HBM JESD22-A114F exceeds 2000 V
 - ◆ MM JESD22-A115B exceeds 200 V
 - ◆ CDM JESD22-C101E exceeds 1000 V
- Specified from $-40\text{ }^{\circ}\text{C}$ to $+85\text{ }^{\circ}\text{C}$ and $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$

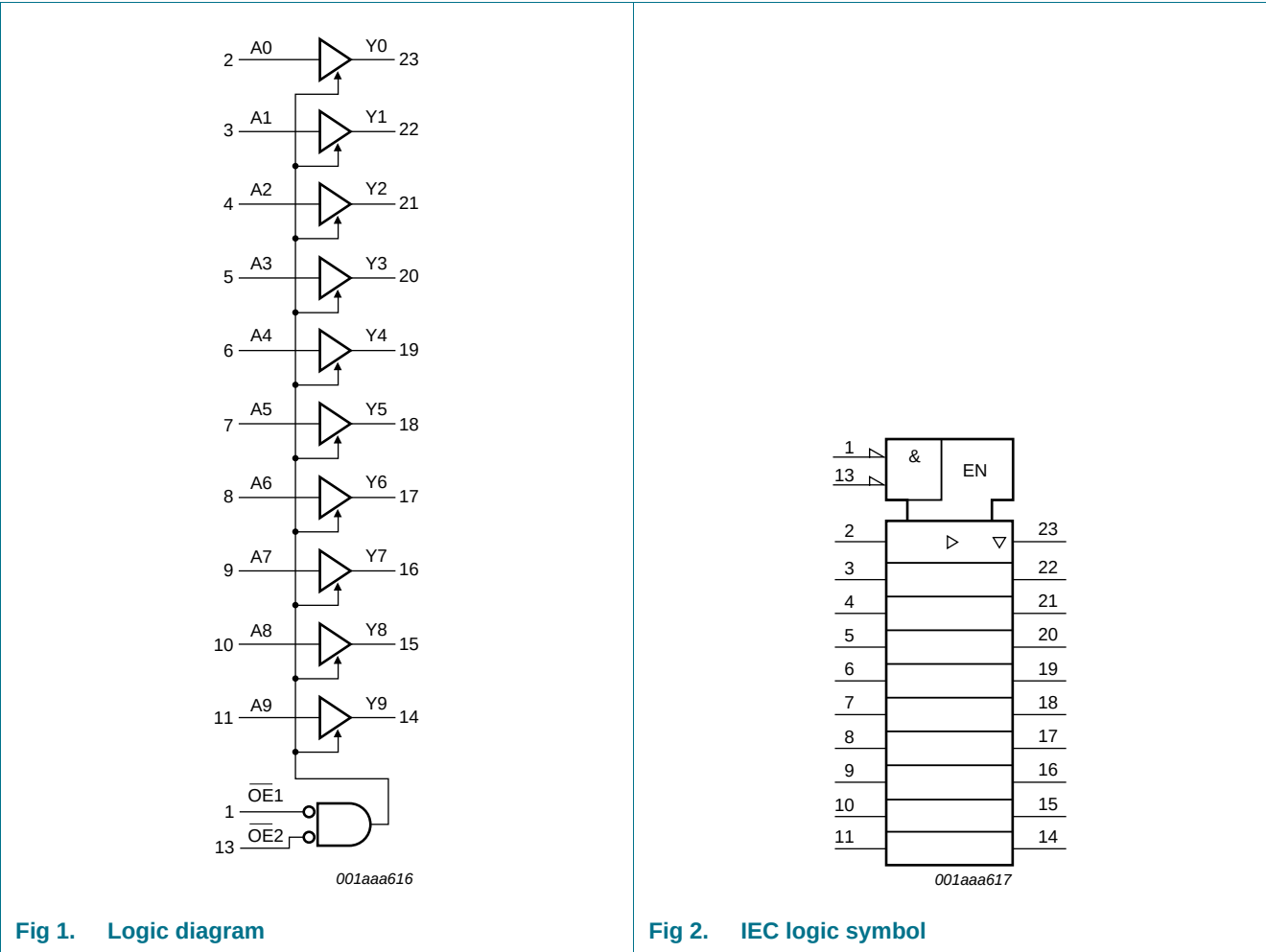
3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74LVC827AD	$-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$	SO24	plastic small outline package; 24 leads; body width 7.5 mm	SOT137-1
74LVC827ADB	$-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$	SSOP24	plastic shrink small outline package; 24 leads; body width 5.3 mm	SOT340-1
74LVC827APW	$-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$	TSSOP24	plastic thin shrink small package outline package; 24 leads; body width 4.4 mm	SOT355-1



4. Functional diagram



5. Pinning information

5.1 Pinning

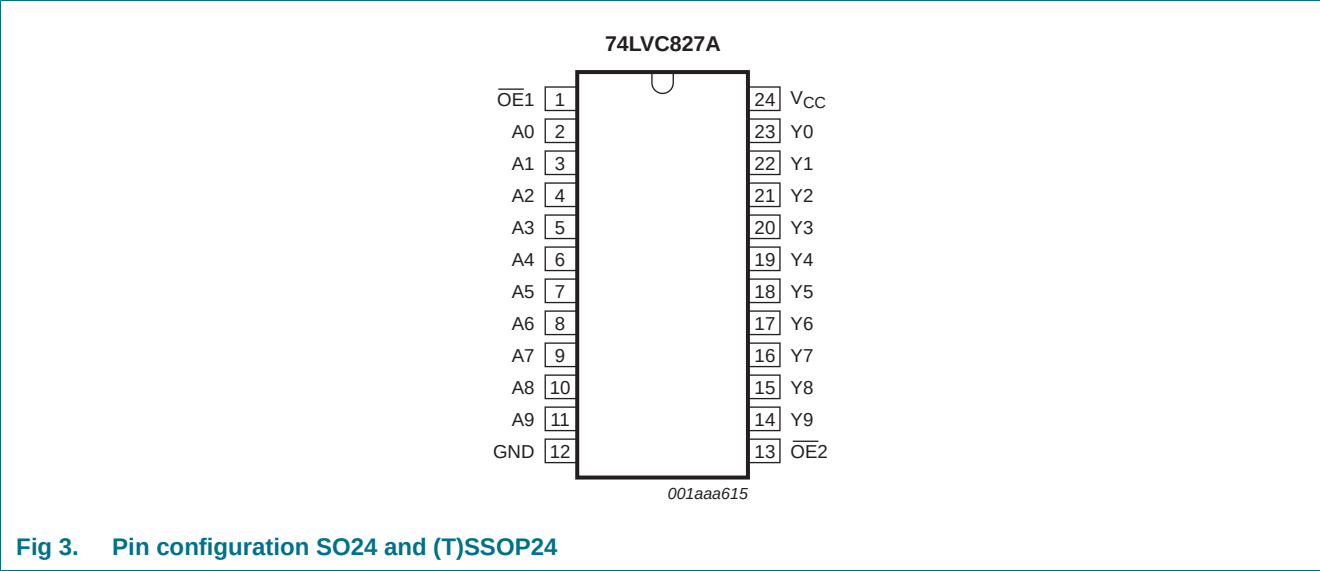


Fig 3. Pin configuration SO24 and (T)SSOP24

5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
OE1	1	output enable input 1 (active LOW)
OE2	13	output enable input 2 (active LOW)
A[0:9]	2, 3, 4, 5, 6, 7, 8, 9, 10, 11	data input
Y[0:9]	23, 22, 21, 20, 19, 18, 17, 16, 15, 14	data output
GND	12	ground (0 V)
VCC	24	supply voltage

6. Functional description

Table 3. Function table^[1]

Control		Input	Output
OE1	OE2	An	Yn
L	L	L	L
L	L	H	H
X	H	X	Z
H	X	X	Z

- [1] H = HIGH voltage level
 L = LOW voltage level
 X = don't care
 Z = high-impedance OFF-state

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+6.5	V
V _I	input voltage		^[1] -0.5	+6.5	V
V _O	output voltage	output HIGH or LOW state	^[2] -0.5	V _{CC} + 0.5	V
		output 3-state	^[2] -0.5	+6.5	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
I _{OK}	output clamping current	V _O > V _{CC} or V _O < 0 V	-	±50	mA
I _O	output current	V _O = 0 V to V _{CC}	-	±50	mA
I _{CC}	supply current		-	100	mA
I _{GND}	ground current		-100	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	^[3]	500	mW

- [1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.
 [2] The output voltage ratings may be exceeded if the output current ratings are observed.
 [3] SO24 package: P_{tot} derates linearly with 8 mW/K above 70 °C.
 (T)SSOP24 package: P_{tot} derates linearly with 5.5 mW/K above 60 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		1.65	3.6	V
		functional	1.2		V
V _I	input voltage		0	5.5	V
V _O	output voltage	output HIGH or LOW state	0	V _{CC}	V
		output 3-state	0	5.5	V
T _{amb}	ambient temperature		−40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 1.65 V to 2.7 V	0	20	ns/V
		V _{CC} = 2.7 V to 3.6 V	0	10	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	−40 °C to +85 °C			−40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 1.2 V	1.08	-	-	1.08	-	V
		V _{CC} = 1.65 V to 1.95 V	0.65 × V _{CC}	-	-	0.65 × V _{CC}	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	1.7	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.2 V	-	-	0.12	-	0.12	V
		V _{CC} = 1.65 V to 1.95 V	-	-	0.35 × V _{CC}	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}						
		I _O = −100 μA; V _{CC} = 1.65 V to 3.6 V	V _{CC} − 0.2	-	-	V _{CC} − 0.3	-	V
		I _O = −4 mA; V _{CC} = 1.65 V	1.2	-	-	1.05	-	V
		I _O = −8 mA; V _{CC} = 2.3 V	1.8	-	-	1.65	-	V
		I _O = −12 mA; V _{CC} = 2.7 V	2.2	-	-	2.05	-	V
		I _O = −18 mA; V _{CC} = 3.0 V	2.4	-	-	2.25	-	V
		I _O = −24 mA; V _{CC} = 3.0 V	2.2	-	-	2.0	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}						
		I _O = 100 μA; V _{CC} = 1.65 V to 3.6 V	-	-	0.2	-	0.3	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.45	-	0.65	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.6	-	0.8	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.4	-	0.6	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.55	-	0.8	V
I _I	input leakage current	V _{CC} = 3.6 V; V _I = 5.5 V or GND	-	±0.1	±5	-	±20	μA

Table 6. Static characteristics ...continued

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	–40 °C to +85 °C			–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _{CC} = 3.6 V; V _O = 5.5 V or GND;	-	0.1	±5	-	±20	μA
I _{OFF}	power-off leakage current	V _{CC} = 0 V; V _I or V _O = 5.5 V	-	0.1	±10	-	±20	μA
I _{CC}	supply current	V _{CC} = 3.6 V; V _I = V _{CC} or GND; I _O = 0 A	-	0.1	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; V _{CC} = 2.7 V to 3.6 V; V _I = V _{CC} – 0.6 V; I _O = 0 A	-	5	500	-	5000	μA
C _I	input capacitance	V _{CC} = 0 V to 3.6 V; V _I = GND to V _{CC}	-	5.0	-	-	-	pF

[1] All typical values are measured at V_{CC} = 3.3 V (unless stated otherwise) and T_{amb} = 25 °C.

10. Dynamic characteristics

Table 7. Dynamic characteristicsVoltages are referenced to GND (ground = 0 V). For test circuit see [Figure 6](#).

Symbol	Parameter	Conditions	T _{amb} = –40 °C to +85 °C			–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
t _{pd}	propagation delay	An to Y _n ; see Figure 4 ^[2]						
		V _{CC} = 1.2 V	-	15	-	-	-	ns
		V _{CC} = 1.65 V to 1.95 V	1.5	6.4	15.5	1.5	17.9	ns
		V _{CC} = 2.3 V to 2.7 V	1.0	3.4	8.0	1.0	9.3	ns
		V _{CC} = 2.7 V	1.5	3.4	7.1	1.5	9.0	ns
t _{en}	enable time	O _{En} to Y _n ; see Figure 5 ^[2]						
		V _{CC} = 1.2 V	-	20	-	-	-	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	7.9	16.7	1.8	19.3	ns
		V _{CC} = 2.3 V to 2.7 V	1.5	4.4	9.2	1.5	10.6	ns
		V _{CC} = 2.7 V	1.5	4.5	8.5	1.5	11.0	ns
t _{dis}	disable time	O _{En} to Y _n ; see Figure 5 ^[2]						
		V _{CC} = 1.2 V	-	10.0	-	-	-	ns
		V _{CC} = 1.65 V to 1.95 V	2.5	4.3	11.3	2.5	13.0	ns
		V _{CC} = 2.3 V to 2.7 V	1.0	2.4	6.4	1.0	7.4	ns
		V _{CC} = 2.7 V	1.5	3.2	7.3	1.5	9.5	ns
t _{sk(o)}	output skew time	V _{CC} = 3.0 V to 3.6 V ^[3]	-	-	1.0	-	1.5	ns

Table 7. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V). For test circuit see [Figure 6](#).

Symbol	Parameter	Conditions	T _{amb} = –40 °C to +85 °C			–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	
C _{PD}	power dissipation capacitance	per input; V _I = GND to V _{CC} ^[4]						
		V _{CC} = 1.65 V to 1.95 V	-	5.5	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	8.8	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	11.7	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.2 V, 1.8 V, 2.5 V, 2.7 V, and 3.3 V respectively.

[2] t_{pd} is the same as t_{PLH} and t_{PHL}.

t_{en} is the same as t_{PZL} and t_{PZH}.

t_{dis} is the same as t_{PLZ} and t_{PHZ}.

[3] Skew between any two outputs of the same package switching in the same direction. This parameter is guaranteed by design.

[4] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz; f_o = output frequency in MHz

C_L = output load capacitance in pF

V_{CC} = supply voltage in Volts

N = number of inputs switching

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs

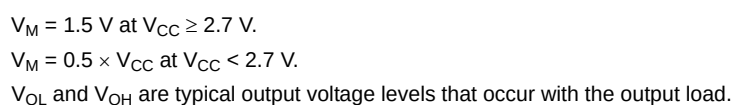


Fig 4. Propagation delay input (An) to output (Yn)

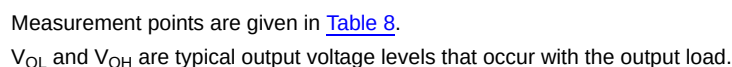
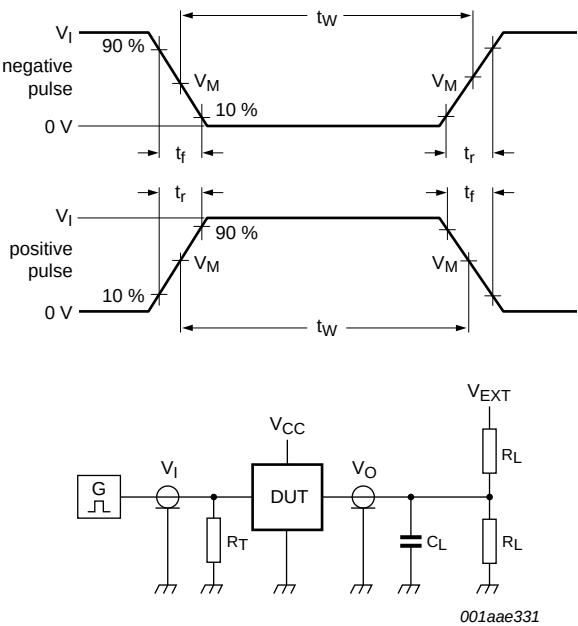


Fig 5. 3-state enable and disable times

Supply voltage	Input	Output		
V _{CC}	V _M	V _M	V _X	V _Y
< 2.7 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{OL} + 0.1 V	V _{OH} − 0.1 V
≥ 2.7 V	1.5 V	1.5 V	V _{OL} + 0.3 V	V _{OH} − 0.3 V



Test data is given in [Table 9](#).
Definitions for test circuit:
 R_L = Load resistance.
 C_L = Load capacitance including jig and probe capacitance.
 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.
 V_{EXT} = External voltage for measuring switching times.

Fig 6. Test circuit for measuring switching times

Table 9. Test data

Supply voltage	Input		Load		V _{EXT}		
	V _I	t _r , t _f	C _L	R _L	t _{PLH} , t _{PHL}	t _{PLZ} , t _{PZL}	t _{PHZ} , t _{PZH}
1.2 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ	open	2 × V _{CC}	GND
1.65 V to 1.95 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ	open	2 × V _{CC}	GND
2.3 V to 2.7 V	V _{CC}	≤ 2 ns	30 pF	500 Ω	open	2 × V _{CC}	GND
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	2 × V _{CC}	GND
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	2 × V _{CC}	GND

12. Package outline

SO24: plastic small outline package; 24 leads; body width 7.5 mm

SOT137-1

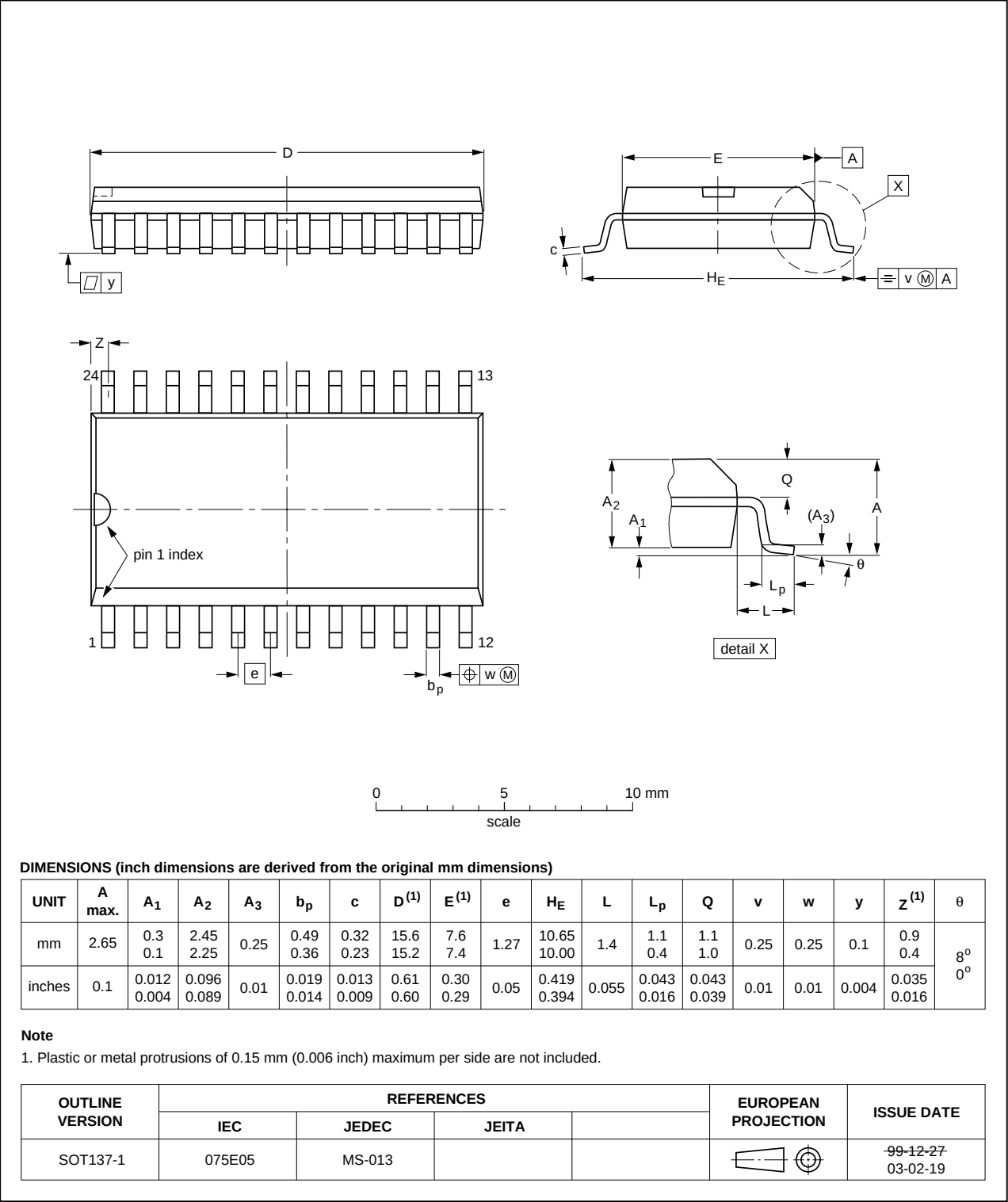


Fig 7. Package outline SOT137-1 (SO24)

SSOP24: plastic shrink small outline package; 24 leads; body width 5.3 mm

SOT340-1

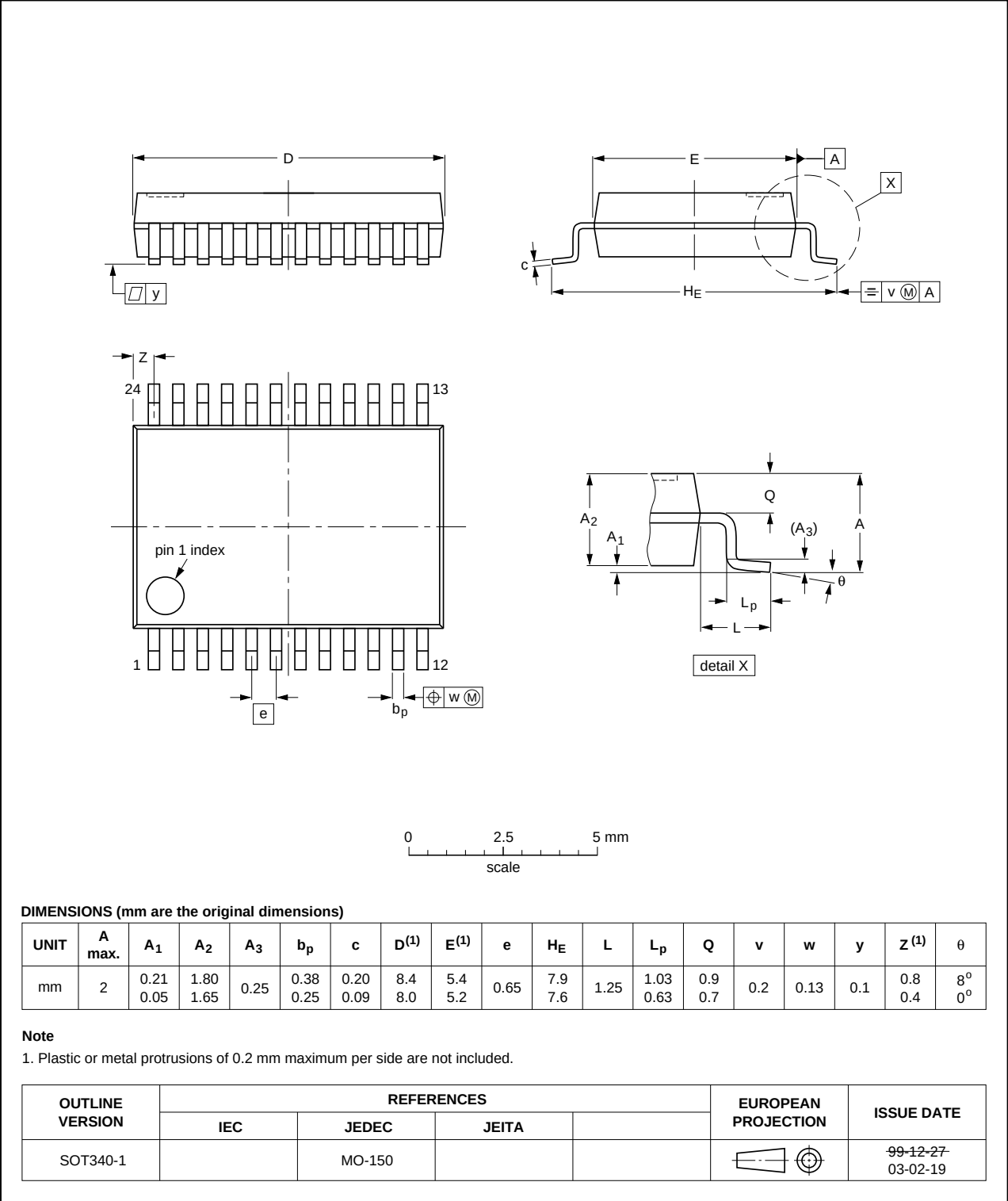


Fig 8. Package outline SOT340-1 (SSOP24)

TSSOP24: plastic thin shrink small outline package; 24 leads; body width 4.4 mm

SOT355-1

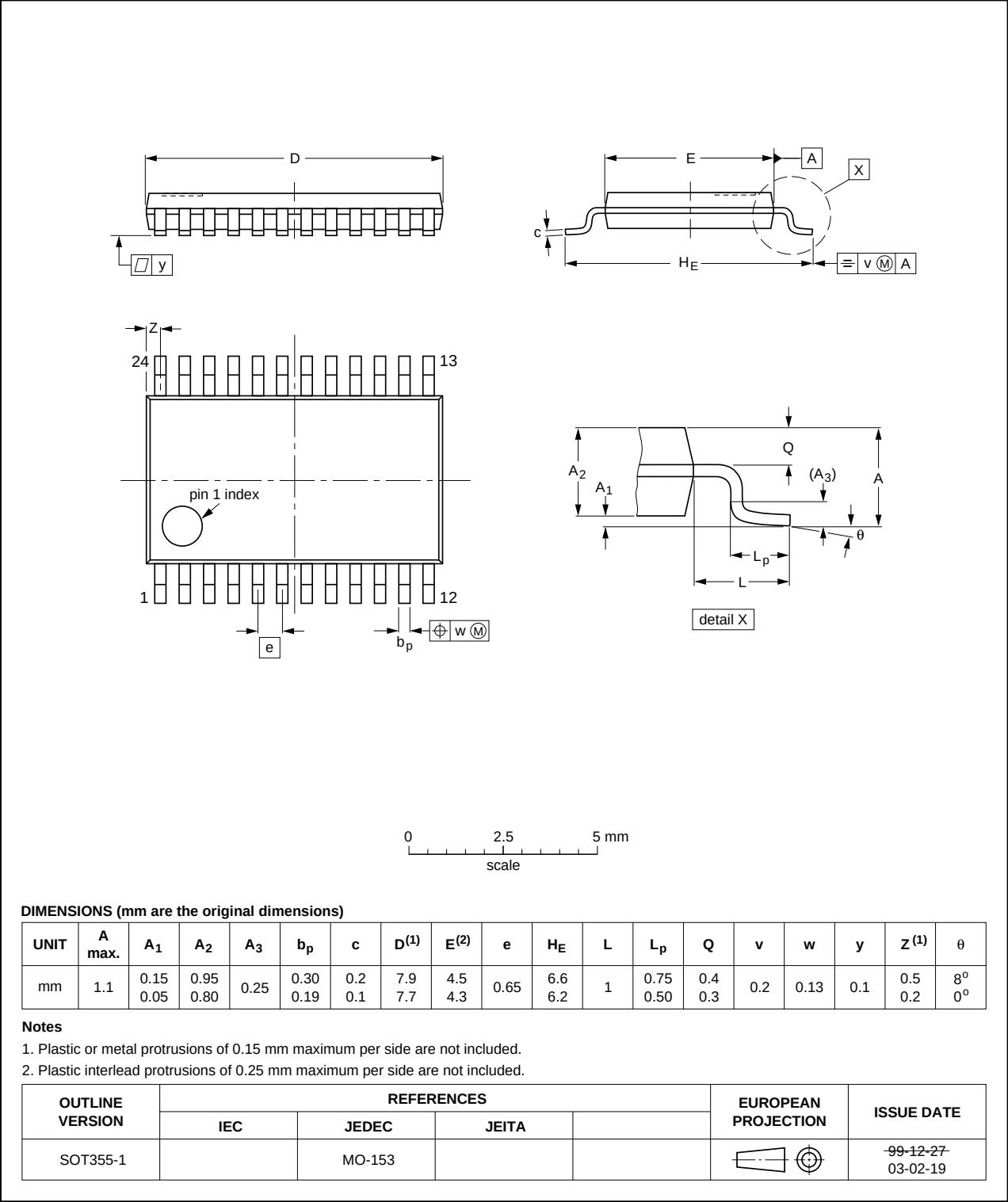


Fig 9. Package outline SOT355-1 (TSSOP24)

13. Abbreviations

Table 10. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LVC827A v.4	20111125	Product data sheet	-	74LVC827A v.3
Modifications:	- Value changes for t_{pd}, t_{en} and t_{dis} in Table 7 "Dynamic characteristics" - Corrected typographical errors			
74LVC827A v.3	20111103	Product data sheet	-	74LVC827A v.2
Modifications:	- The format of this document has been redesigned to comply with the new identity guidelines of NXP Semiconductors. - Legal texts have been adapted to the new company name where appropriate. Table 4, Table 5, Table 6, Table 7, and Table 9: values added for lower voltage ranges. - Added: type number 74LVC827ABQ (DHVQFN24 package)			
74LVC827A v.2	20040408	Product specification	-	74LVC827A v.1
74LVC827A v.1	19980904	Product specification	-	-

15. Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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[2] The term 'short data sheet' is explained in section "Definitions".

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17. Contents

1	General description	1
2	Features and benefits	1
3	Ordering information	1
4	Functional diagram	2
5	Pinning information	3
5.1	Pinning	3
5.2	Pin description	3
6	Functional description	4
7	Limiting values	4
8	Recommended operating conditions	5
9	Static characteristics	5
10	Dynamic characteristics	6
11	Waveforms	8
12	Package outline	10
13	Abbreviations	13
14	Revision history	13
15	Legal information	14
15.1	Data sheet status	14
15.2	Definitions	14
15.3	Disclaimers	14
15.4	Trademarks	15
16	Contact information	15
17	Contents	16

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