

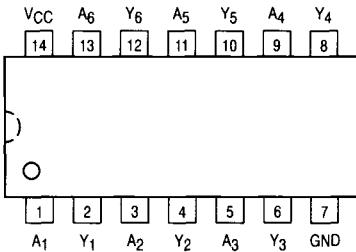
**MOTOROLA****MC74AC05
MC74ACT05**

Hex Inverter With Open-Drain Outputs

The MC74AC/ACT05 is identical in pinout to the LS05. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with TTL outputs.

- Outputs Source/Sink 24 mA
- 'ACT05 Has TTL Compatible Inputs

Pinout: 14-Lead Packages (Top View)



FUNCTION TABLE

Input A	Output Y
L	Z
H	L

Z = High Impedance

HEX INVERTER WITH
OPEN-DRAIN OUTPUTS



N SUFFIX
CASE 646-06
PLASTIC PACKAGE



D SUFFIX
CASE 751A-03
PLASTIC PACKAGE

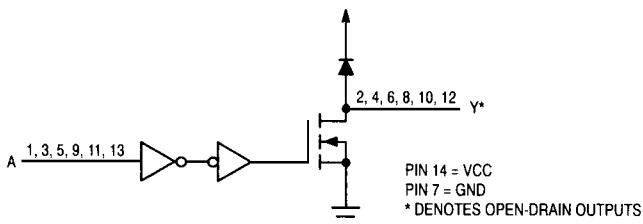
MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
V _{in}	DC Input Voltage (Referenced to GND)	-0.5 to V _{CC} + 0.5	V
V _{out}	DC Output Voltage (Referenced to GND)	-0.5 to V _{CC} + 0.5	V
I _{in}	DC Input Current, per Pin	± 20	mA
I _{out}	DC Output Sink/Source Current, per Pin	± 50	mA
I _{CC}	DC V _{CC} or GND Current per Output Pin	± 50	mA
T _{tsg}	Storage Temperature	-65 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

MC74AC05 • MC74ACT05

LOGIC DIAGRAM



RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Typ	Min	Unit
VCC	Supply Voltage	'AC	2.0	5.0	6.0	V
		'ACT	4.5	5.0	5.5	
VREG	DC Regulated Power Voltage (Ref. to GND)		0		V _{CC}	V
t _r , t _f	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V _{CC} @ 3.0 V		150		ns/V
		V _{CC} @ 4.5 V		40		
		V _{CC} @ 5.5 V		25		
t _r , t _f	Input Rise and Fall Time (Note 2) 'ACT Devices except Schmitt Inputs	V _{CC} @ 4.5 V		10		ns/V
		V _{CC} @ 5.5 V		8.0		
T _J	Junction Temperature (PDIP)				140	°C
T _A	Operating Ambient Temperature Range		-40	25	85	°C
I _{OH}	Output Current — HIGH				-24	mA
I _{OL}	Output Current — LOW				24	mA

1. V_{in} from 30% to 70% V_{CC}; see individual Data Sheets for devices that differ from the typical input rise and fall times.

2. V_{in} from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

MC74AC05 • MC74ACT05

DC CHARACTERISTICS

Symbol	Parameter	V _{CC} (V)	74AC		74AC		Unit	Conditions		
			T _A = +25°C		T _A = -40°C to +85°C					
			Typ	Guaranteed Limits						
V _{IH}	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85		V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V		
V _{IL}	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65		V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V		
V _{OL}	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1		V	I _{OUT} = 50 μA		
		3.0 4.5 5.5		0.36 0.36 0.36	0.44 0.44 0.44		V	*V _{IN} = V _{IL} or V _{IH} 12 mA I _{OL} 24 mA 24 mA		
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	±1.0		μA	V _I = V _{CC} , GND		
I _{OLD}	†Minimum Dynamic Output Current	5.5			75		mA	V _{OLD} = 1.65 V Max		
I _{OHD}		5.5			-75		mA	V _{OH} = 3.85 V Min		
I _{CC}	Maximum Quiescent Supply Current	5.5		4.0	40		μA	V _{IN} = V _{CC} or GND		

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

Note: I_{IN} and I_{CC} @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V.

5

AC CHARACTERISTICS

Symbol	Parameter	V _{CC} * (V)	74AC			74AC		Unit	
			T _A = +25°C C _L = 50 pF			T _A = -40°C to +85°C C _L = 50 pF			
			Min	Typ	Max	Min	Max		
t _{PZL}	Propagation Delay Output Enable	3.3 5.0	1.5 1.5		8.0 6.0	1.0 1.0	9.0 6.5	ns	
t _{PLZ}	Propagation Delay Output Enable	3.3 5.0	1.5 1.5		8.0 6.0	1.0 1.0	9.0 6.5	ns	

* Voltage Range 3.3 V is 3.3 V ±0.3 V.

Voltage Range 5.0 V is 5.0 V ±0.5 V.

MC74AC05 • MC74ACT05

DC CHARACTERISTICS

Symbol	Parameter	V_{CC} (V)	74ACT		74ACT	Unit	Conditions
			$T_A = +25^\circ C$		$T_A = -40^\circ C \text{ to } +85^\circ C$		
			Typ	Guaranteed Limits			
V_{IH}	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$
V_{IL}	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$
V_{OL}	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V	$I_{OUT} = 50 \mu A$
		4.5 5.5		0.36 0.36	0.44 0.44	V	$*V_{IN} = V_{IL} \text{ or } V_{IH}$ $ I_{OH} = 24 mA$ $ I_{OL} = 24 mA$
I_{IN}	Maximum Input Leakage Current	5.5		± 0.1	± 1.0	μA	$V_I = V_{CC}, GND$
ΔI_{CCT}	Additional Max. I_{CC} /Input	5.5	0.6		1.5	mA	$V_I = V_{CC} - 2.1 V$
I_{OLD}	†Minimum Dynamic Output Current	5.5			75	mA	$V_{OLD} = 1.65 V$ Max
		5.5			-75	mA	$V_{OHD} = 3.85 V$ Min
I_{CC}	Maximum Quiescent Supply Current	5.5		4.0	40	μA	$V_{IN} = V_{CC} \text{ or } GND$

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms. one output loaded at a time.

AC CHARACTERISTICS

Symbol	Parameter	V_{CC}^* (V)	74ACT			74ACT	Unit
			$T_A = +25^\circ C$ $C_L = 50 pF$		$T_A = -40^\circ C$ $to +85^\circ C$ $C_L = 50 pF$		
			Min	Typ	Max	Min	
t_{PZL}	Propagation Delay Output Enable	5.0	1.5		8.0	1.0	8.5 ns
t_{PLZ}	Propagation Delay Output Enable	5.0	1.5		8.5	1.0	9.0 ns

* Voltage Range 5.0 V is 5.0 V ± 0.5 V.

CAPACITANCE

Symbol	Parameter	Value Typ	Unit	Test Conditions
C_{IN}	Input Capacitance	4.5	pF	$V_{CC} = 5.0 V$
CPD	Power Dissipation Capacitance	30	pF	$V_{CC} = 5.0 V$