

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL00AA is a 2-input CMOS NAND Gate, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UL00AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 2.6ns (TYP.)

Operating Voltage Range : 2V ~ 5.5V

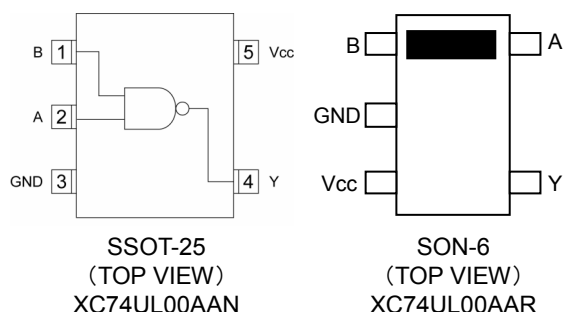
Low Power Consumption: 1 μ A (MAX.)

CMOS 2-Input NAND Gate

Ultra Small Package : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



■ FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Voltage	Vcc	-0.5~+6.0	V
Input Voltage	Vin	-0.5~+6.0	V
Output Voltage	Vout	-0.5~Vcc+0.5	V
Input Diode Current	Iik	-20	mA
Output Diode Current	Iok	±20	mA
Output Current	Iout	±25	mA
Vcc,GND Current	Icc,IGND	±50	mA
Power Dissipation	SSOT-25*1	150	mW
	SON-6*2	200	
Storage Temperature Range	Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	Topr	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	tr,tf	3.3	0~100	ns/V
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

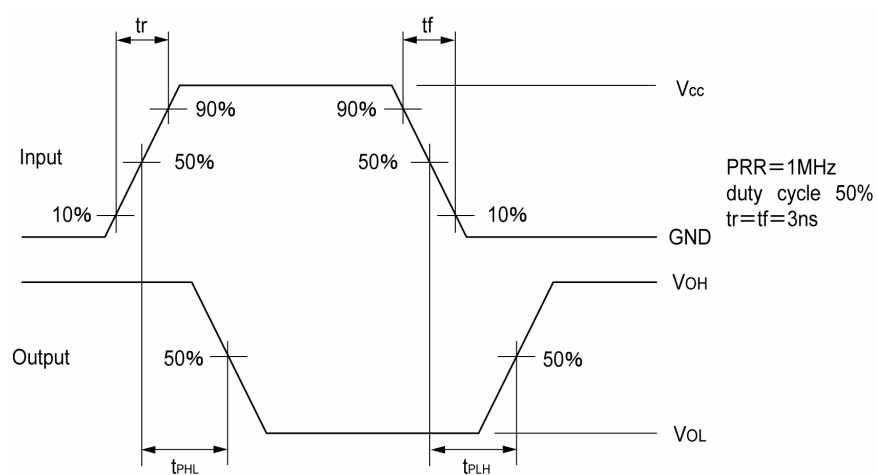
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IH} or V _{IL}	I _{OH} =-50 μA	1.9	2.0	—	1.9	V
		3.0			2.9	3.0	—	2.9	
		4.5			4.4	4.5	—	4.4	
		3.0		I _{OH} =-4mA	2.58	—	—	2.48	
		4.5		I _{OH} =-8mA	3.94	—	—	3.80	
	V _{OL}	2.0	V _{IN} =V _{IH}	I _{OL} =50 μA	—	—	0.1	—	V
		3.0			—	—	0.1	—	
		4.5			—	—	0.1	—	
		3.0		I _{OL} =4mA	—	—	0.36	—	
		4.5		I _{OL} =8mA	—	—	0.36	—	
Input Current	I _{IN}	0~5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

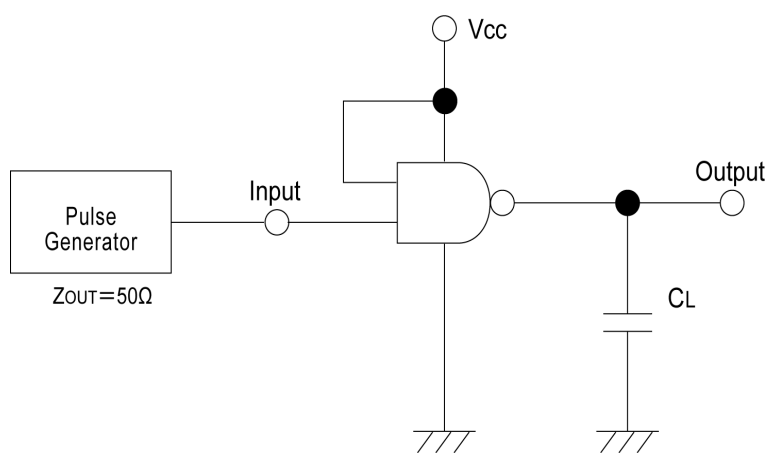
tr=tf=3ns

PARAMETER	SYMBOL	C _L	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	3.7	7.9	1.0	9.5	ns
			5.0		—	2.7	5.5	1.0	6.5	
		50pF	3.3		—	5.4	11.4	1.0	13.0	ns
			5.0		—	3.6	7.5	1.0	8.5	
	t _{PHL}	15pF	3.3		—	3.3	7.9	1.0	9.5	ns
			5.0		—	2.5	5.5	1.0	6.5	
		50pF	3.3		—	4.6	11.4	1.0	13.0	ns
			5.0		—	3.5	7.5	1.0	8.5	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	2	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	9.3	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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TOREX SEMICONDUCTOR LTD.

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL02AA is a 2-input CMOS NOR Gate, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UL02AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 2.65ns (TYP.)

Operating Voltage Range : 2V ~ 5.5V

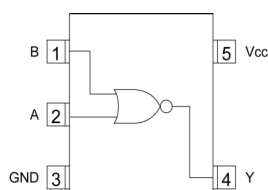
Low Power Consumption : 1 μ A (MAX.)

CMOS 2-Input NOR Gate

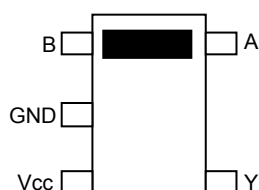
Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



SSOT-25
(TOP VIEW)
XC74UL02AAN



SON-6
(TOP VIEW)
XC74UL02AAR

■ FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	H
L	H	L
H	L	L
H	H	L

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER		SYMBOL	RATINGS	UNITS
Supply Voltage		VCC	-0.5~+6.0	V
Input Voltage		VIN	-0.5~+6.0	V
Output Voltage		VOUT	-0.5~VCC+0.5	V
Input Diode Current		I _{IK}	-20	mA
Output Diode Current		I _{OK}	±20	mA
Output Current		I _{OUT}	±25	mA
VCC,GND Current		I _{CC} ,I _{GND}	±50	mA
Power Dissipation	SSOT-25*1	Pd	150	mW
	SON-6*2		200	
Storage Temperature Range		Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	Topr	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	tr,tf	3.3	0~100	ns
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

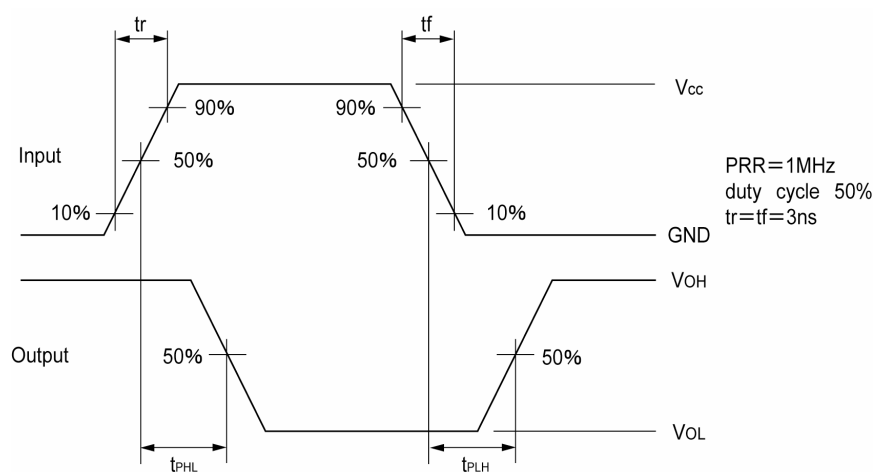
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IH}	I _{OH} =-50 μA	1.9	2.0	—	1.9	V
		3.0			2.9	3.0	—	2.9	
		4.5		I _{OH} =-4mA	4.4	4.5	—	4.4	
		3.0			2.58	—	—	2.48	
		4.5			3.94	—	—	3.80	
	V _{OL}	2.0	V _{IN} =V _{IH}	I _{OL} =50 μA	—	—	0.1	—	V
		3.0			—	—	0.1	—	
		4.5			—	—	0.1	—	
		3.0		I _{OL} =4mA	—	—	0.36	—	
		4.5		I _{OL} =8mA	—	—	0.36	—	
Input Current	I _{IN}	0~5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

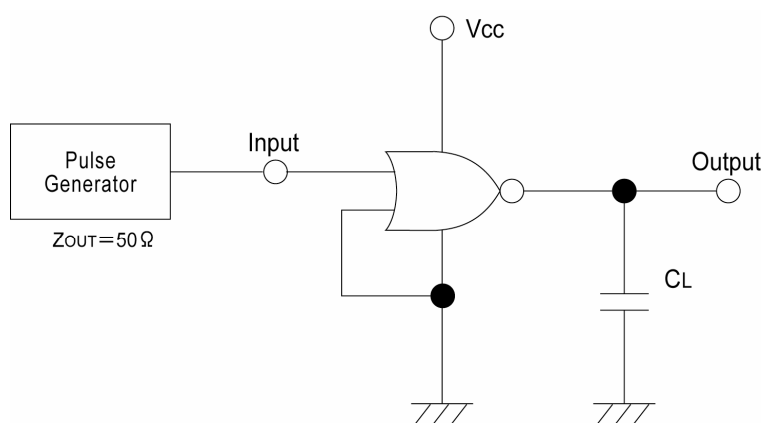
tr=tf=3ns

PARAMETER	SYMBOL	C _L	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	3.9	7.9	1.0	9.5	ns
			5.0		—	2.7	5.5	1.0	6.5	
		50pF	3.3		—	5.5	11.4	1.0	13.0	ns
			5.0		—	3.9	7.5	1.0	8.5	
	t _{PHL}	15pF	3.3		—	3.5	7.9	1.0	9.5	ns
			5.0		—	2.6	5.5	1.0	6.5	
		50pF	3.3		—	4.9	11.4	1.0	13.0	ns
			5.0		—	3.6	7.5	1.0	8.5	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	4	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	9.7	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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TOREX SEMICONDUCTOR LTD.

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL04AA is a CMOS inverter, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

The internal circuit is composed of inverter and buffer, which provide high noise immunity and stable output.

As the XC74UL04AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 2.05ns(TYP.)

Operating Voltage Range : 2V ~ 5.5V

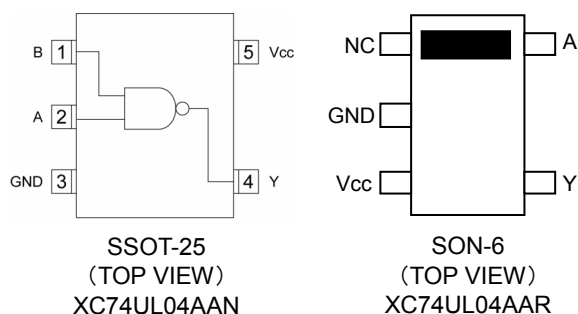
Low Power Consumption: 1 μ A (MAX.)

CMOS Inverter

Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



■ FUNCTIONS

INPUT	OUTPUT
A	Y
H	L
L	H

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Voltage	VCC	-0.5~+6.0	V
Input Voltage	VIN	-0.5~+6.0	V
Output Voltage	VOUT	-0.5~VCC+0.5	V
Input Diode Current	IiK	-20	mA
Output Diode Current	Iok	±20	mA
Output Current	IOUT	±25	mA
VCC,GND Current	ICC,IGND	±50	mA
Power Dissipation	SSOT-25* ¹	Pd	150
	SON-6* ²		200
Storage Temperature Range	Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	Topr	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	tr,tf	3.3	0~100	ns
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

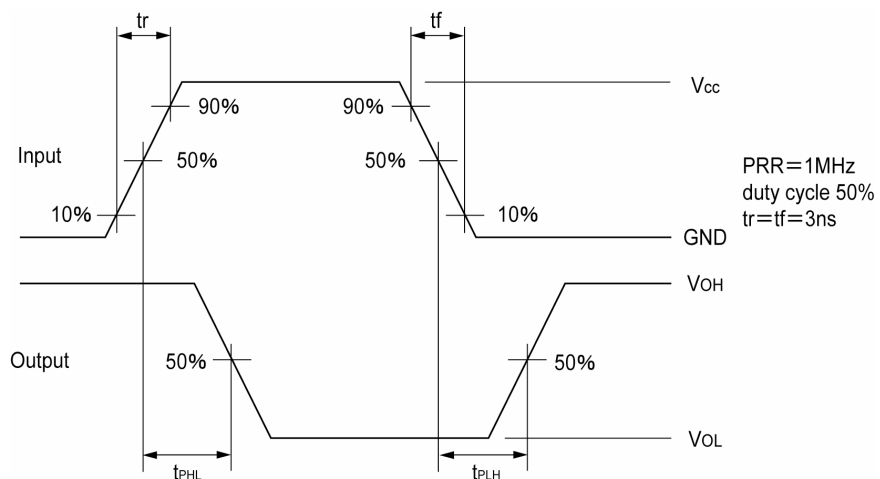
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IL}	1.9	2.0	—	1.9	—	V
		3.0		2.9	3.0	—	2.9	—	
		4.5		4.4	4.5	—	4.4	—	
		3.0	I _{OH} =-4mA	2.58	—	—	2.48	—	
		4.5		3.94	—	—	3.80	—	
		4.5	I _{OH} =-8mA	3.94	—	—	3.80	—	
	V _{OL}	2.0	V _{IN} =V _{IH}	—	—	0.1	—	0.1	V
		3.0		—	—	0.1	—	0.1	
		4.5		—	—	0.1	—	0.1	
		3.0	I _{OL} =50 μA	—	—	0.36	—	0.44	
		4.5		—	—	0.36	—	0.44	
		4.5	I _{OL} =8mA	—	—	0.36	—	0.44	
Input Current	I _{IN}	0~5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

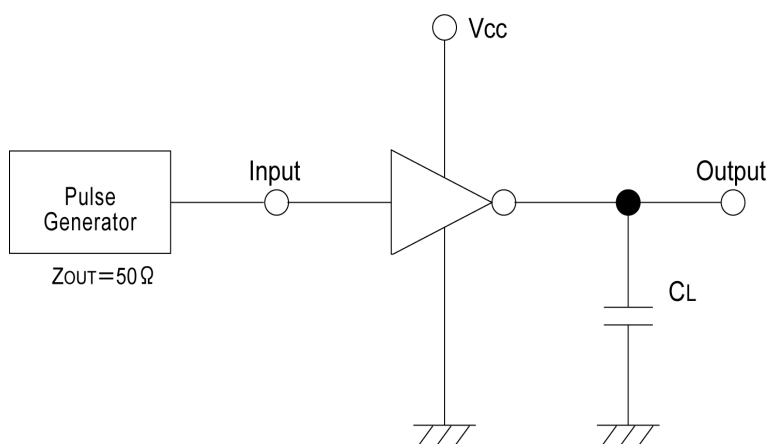
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PARAMETER	SYMBOL	CL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	2.7	7.1	1.0	8.5	ns
			5.0		—	2.1	5.5	1.0	6.5	
		50pF	3.3		—	4.1	10.6	1.0	12.0	ns
			5.0		—	3.2	7.5	1.0	8.5	
	t _{PHL}	15pF	3.3		—	2.5	7.1	1.0	8.5	ns
			5.0		—	2.0	5.5	1.0	6.5	
		50pF	3.3		—	3.9	10.6	1.0	12.0	ns
			5.0		—	3.0	7.5	1.0	8.5	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	2	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	8.9	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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■ APPLICATIONS

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- Digital equipment

■ FEATURES

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Operating Voltage Range : 2V ~ 5.5V

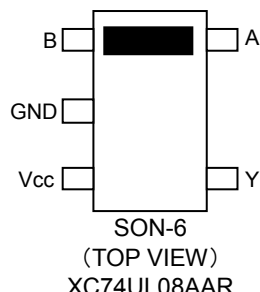
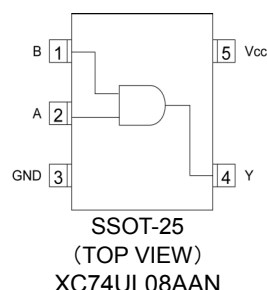
Low Power Consumption : 1 μ A (MAX.)

CMOS 2-Input AND Gate

Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



■ FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Voltage	VCC	-0.5~+6.0	V
Input Voltage	VIN	-0.5~+6.0	V
Output Voltage	VOUT	-0.5~VCC+0.5	V
Input Diode Current	I _{IK}	-20	mA
Output Diode Current	I _{OK}	±20	mA
Output Current	I _{OUT}	±25	mA
VCC,GND Current	I _{CC} ,I _{GND}	±50	mA
Power Dissipation	SSOT-25* ¹	Pd	mW
	SON-6* ²		
Storage Temperature Range	Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	Topr	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	tr,tf	3.3	0~100	ns
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

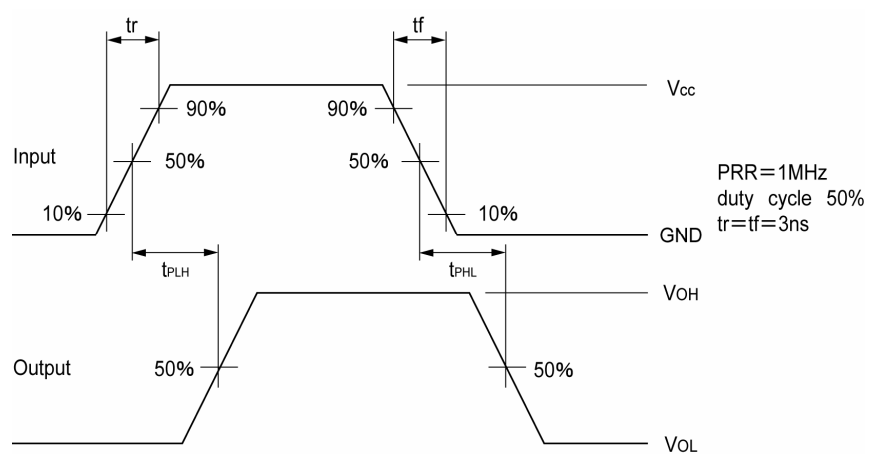
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IH}	I _{OH} =-50 μA	1.9	2.0	—	1.9	V
		3.0			2.9	3.0	—	2.9	
		4.5		I _{OH} =-4mA	4.4	4.5	—	4.4	
		3.0			2.58	—	—	2.48	
		4.5			3.94	—	—	3.80	
	V _{OL}	2.0	V _{IN} =V _{IH} or V _{IL}	I _{OL} =50 μA	—	—	0.1	—	V
		3.0			—	—	0.1	—	
		4.5			—	—	0.1	—	
		3.0		I _{OL} =4mA	—	—	0.36	—	
		4.5		I _{OL} =8mA	—	—	0.36	—	
Input Current	I _{IN}	5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

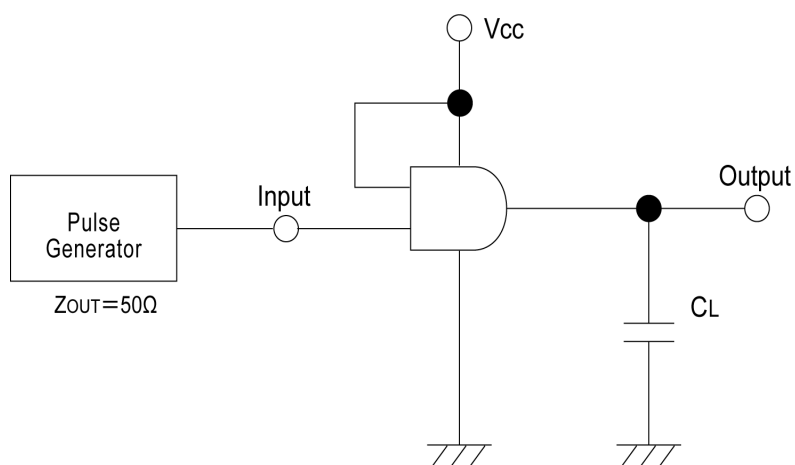
tr=tf=3ns

PARAMETER	SYMBOL	CL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	3.7	8.8	1.0	10.5	ns
			5.0		—	2.8	5.9	1.0	7.0	
		50pF	3.3		—	5.2	12.3	1.0	14.0	ns
			5.0		—	3.7	7.9	1.0	9.0	
	t _{PHL}	15pF	3.3		—	3.2	8.8	1.0	10.5	ns
			5.0		—	2.4	5.9	1.0	7.0	
		50pF	3.3		—	4.5	12.3	1.0	14.0	ns
			5.0		—	3.4	7.9	1.0	9.0	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	4	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	9.3	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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TOREX SEMICONDUCTOR LTD.

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL14AA is a CMOS schmitt trigger inverter, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UL14AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 2.3ns (TYP.)

Operating Voltage Range : 2V ~ 5.5V

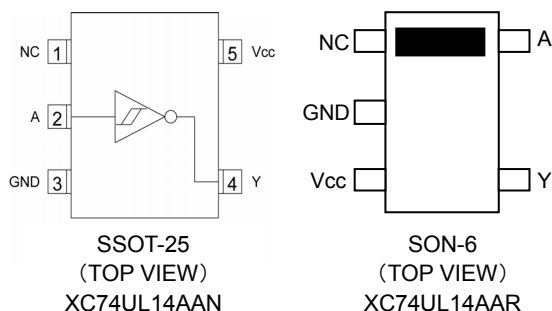
Low Power Consumption : 1 μ A (MAX.)

CMOS Schmitt Trigger Inverter

Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



■ FUNCTIONS

INPUT	OUTPUT
A	Y
H	L
L	H

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER		SYMBOL	RATINGS	UNITS
Supply Voltage		VCC	-0.5~+6.0	V
Input Voltage		VIN	-0.5~+6.0	V
Output Voltage		VOUT	-0.5~VCC+0.5	V
Input Diode Current		IiK	-20	mA
Output Diode Current		IOK	±20	mA
Output Current		IOUT	±25	mA
VCC,GND Current		ICC,IGND	±50	mA
Power Dissipation	SSOT-25*1	Pd	150	mW
	SON-6*2		200	
Storage Temperature Range		Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	Topr	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	

DC ELECTRICAL CHARACTERISTICS

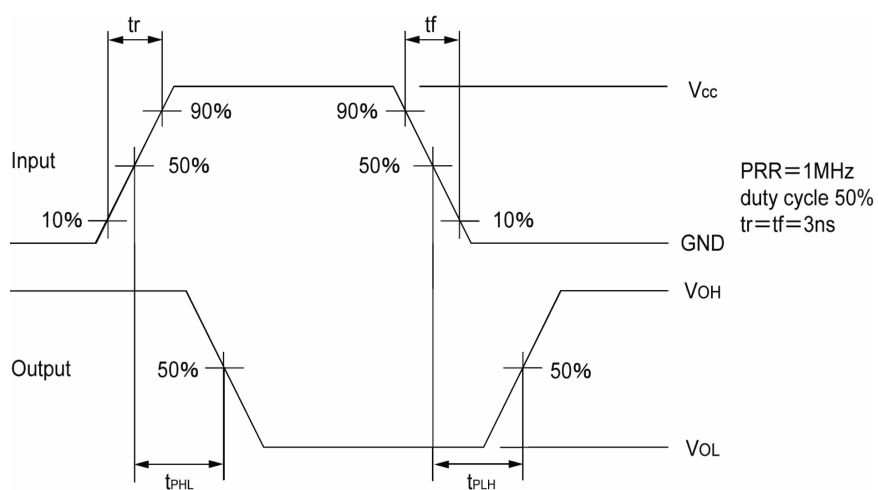
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS		Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Threshold Voltage	V _{T+}	2.0			—	—	2.2	—	2.2	V
		3.0			—	—	3.15	—	3.15	
		5.5			—	—	3.85	—	3.85	
	V _{T-}	2.0			0.9	—	—	0.9	—	V
		3.0			1.35	—	—	1.35	—	
		5.5			1.65	—	—	1.65	—	
Hysteresis Voltage	V _H	3.0			0.25	—	1.2	0.25	1.2	
		4.5			0.30	—	1.4	0.30	1.4	
		5.5			0.35	—	1.6	0.35	1.6	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IL}	I _{OH} =-50 μA	1.9	2.0	—	1.9	—	V
		3.0			2.9	3.0	—	2.9	—	
		4.5			4.4	4.5	—	4.4	—	
		3.0		I _{OH} =-4mA	2.58	—	—	2.48	—	
		4.5		I _{OH} =-8mA	3.94	—	—	3.80	—	
	V _{OL}	2.0	V _{IN} =V _{IH}	I _{OL} =50 μA	—	—	0.1	—	0.1	V
		3.0			—	—	0.1	—	0.1	
		4.5			—	—	0.1	—	0.1	
		3.0		I _{OL} =4mA	—	—	0.36	—	0.44	
		4.5		I _{OL} =8mA	—	—	0.36	—	0.44	
Input Current	I _{IN}	5.5	V _{IN} =V _{CC} or GND		-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA		—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

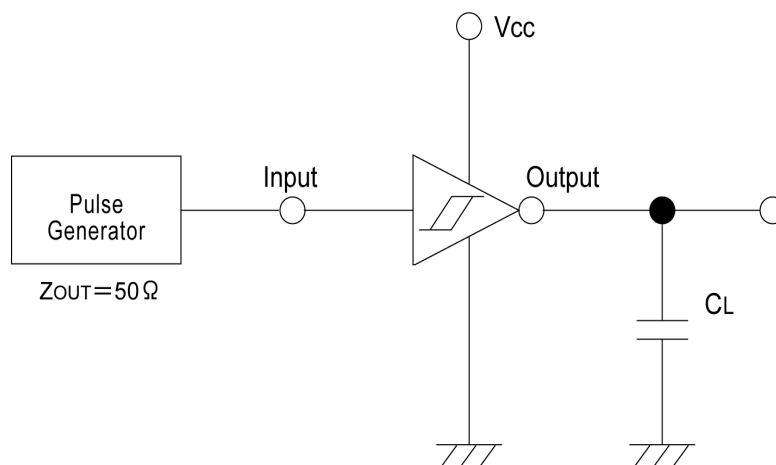
tr=tf=3ns

PARAMETER	SYMBOL	CL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	2.8	12.8	1.0	15.0	ns
			5.0		—	2.1	8.6	1.0	10.0	
		50pF	3.3		—	4.3	16.3	1.0	18.5	ns
			5.0		—	3.1	10.6	1.0	12.0	
	t _{PHL}	15pF	3.3		—	3.1	12.8	1.0	15.0	ns
			5.0		—	2.5	8.6	1.0	10.0	
		50pF	3.3		—	4.4	16.3	1.0	18.5	ns
			5.0		—	3.4	10.6	1.0	12.0	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	2	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	10	—	—	—	pF

WAVEFORM



TEST CIRCUIT



Note: Open output when measuring supply current

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TOREX SEMICONDUCTOR LTD.

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL32AA is a 2-input CMOS OR Gate, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UL32AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 3.8ns (TYP.)

Operating Voltage Range : 2V ~ 5.5V

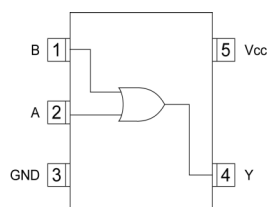
Low Power Consumption : 1 μ A (MAX.)

CMOS 2-Input OR Gate

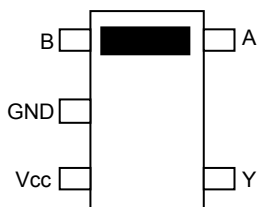
Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



SSOT-25
(TOP VIEW)
XC74UL32AAN



SON-6
(TOP VIEW)
XC74UL32AAR

■ FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	L
L	H	H
H	L	H
H	H	H

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNITS
Supply Voltage		V _{CC}	-0.5~+6.0	V
Input Voltage		V _{IN}	-0.5~+6.0	V
Output Voltage		V _{OUT}	-0.5~V _{CC} +0.5	V
Input Diode Current		I _{IK}	-20	mA
Output Diode Current		I _{OK}	±20	mA
Output Current		I _{OUT}	±25	mA
V _{CC} ,GND Current		I _{CC} ,I _{GND}	±50	mA
Power Dissipation	SSOT-25* ¹	P _d	150	mW
	SON-6* ²		200	
Storage Temperature Range		T _{stg}	-65~+150	°C

Voltage is all ground standardized.

* 1) T_a=55°C

* 2) T_a=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	T _{opr}	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	t _r ,t _f	3.3	0~100	ns
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

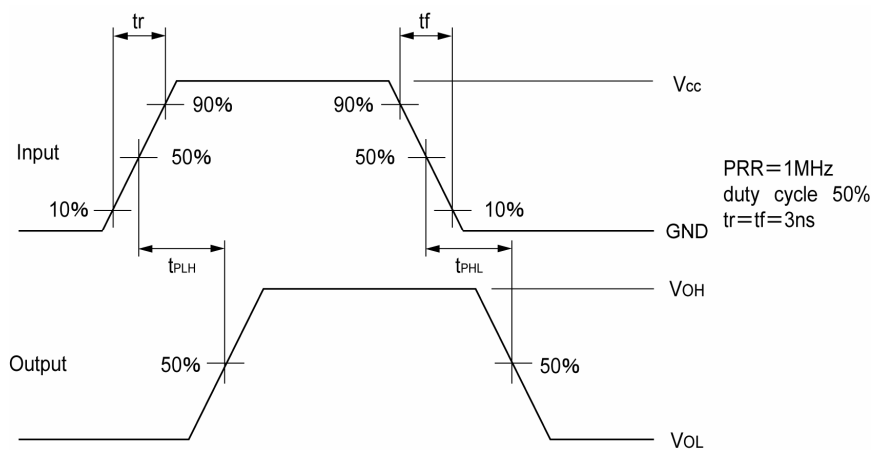
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IH} or V _{IL}	I _{OH} =-50 μA	1.9	2.0	—	1.9	V
		3.0			2.9	3.0	—	2.9	
		4.5		I _{OH} =-4mA	4.4	4.5	—	4.4	
		3.0			2.58	—	—	2.48	
		4.5		I _{OH} =-8mA	3.94	—	—	3.80	
		5.5							
	V _{OL}	2.0	V _{IN} =V _{IL}	I _{OL} =50 μA	—	—	0.1	—	V
		3.0			—	—	0.1	—	
		4.5		I _{OL} =4mA	—	—	0.1	—	
		3.0			—	—	0.36	—	
		4.5		I _{OL} =8mA	—	—	0.36	—	
		5.5							
Input Current	I _{IN}	5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

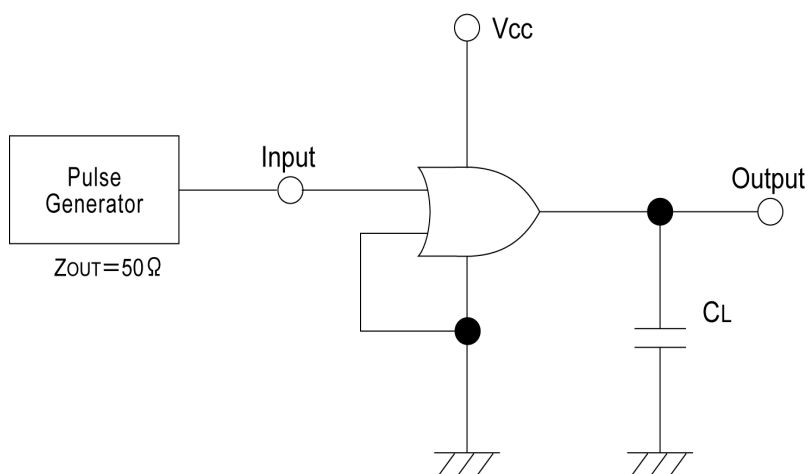
t_r=t_f=3ns

PARAMETER	SYMBOL	C _L	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	5.5	7.9	1.0	9.5	ns
			5.0		—	3.8	5.5	1.0	6.5	
		50pF	3.3		—	8.0	11.4	1.0	13.0	ns
			5.0		—	5.3	7.5	1.0	8.5	
	t _{PHL}	15pF	3.3		—	5.5	7.9	1.0	9.5	ns
			5.0		—	3.8	5.5	1.0	6.5	
		50pF	3.3		—	8.0	11.4	1.0	13.0	ns
			5.0		—	5.3	7.5	1.0	8.5	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	2	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	8.9	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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TOREX SEMICONDUCTOR LTD.

CMOS Logic

DESCRIPTION

XC74UL4066 is CMOS analog switch manufactured using silicon gate CMOS processes. The small supply current, which is one of the features of the CMOS logic, gives way to high speed analog or digital signal switching. As the series is integrated into a mini molded, SSOT-25 package, high density mounting is possible.

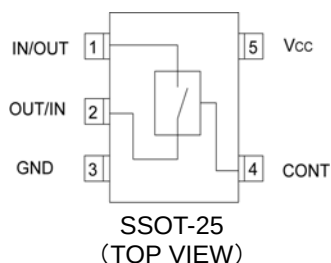
APPLICATIONS

- Palmtops
- Digital equipment

FEATURES

High Speed Operation : tpd = 2ns (TYP.)
Operating Voltage Range : 2V ~ 5.5V
Low Power Consumption : 1 μ A (MAX.)
Low ON Resistance : 22 Ω (TYP.)
CMOS Logic Analog Switch
Ultra Small Package : SSOT-25

PIN CONFIGURATION

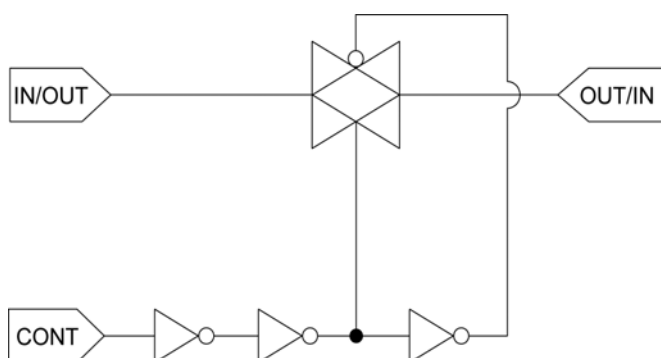


PRODUCT CLASSIFICATION

- Ordering Information

PRODUCT NAME	PACKAGE	ORDER UNIT
XC74UL4066NR	SSOT-25	3,000/Reel

LOGIC DIAGRAM



FUNCTIONS

CONTROL	STATE
L	OFF
H	ON

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Voltage	V _{CC}	-0.5~+6.0	V
Control Input Voltage	V _{CONT}	-0.5~+6.0	V
Switch Output Voltage	V _{OUT}	-0.5~V _{CC} +0.5	V
Control Input Diode Current	I _{IK}	-20	mA
Switch Output Diode Current	I _{OK}	±20	mA
Switch Output Current	I _{OUT}	±25	mA
V _{CC} ,GND Current	I _{CC} ,I _{GND}	±50	mA
Power Dissipation*	P _d	150	mW
Storage Temperature Range	T _{stg}	-65~+150	°C

Voltage is all ground standardized.

* Ta=25°C

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	T _{opr}	—	-40~+85	°C
Input Rise and Fall Time	tr,tf	3.3	0~100	ns/V
		5.0	0~20	

■ DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
“High” Level Control Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
“Low” Level Control Input Voltage	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Peak ON Resistance	R _{ONmax}	2.0	V _{CONT} =V _{IH}	—	130	350	—	550	Ω
		3.0	V _{IN} =0~V _{CC}	—	22	50	—	65	
		4.5	I _{IN} /O _{UT} =1mA	—	12	25	—	35	
ON Resistance	R _{ON(1)}	2.0	V _{CONT} =V _{IH}	—	23	50	—	65	
		3.0	V _{IN} =GND or V _{CC}	—	14	30	—	40	
		4.5	I _{IN} /O _{UT} =1mA	—	10	20	—	25	
Power Off Leak Current	I _{S(OFF)}	5.5	V _{CONT} =V _{IL} , V _{IN} =V _{CC} , V _{OUT} =GND	—	—	±0.1	—	±1.0	μA
Power On Leak Current	I _{S(ON)}	5.5	V _{CONT} =V _{IH} , V _{IN} =V _{CC} , OR GND	—	—	±0.1	—	±1.0	μA
Control Input Current	I _{CONT}	5.5	V _{IN} =V _{CC} or GND	—	—	±0.1	—	±1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND	—	—	1.0	—	5.0	μA

■ SWITCHING ELECTRICAL CHARACTERISTICS

(tr=tf=3ns)

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	2.0	R _L =10kΩ	—	4	20	—	23	ns
	t _{PHL}	3.3	C _L =50pF	—	3	6	—	8	
		5.0		—	2	5	—	6	
Output Enable Time	t _{ZL}	2.0	R _L =1kΩ	—	9	50	—	65	ns
	t _{ZH}	3.3	C _L =50pF	—	5	10	—	12	
		5.0		—	3	8	—	10	
Output Disable Time	t _{LZ}	2.0	R _L =1kΩ	—	12	60	—	75	ns
	t _{HZ}	3.3	C _L =50pF	—	10	23	—	27	
		5.0		—	8	20	—	25	
Sine Wave Distortion Rate		3.0	R _L =10kΩ C _L =50pF f _{IN} =1kHz	—	0.05	—	—	—	%
-3dB Band Width		3.0	R _L =600kΩ, C _L =50pF $20\log_{10} \frac{V_{OUT}}{V_{IN}} = -3\text{dB}$	—	200	—	—	—	MHz
Feed Through (Switch- off)		3.0	R _L =600kΩ C _L =50pF f _{IN} =1kHz	—	-60	—	—	—	dB
Cross Talk (Control Switch)		2.0	R _L =600kΩ	—	60	—	—	—	mV
		3.0	C _L =50pF	—	100	—	—	—	
		4.5	f _{IN} =1kHz	—	150	—	—	—	
Maximum Control Input Frequency		2.0	R _L =1kΩ	—	30	—	—	—	MHz
		3.0	C _L =15pF	—	30	—	—	—	
		4.5	V _{OUT} =V _{CC} /2	—	30	—	—	—	
Control Input Capacitance	C _{IN}	—		—	5	10	—	10	pF
Switch Input/Output Capacitance	C _{IN/OUT}	—		—	6	—	—	—	pF
Feed Through Capacitance	C _{IN-OUT}	—		—	0.5	—	—	—	pF
Power Dissipation Capacitance	C _{PD}	—		—	13	—	—	—	pF

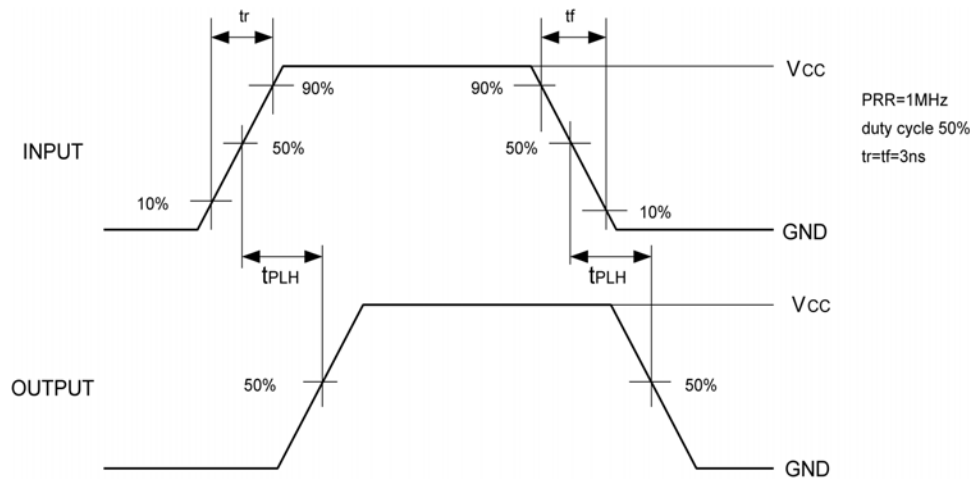
Note: CPD is defined as the value of the internal equivalent capacitance which is derived from the operating supply current at times of "No Load".

Ensure that the average operating supply current at times of "No Load" meets the following conditions:

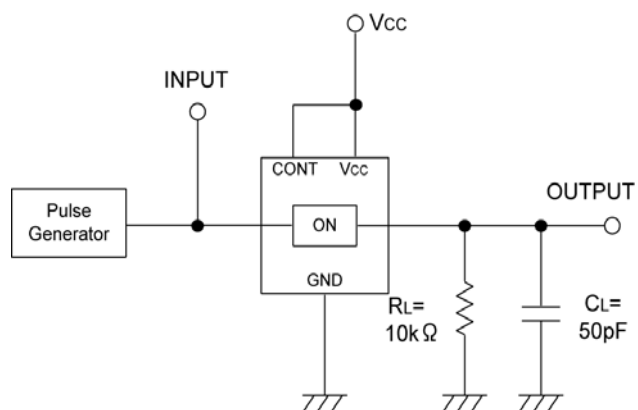
$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

■ DELAY TIME

● WAVEFORM



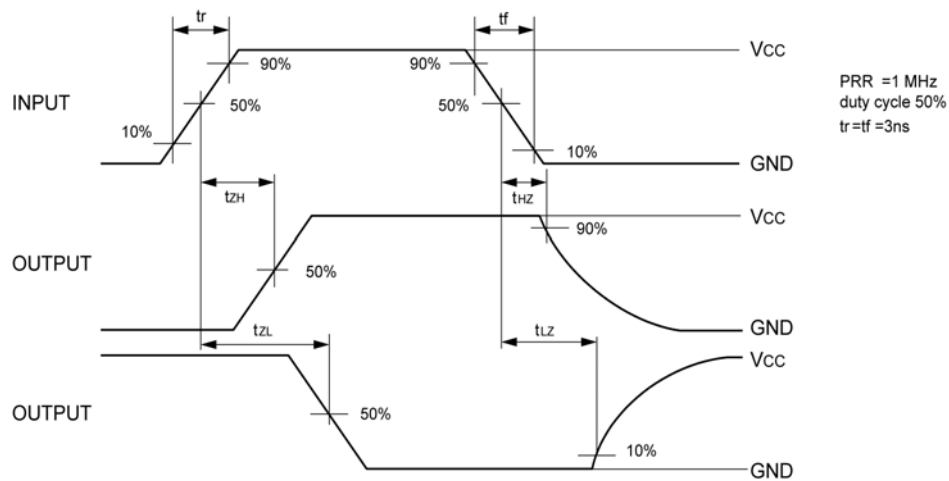
● TEST CIRCUIT



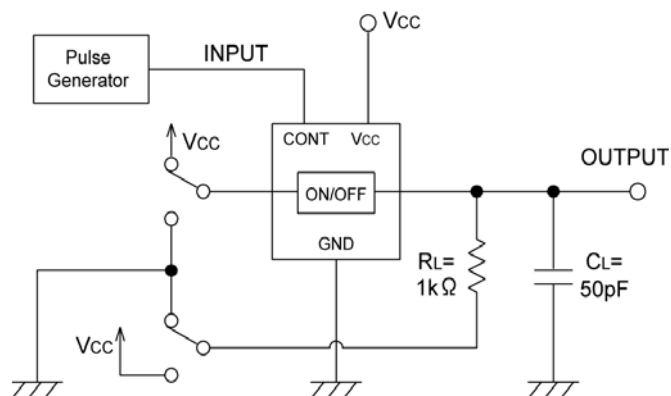
Note: Open output when measuring supply current

■ OUTPUT ENABLE TIME, OUTPUT DISABLE TIME

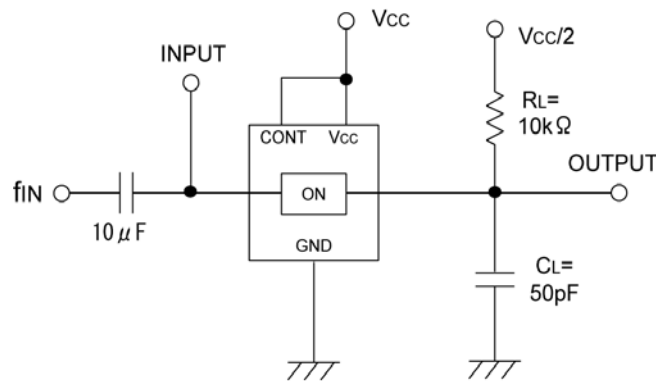
● WAVEFORM



● TEST CIRCUIT

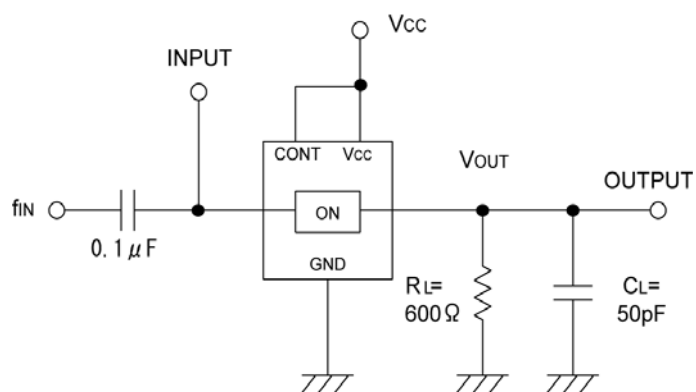


■ SINE WAVE DISTORTION RATE



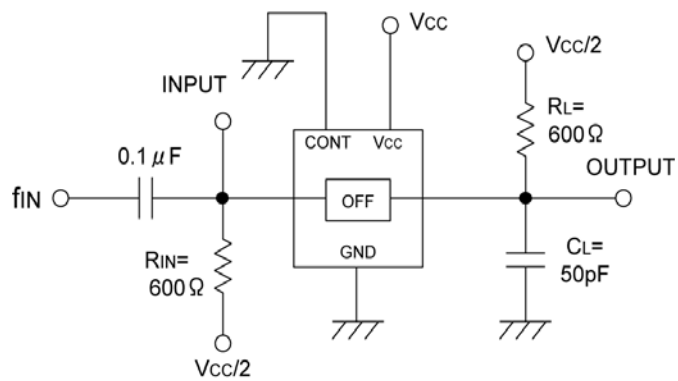
* Input by sine wave

■ -3dB BAND WIDTH



* Input by sine wave

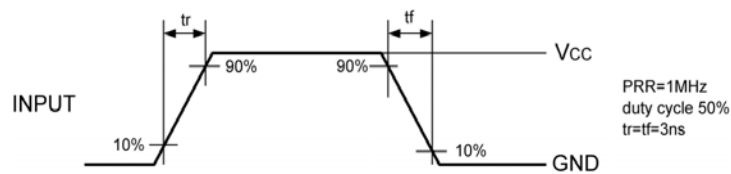
■ FEED THROUGH TEST CIRCUIT



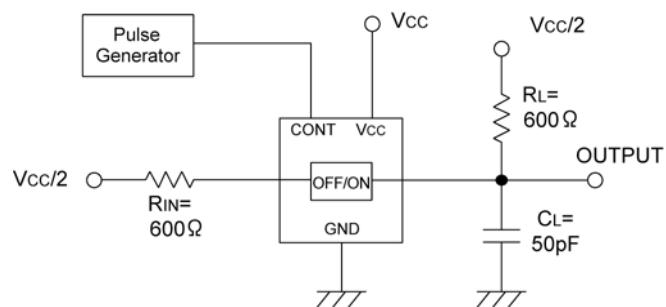
* Input by sine wave

■ CROSS TALK

● WAVEFORM

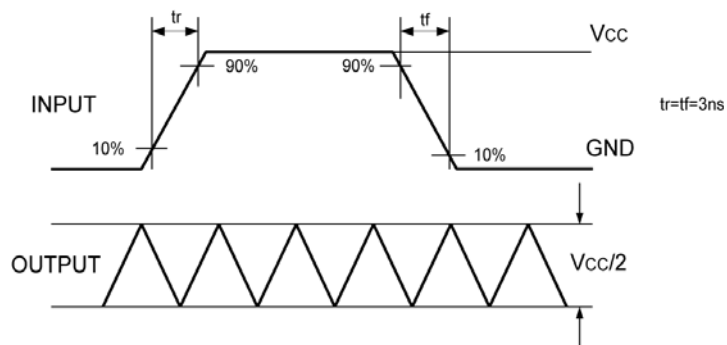


● TEST CIRCUIT

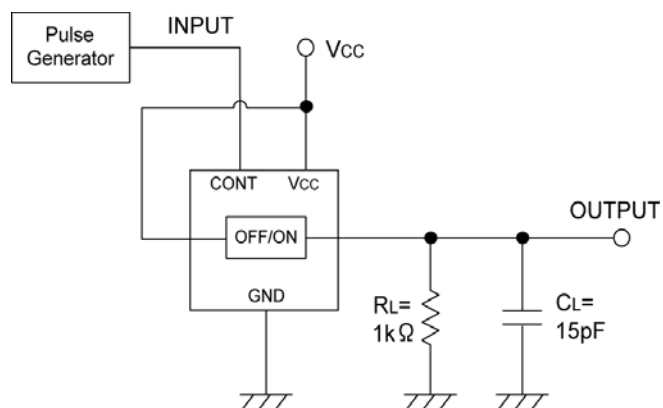


■ MAXIMUM CONTROL INPUT FREQUENCY

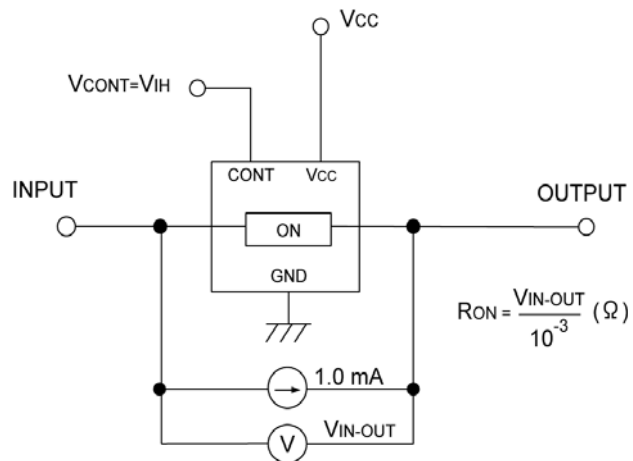
● WAVEFORM



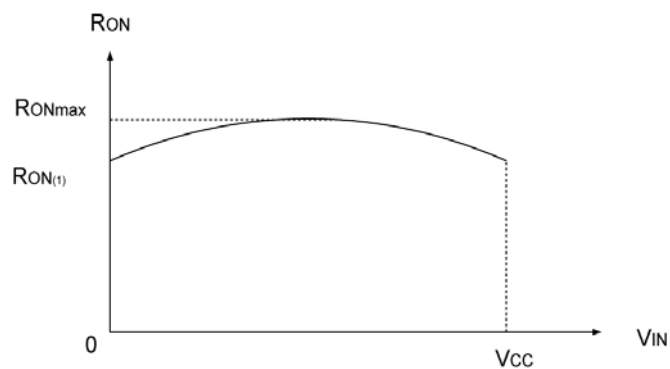
● TEST CIRCUIT



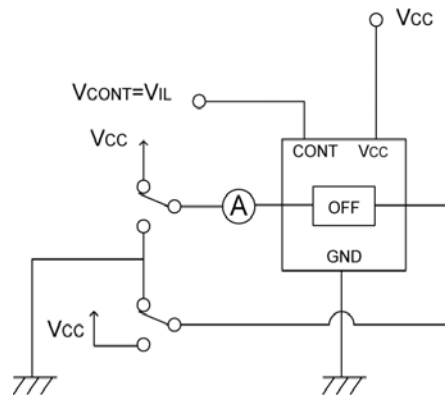
■ ON RESISTANCE



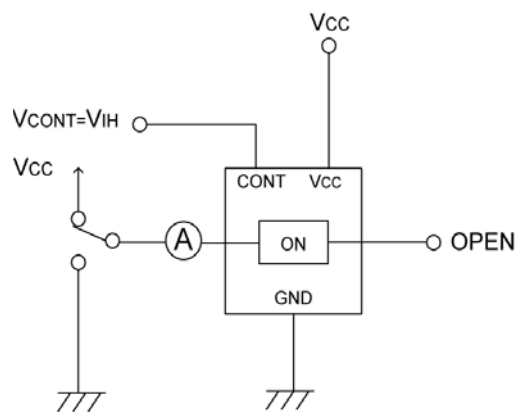
■ VOLTAGE DEPENDANCIES OF ON RESISTANCE



■ POWER OFF LEAK CURRENT



■ POWER ON LEAK CURRENT



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TOREX SEMICONDUCTOR LTD.

CMOS Logic

■ GENERAL DESCRIPTION

The XC74UL86AA is a 2-input CMOS exclusive-OR Gate, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operation achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UL86AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■ APPLICATIONS

- Palmtops
- Digital equipment

■ FEATURES

High Speed Operation : tpd = 3.1ns (TYP.)

Operating Voltage Range : 2V ~ 5.5V

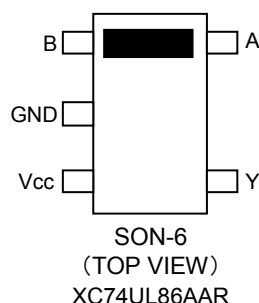
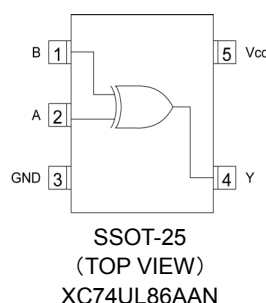
Low Power Consumption : 1 μ A (MAX.)

CMOS 2-Input Exclusive-OR Gate

Ultra Small Packages : SSOT-25, SON-6*

* Under Development

■ PIN CONFIGURATION



■ FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	L
L	H	H
H	L	H
H	H	L

H=High level

L=Low level

■ ABSOLUTE MAXIMUM RATINGS

Ta=-40°C~85°C

PARAMETER		SYMBOL	RATINGS	UNITS
Supply Voltage		VCC	-0.5~+6.0	V
Input Voltage		VIN	-0.5~+6.0	V
Output Voltage		VOUT	-0.5~VCC+0.5	V
Input Diode Current		IiK	-20	mA
Output Diode Current		IOK	±20	mA
Output Current		IOUT	±25	mA
VCC,GND Current		ICC,IGND	±50	mA
Power Dissipation	SSOT-25*1	Pd	150	mW
	SON-6*2		200	
Storage Temperature Range		Tstg	-65~+150	°C

Voltage is all ground standardized.

* 1) Ta=55°C

* 2) Ta=25°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	UNITS
Supply Voltage	V _{CC}	—	2~5.5	V
Input Voltage	V _{IN}	—	0~5.5	V
Output Voltage	V _{OUT}	—	0~V _{CC}	V
Operating Temperature Range	T _{opr}	—	-40~+85	°C
Output Current	I _{OH}	3.0	-4	mA
		4.5	-8	
	I _{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	t _r ,t _f	3.3	0~100	ns
		5.0	0~20	

DC ELECTRICAL CHARACTERISTICS

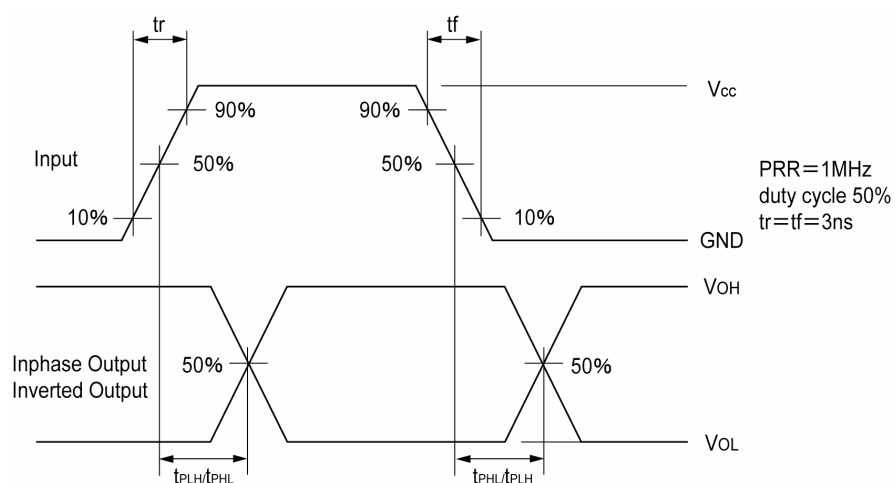
PARAMETER	SYMBOL	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
				MIN.	TYP.	MAX.	MIN.	MAX.	
Input Voltage	V _{IH}	2.0		1.5	—	—	1.5	—	V
		3.0		2.1	—	—	2.1	—	
		5.5		3.85	—	—	3.85	—	
	V _{IL}	2.0		—	—	0.5	—	0.5	V
		3.0		—	—	0.9	—	0.9	
		5.5		—	—	1.65	—	1.65	
Output Voltage	V _{OH}	2.0	V _{IN} =V _{IH} or V _{IL}	I _{OH} =-50 μA	1.9	2.0	—	1.9	V
		3.0			2.9	3.0	—	2.9	
		4.5			4.4	4.5	—	4.4	
		3.0		I _{OH} =-4mA	2.58	—	—	2.48	
		4.5		I _{OH} =-8mA	3.94	—	—	3.80	
		5.5		I _{OH} =-8mA	—	—	—	—	
	V _{OL}	2.0	V _{IN} =V _{IH} or V _{IL}	I _{OL} =50 μA	—	—	0.1	—	V
		3.0			—	—	0.1	—	
		4.5			—	—	0.1	—	
		3.0		I _{OL} =4mA	—	—	0.36	—	
		4.5		I _{OL} =8mA	—	—	0.36	—	
		5.5		I _{OL} =8mA	—	—	—	—	
Input Current	I _{IN}	5.5	V _{IN} =V _{CC} or GND	-0.1	—	0.1	-1.0	1.0	μA
Static Supply Current	I _{CC}	5.5	V _{IN} =V _{CC} or GND, I _{OUT} =0 μA	—	—	1.0	—	10.0	

SWITCHING ELECTRICAL CHARACTERISTICS

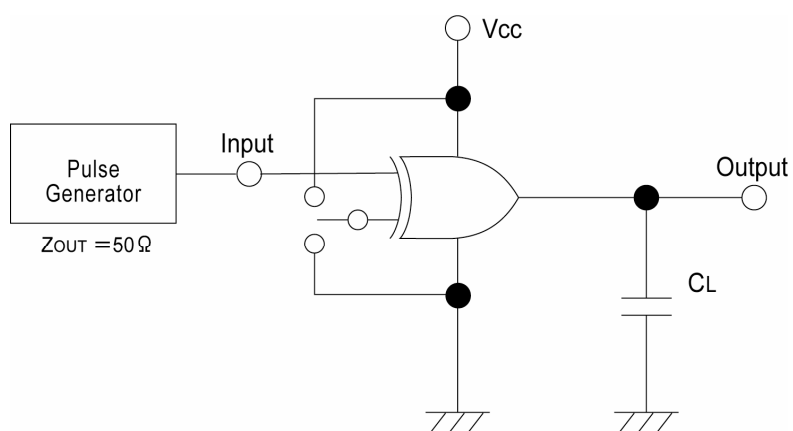
t_r=t_f=3ns

PARAMETER	SYMBOL	C _L	V _{CC} (V)	CONDITIONS	Ta=25°C			Ta=-40°C~85°C		UNITS
					MIN.	TYP.	MAX.	MIN.	MAX.	
Delay Time	t _{PLH}	15pF	3.3		—	4.4	11.0	1.0	13.0	ns
			5.0		—	3.3	6.8	1.0	8.0	
		50pF	3.3		—	6.1	14.5	1.0	16.5	ns
			5.0		—	4.4	8.8	1.0	10.0	
	t _{PHL}	15pF	3.3		—	4.0	11.0	1.0	13.0	ns
			5.0		—	2.9	6.8	1.0	8.0	
		50pF	3.3		—	5.6	14.5	1.0	16.5	ns
			5.0		—	4.1	8.8	1.0	10.0	
Input Capacitance	C _{IN}	—	5.0	V _{IN} =V _{CC} or GND	—	4	10	—	10	pF
Power Dissipation Capacitance	C _{pd}	No Load, f=1MHz			—	12	—	—	—	pF

■ WAVEFORM



■ TEST CIRCUIT



Note: Open output when measuring supply current

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