

1.8V, LVDS Compliant, 4 Differential Channel, 2:1 Mux/DeMux Switch w/Single Enable

Features

- 4 Differential Channel, 2:1 Mux/DeMux
- LVDS singal switching beyond 3.6 Gbps
- Low insertion loss, -2.88dB @ 2.9 Gbps
- Low bit-to-bit skew, 10ps max
- Low Off Isolation: -77dB@10 MHz
- Low crosstalk: -68dB@10 MHz
- V_{CC} Operating Range: +1.5V to +2.0V
- ESD Tolerance: 8kV HBM I/O; 2kV HBM select pin
- Packaging (Pb-free & Green):
 - 42-contact TQFN (ZH)

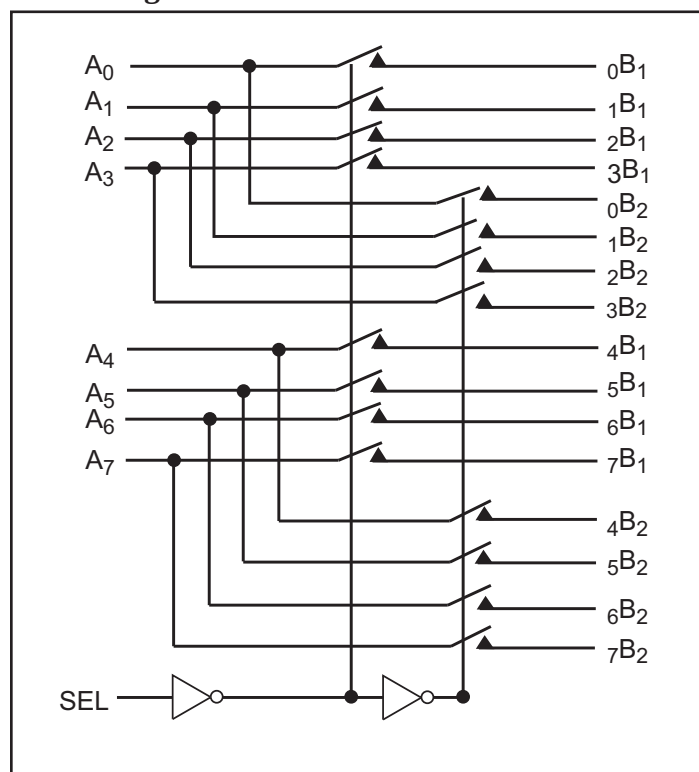
Description

Pericom Semiconductor's PI2LVD412 is an 8-to-4 differential channel multiplexer/demultiplexer switch. The device is bi-directional and designed specifically for Low Voltage Differential Signals. Users can take advantage of low bit-to-bit skew and high channel-to-channel noise isolation.

Application

Routes multiple Low Voltage Differential Signals at high-speed data rates with little or no attenuation.

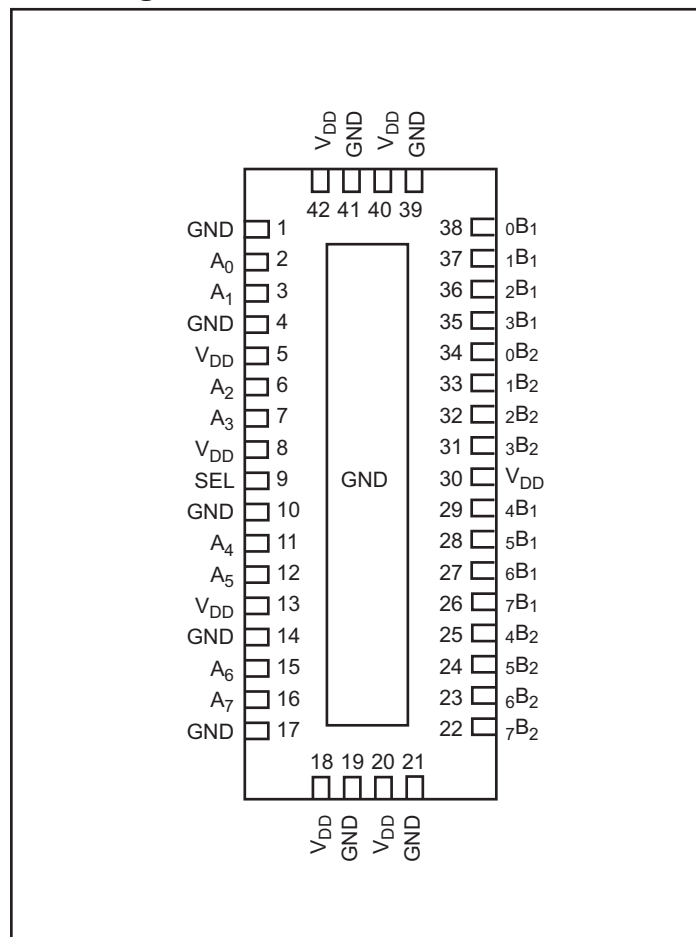
Block Diagram



Truth Table

Function	SEL
A _N to N B ₁	L
A _N to N B ₂	H

Pin Configuration



Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	-65°C to +150°C
Supply Voltage to Ground Potential.....	-0.5V to +2.5V
DC Input Voltage	-0.5V to V_{DD}
DC Output Current.....	120mA
Power Dissipation.....	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for Switching over Operating Range ($T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 1.5\text{V}$ to 2.0V)

Parameter	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed HIGH level	$0.65 \times V_{DD}$			V
V_{IL}	Input LOW Voltage	Guaranteed LOW level	-0.5		$0.35 \times V_{DD}$	
V_{IK}	Clamp Diode Voltage	$V_{DD} = \text{Max.}$, $I_{IN} = -18\text{mA}$		-0.7	-1.2	
I_{IH}	Input HIGH Current	$V_{DD} = \text{Max.}$, $V_{IN} = V_{DD}$			± 5	μA
I_{IL}	Input LOW Current	$V_{DD} = \text{Max.}$, $V_{IN} = \text{GND}$			± 5	
$R_{FLAT(ON)}$	On-Resistance Flatness ⁽⁴⁾	$V_{DD} = \text{Min.}$, $0\text{V} \leq V_{IN} \leq 1.5\text{V}$ and V_{DD} , $I_{IN} = -40\text{mA}$		1		
ΔR_{ON}	On-Resistance match from center ports to any other port ⁽⁵⁾	$V_{DD} = \text{Min.}$, $0\text{V} \leq V_{IN} \leq 1.5\text{V}$, $I_{IN} = -40\text{mA}$		0.9		

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$, $V_{IN} = \text{GND}$ or V_{DD}		200		μA

Dynamic Electrical Characteristics Over the Operating Range ($T_A = -40^{\circ}$ to $+85^{\circ}\text{C}$, $V_{CC} = 1.8\text{V} \pm 10\%$, $\text{GND} = 0\text{V}$)

Parameter ⁽³⁾	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
X_{TALK}	Crosstalk	See Fig. 1 for Measurement Setup, $f = 10\text{ MHz}$		-68		dB
O_{IRR}	OFF Isolation	See Fig. 2 for Measurement Setup, $f = 10\text{ MHz}$		-77		
-3dB BW	Bandwidth	See Fig. 3 for measurement Setup		1.8		GHz

Notes:

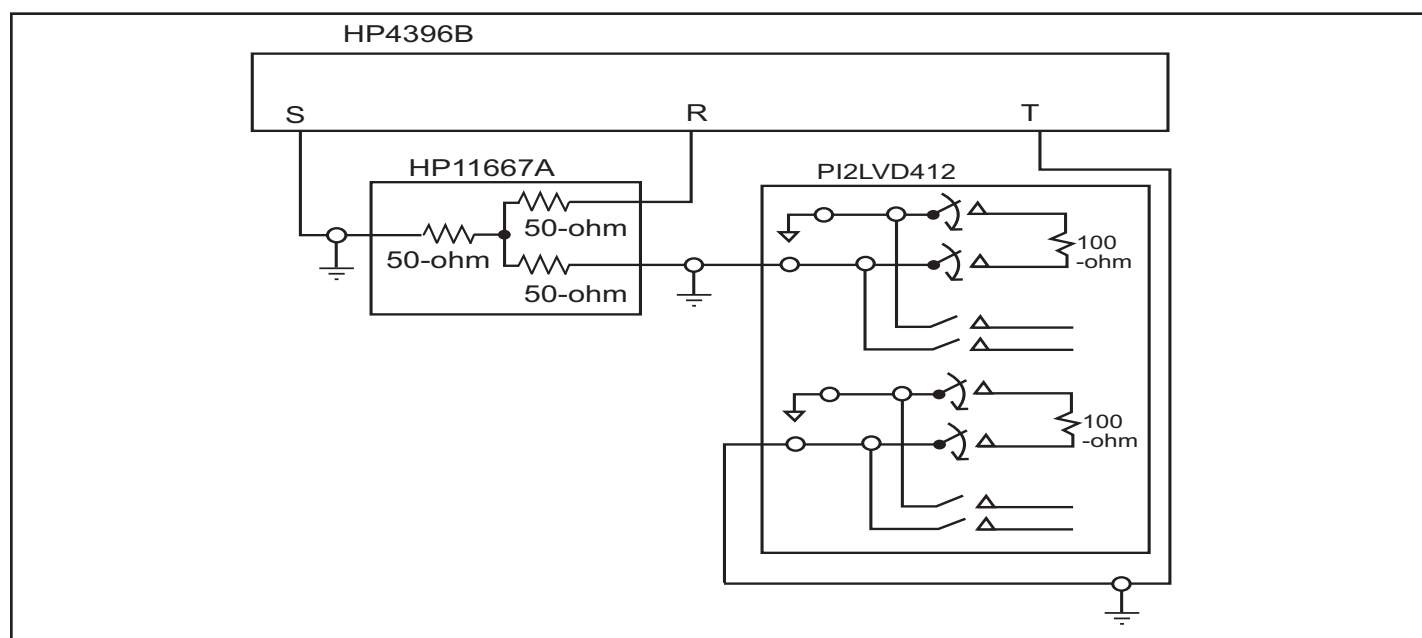
- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{DD} = 1.8\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
- Guaranteed by design.
- Flatness is defined as the difference between the max and min value of On-Resistance.
- $\Delta R_{ON} = R_{ON\text{ max}} - R_{ON\text{ min}}$

Switching Characteristics ($T_A = -40^\circ$ to $+85^\circ\text{C}$, $V_{DD} = 1.8\text{V} \pm 10\%$)

Parameter	Description	Min. ⁽¹⁾	Max. ⁽¹⁾	Units
tPZH, tPZL	Line Enable Time - SEL to A _N , B _N	0.5	8.0	ns
tPHZ, tPLZ	Line Disable Time - SEL to A _N , B _N	0.5	4.0	
t _{b-b}	Bit-to-bit skew		10	ps
t _{ch-ch}	Channel-to-channel skew		35	ps

Note:

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.


Fig 1. Crosstalk Setup

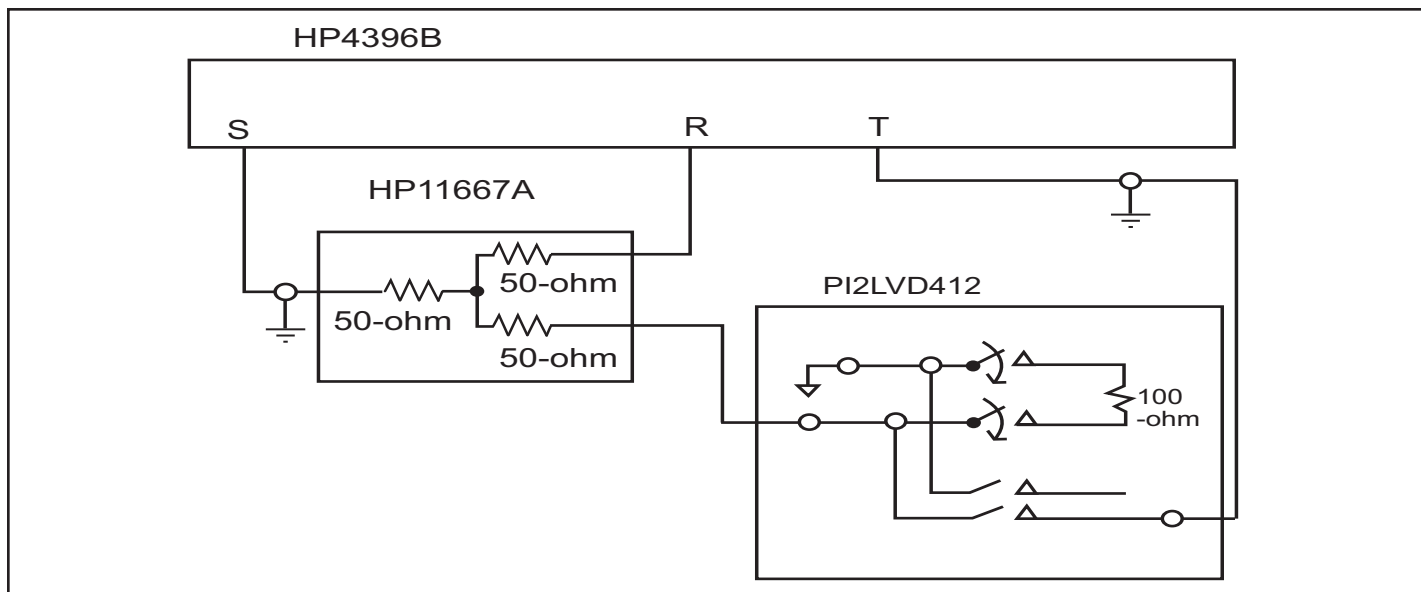


Fig 2. Off-isolation setup

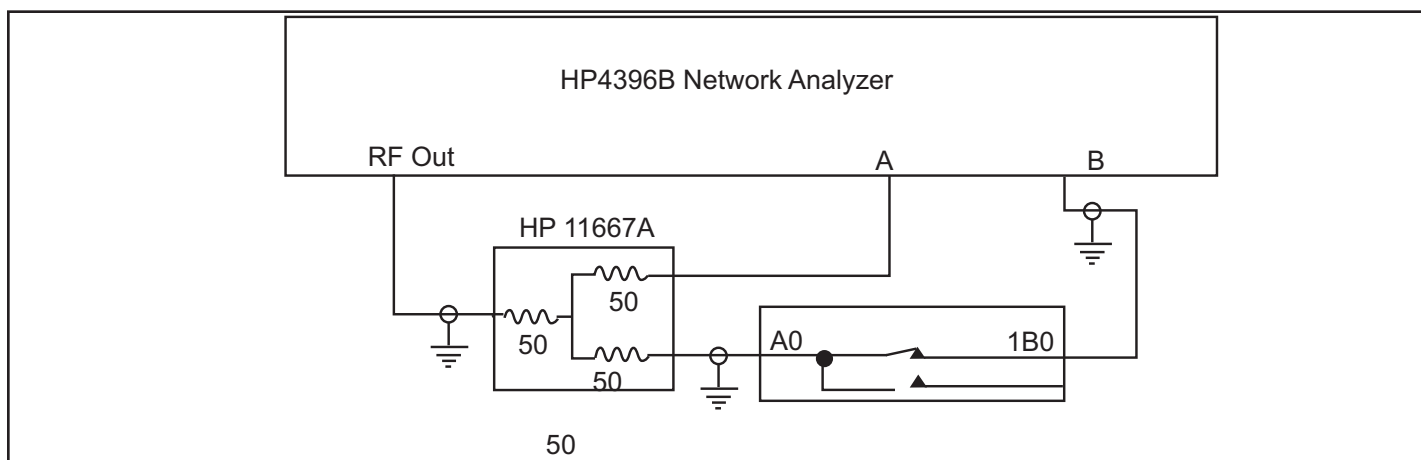


Fig 3. Bandwidth Setup

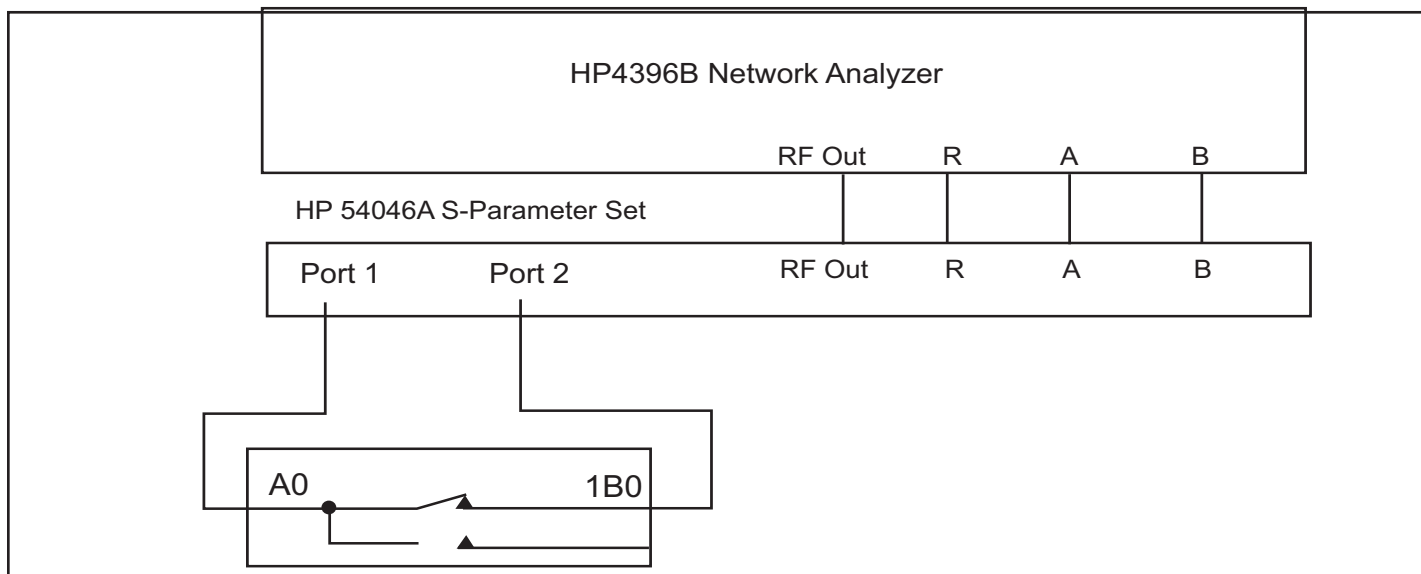


Fig 4. S21 (Insertion Loss) and S11 (Reflection Signal) Setup



Fig 5. Crosstalk, $V_{DD} = 1.8V$, 25°C

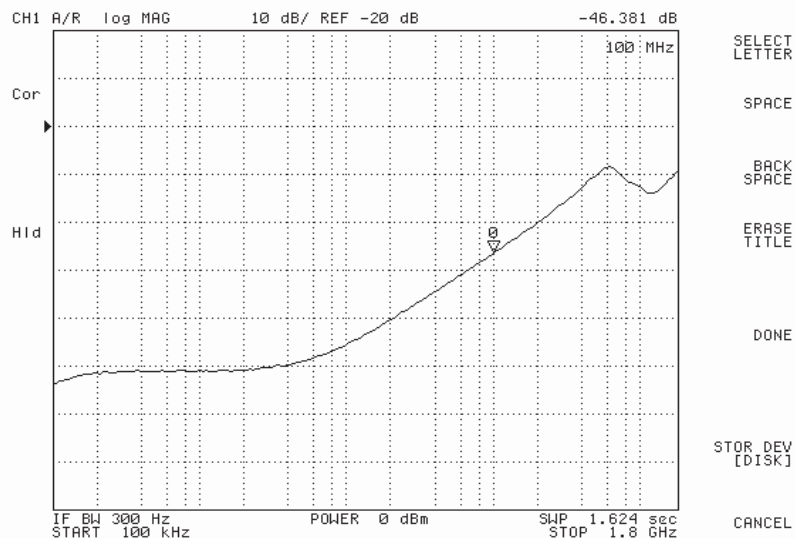


Fig 6. Off Isolation, $V_{DD} = 1.8V$, 25°C

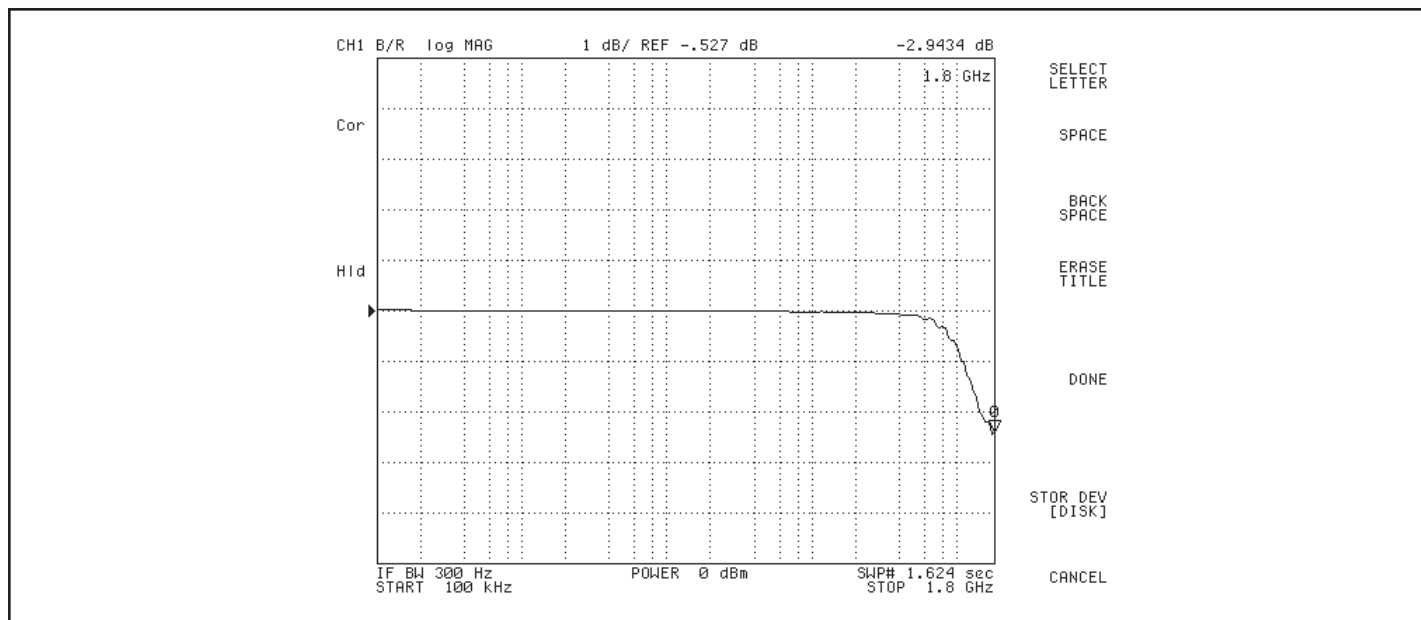


Fig 7. -3 dB Bandwidth, $V_{DD}=1.8V$, 25°C

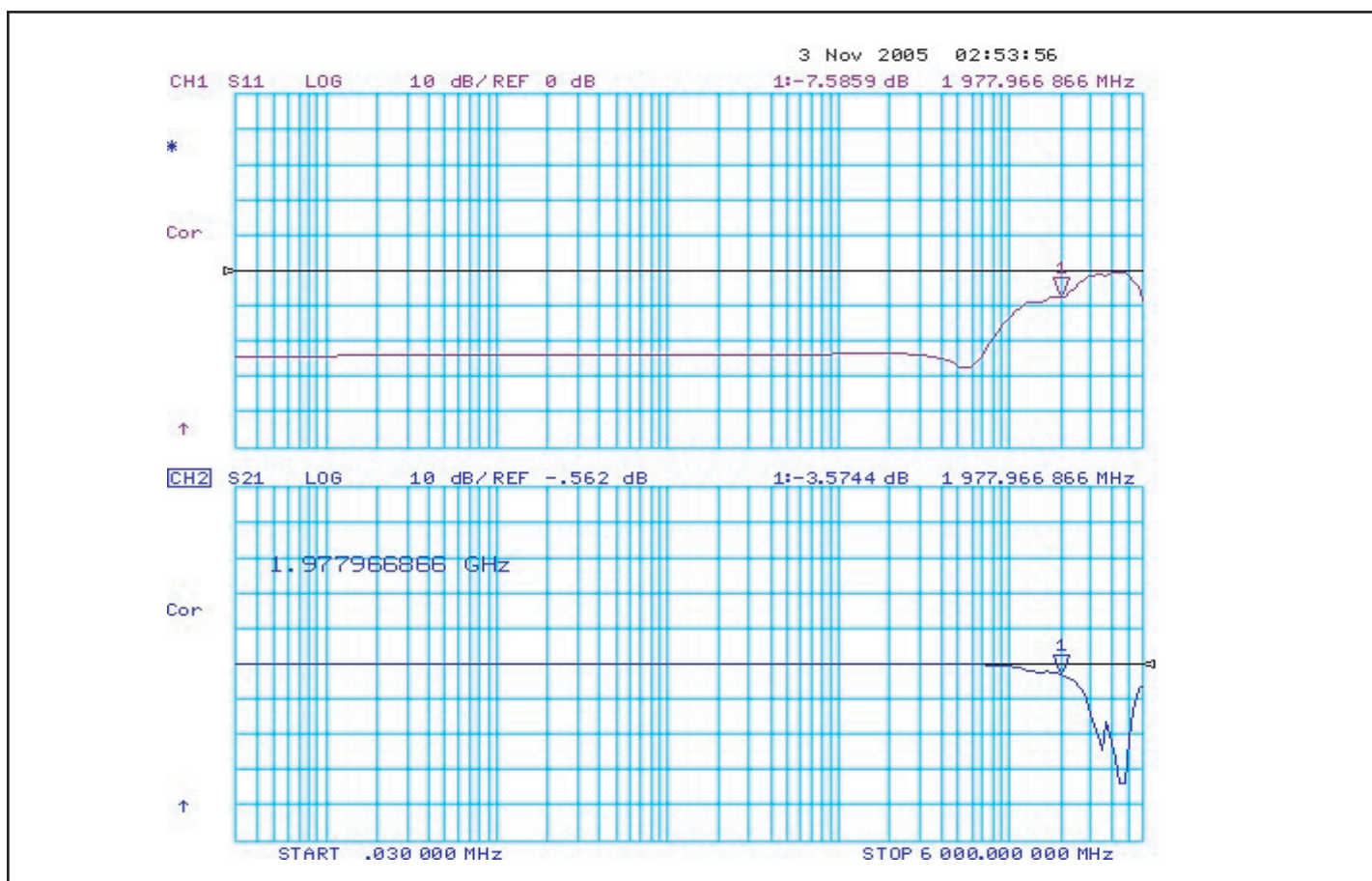
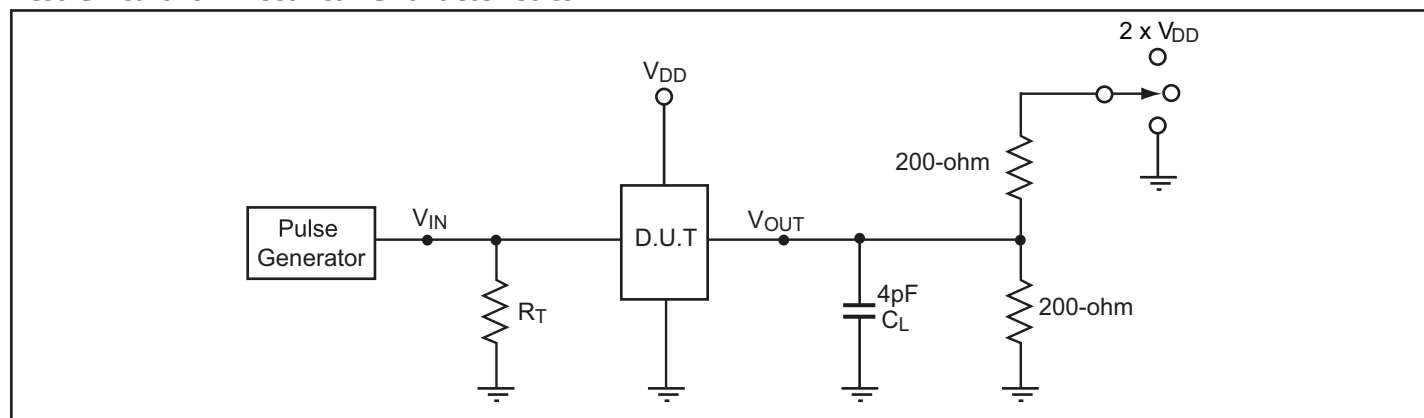


Fig 8. Return and Insertion Loss Log Diagram
from 30kHz to 6.0GHz using Matching Cables, $V_{DD}=1.8V$, 25C

Test Circuit for Electrical Characteristics



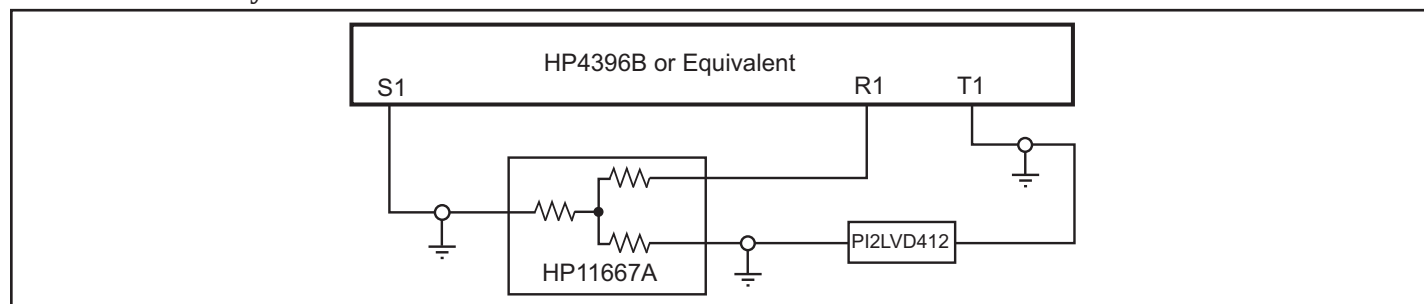
Notes:

- C_L = Load capacitance: includes jig and probe capacitance.
- R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
- Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- All input impulses are supplied by generators having the following characteristics: $PRR \leq \text{MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.5\text{ns}$, $t_F \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

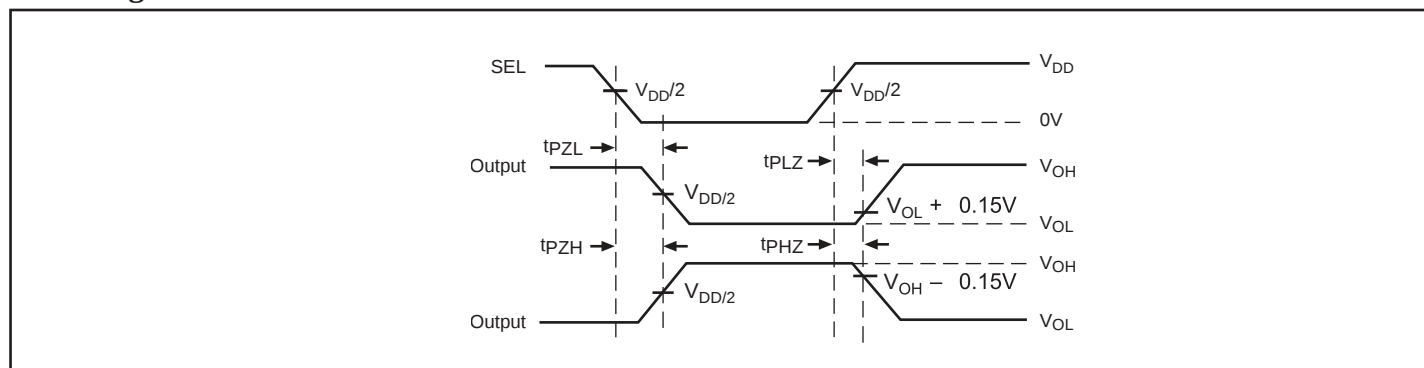
Switch Positions

Test	Switch
t_{PLZ} , t_{PZL} (output on B-side)	$2 \times V_{CC}$
t_{PHZ} , t_{PZH} (output on B-side)	GND
Prop Delay	Open

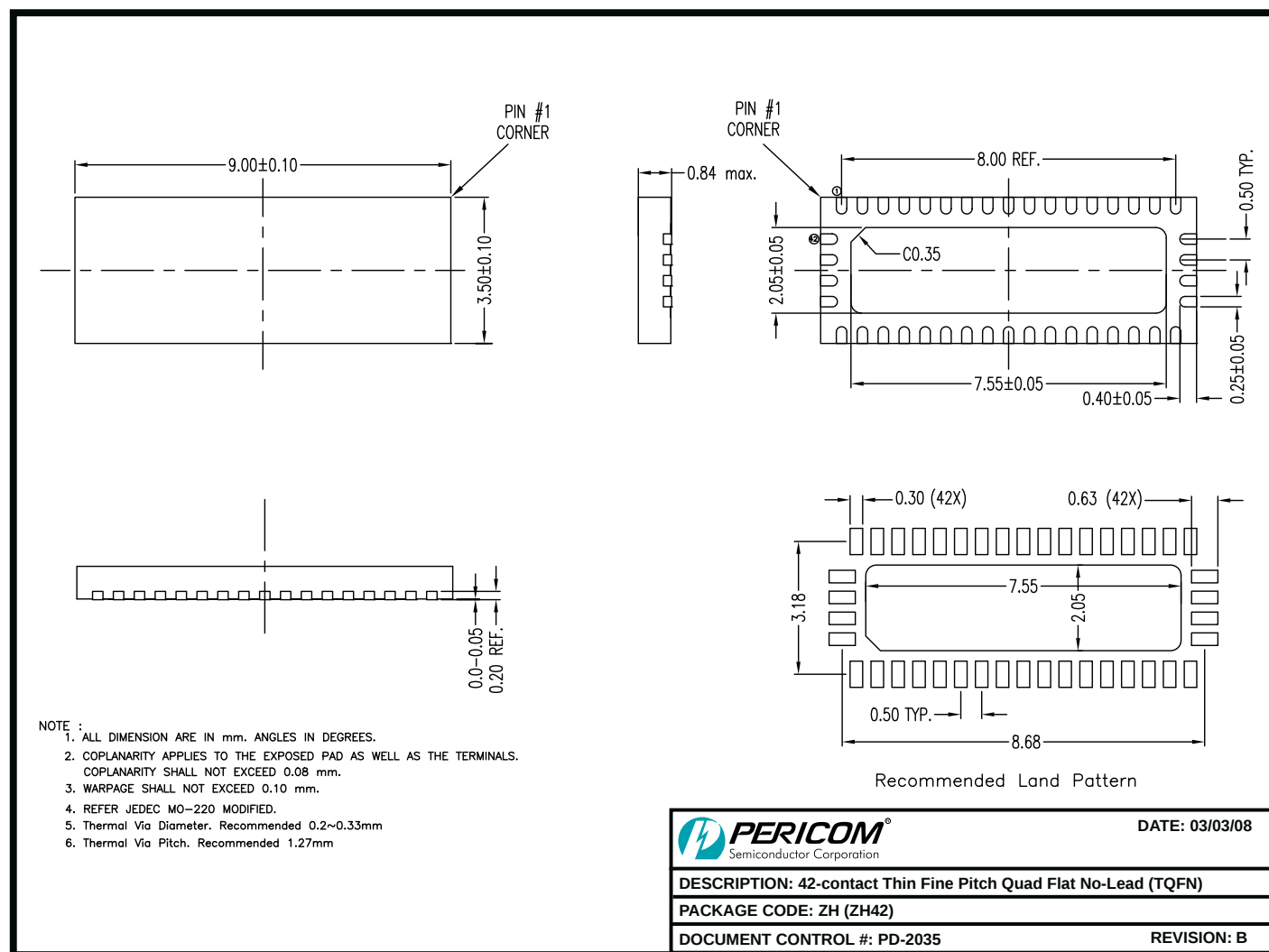
Test Circuit for Dynamic Electrical Characteristics



Switching Waveforms



Voltage Waveforms Enable and Disable Times

Packaging Mechanical: 42-Contact TQFN (ZH)


08-0098

Ordering Information

Ordering Code	Package Code	Package Description
PI2LVD412ZHE	ZH	Pb-free & Green, 42-Contact TQFN

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- X Suffix = Tape/Reel