


SANYO Semiconductors

DATA SHEET

LB1947 — Monolithic Digital IC PWM Current Control Type Bidirectional Motor Driver

Overview

The LB1947 is a PWM current control type bidirectional motor driver IC.

Features

- PWM current control (fixed OFF time)
- Selectable current decay pattern (FAST, SLOW, and MIX DECAY modes)
- Simultaneous ON prevention function (feed-through current prevention)
- Built-in thermal shutdown circuit
- Built-in noise canceler

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum motor supply voltage	$V_{BB \text{ max}}$		50	V
Output peak current	$I_O \text{ peak}$	$t_W \leq 20\mu\text{s}$	2.25	A
Output continuous current	$I_O \text{ max}$		2.0	A
Logic supply voltage	$V_{CC \text{ max}}$		7.0	V
Logic input voltage range	V_{IN}		-0.3 to V_{CC}	V
Emitter output voltage	$V_E \text{ max}$		1.1	V
Reference voltage	V_{REF}		-0.3 to V_{CC}	V
Allowable power dissipation	$P_d \text{ max}$		1.6	W
Operating temperature	T_{opr}		-20 to +85	$^\circ\text{C}$
Storage temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

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LB1947

Allowable Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Motor supply voltage	V _{BB}		10 to 45	V
Logic supply voltage	V _{CC}		4.75 to 5.25	V
Reference voltage	V _{REF}		0 to V _{CC} -2	V

Electrical Characteristics at Ta = 25°C, V_{CC} = 5V

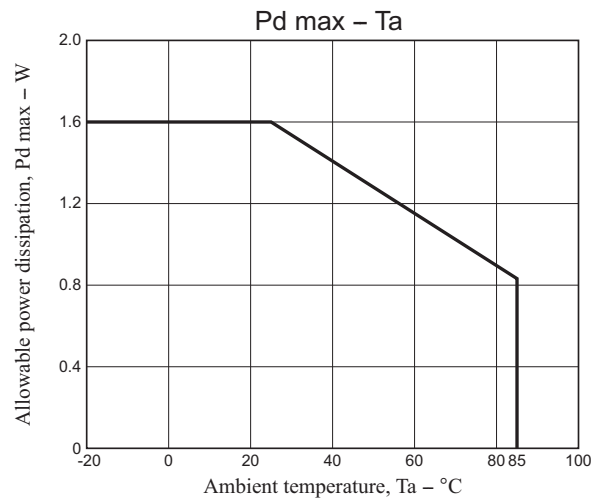
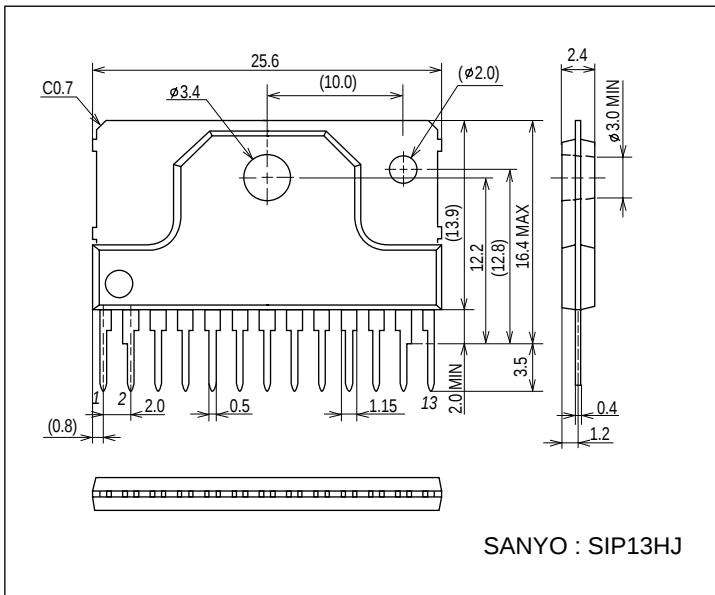
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Output Block						
Output stage supply current	I _{BB} ON	No-load state, Forward	0.4	0.6	1.0	mA
	I _{BB} BR	No-load state, Brake	0.2	0.4	0.8	mA
	I _{BB} OFF	No-load state, Output off	0.2	0.4	0.8	mA
	I _{BB} wt	No-load state, Standby mode			0.1	mA
Output saturation voltage	V _O sat1	I _O = +1.0A, Sink		1.2	1.5	V
	V _O sat2	I _O = +2.0A, Sink		1.6	1.9	V
	V _O sat3	I _O = -1.0A, Source		1.8	2.2	V
	V _O sat4	I _O = -2.0A, Source		2.1	2.4	V
Output leak current	I _O 1(leak)	V _O = V _{BB} , Sink			50	μA
	I _O 2(leak)	V _O = 0V, Source	-50			μA
Output sustain voltage	V _{SUS}	L = 3.9mH, I _O = 2.0A, Design guarantee value*	50			V
Logic Block						
Logic supply current	I _{CC} ON	IN1: High, IN2: Low, ST: High	11	16	21	mA
	I _{CC} BR	IN1: Low, IN2: High, ST: High	11	16	21	mA
	I _{CC} OFF	IN1: Low, IN2: Low, ST: High	11	16	21	mA
	I _{CC} wt	ST: Low	1.0	2	3.0	mA
Input voltage	V _{IN} H		2			V
	V _{IN} L				0.8	V
Input current	I _{IN} H	V _{IN} = 5V	60	90	120	μA
	I _{IN} L	V _{IN} = 0.8V	6	10	13	μA
Sensing voltage	V _E		0		1.1	V
Sensing voltage 25H	V _{EH} 25	V _I = High, V _{REF} = 2.5V	0.970	1.0	1.030	V
Sensing voltage 25L	V _{EL} 25	V _I = Low, V _{REF} = 2.5V	0.483	0.5	0.513	V
Sensing voltage 15H	V _{EH} 15	V _I = High, V _{REF} = 1.5V	0.385	0.4	0.410	V
Sensing voltage 15L	V _{EL} 15	V _I = Low, V _{REF} = 1.5V	0.190	0.2	0.210	V
Sensing voltage 05H	V _{EH} 05	V _I = High, V _{REF} = 0.5V	0.190	0.2	0.210	V
Sensing voltage 05L	V _{EL} 05	V _I = Low, V _{REF} = 0.5V	0.092	0.1	0.108	V
Reference current	I _{ref}	V _{REF} = 1.0V	-0.5		+0.5	μA
CR pin current	I _{CR}		-1.56	-1.3	-1.04	mA
MD pin voltage	V _{MD} H		V _{CC} -0.3			V
	V _{MD} M		0.3V _{CC}		V _{CC} -1.0	V
	V _{MD} L				.04	V
MD input current	I _{MD} H	MD = V _{CC} -0.5V, CR = 1.0V	-1.0		+1.0	μA
	I _{MD} L	MD = 0.4V, CR = 2.0V	-5.0			μA
Thermal shutdown temperature	TSD	Design guarantee value*		170		°C

* Design guarantee value, Do not measurement.

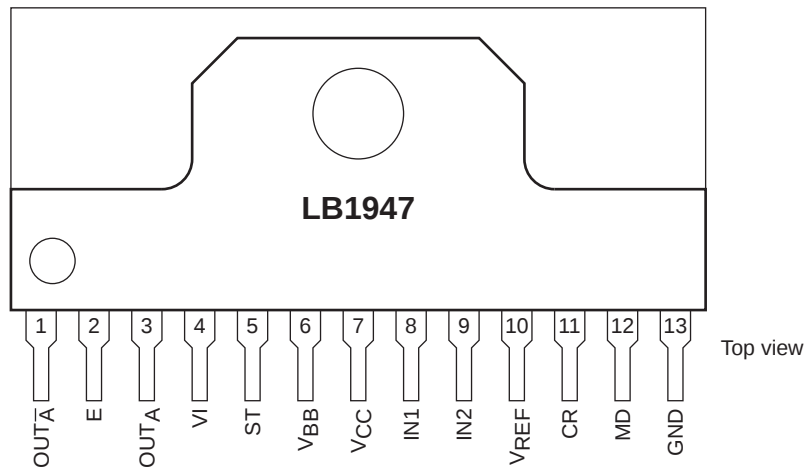
Package Dimensions

unit : mm (typ)

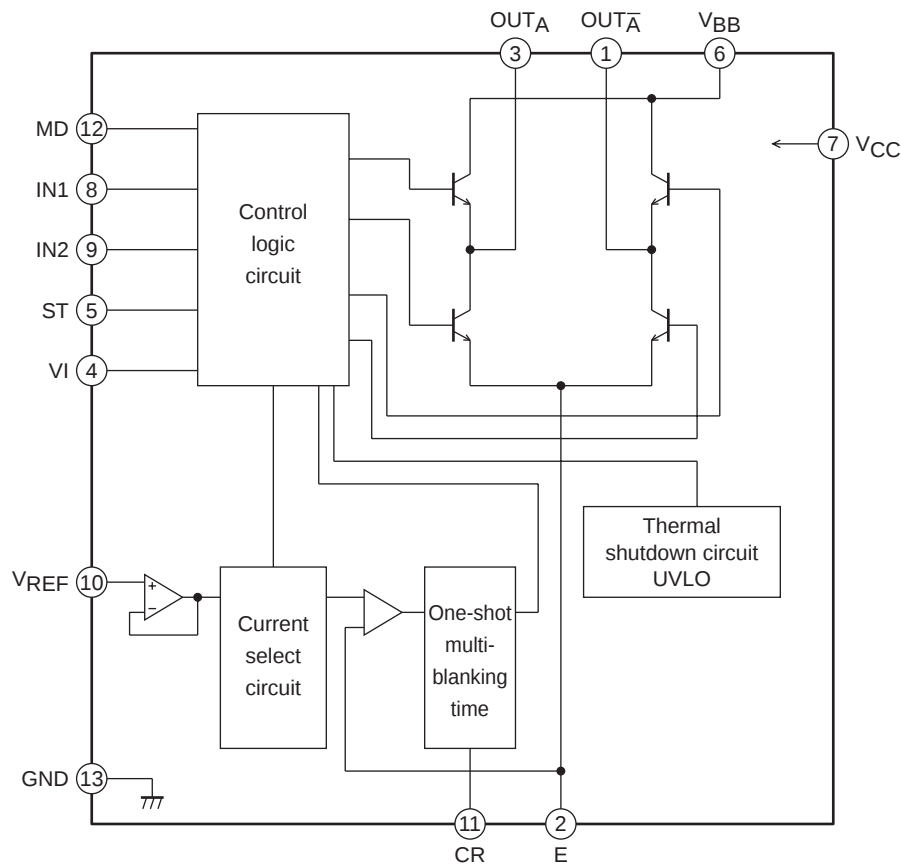
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Pin Assignment



Block Diagram



Truth Table

IN1	IN2	ST	VI	MD	OUT _A	OUT _A [̄]	Operating mode
H	L	H	H	L	H	L	Forward, 2/5 times, FAST
H	L	H	H	M	H	L	Forward, 2/5 times, MIX
H	L	H	H	H	H	L	Forward, 2/5 times, SLOW
H	L	H	L	L	H	L	Forward, 1/5 times, FAST
H	L	H	L	M	H	L	Forward, 1/5 times, MIX
H	L	H	L	H	H	L	Forward, 1/5 times, SLOW
H	H	H	H	L	L	H	Reverse, 2/5 times, FAST
H	H	H	H	M	L	H	Reverse, 2/5 times, MIX
H	H	H	H	H	L	H	Reverse, 2/5 times, SLOW
H	H	H	L	L	L	H	Reverse, 1/5 times, FAST
H	H	H	L	M	L	H	Reverse, 1/5 times, MIX
H	H	H	L	H	L	H	Reverse, 1/5 times, SLOW
L	H	H	H	L	L	L	Brake, 2/5 times, FAST
L	H	H	H	M	L	L	Brake, 2/5 times, MIX
L	H	H	L	L	L	L	Brake, 1/5 times, FAST
L	H	H	L	M	L	L	Brake, 1/5 times, MIX
L	H	H	X	H	L	L	Brake, no current limiting
L	L	H	X	X	OFF	OFF	Output OFF
X	X	L or OPEN	X	X	OFF	OFF	Standby mode (circuit OFF)

Except for MD pin, Low at input OPEN.

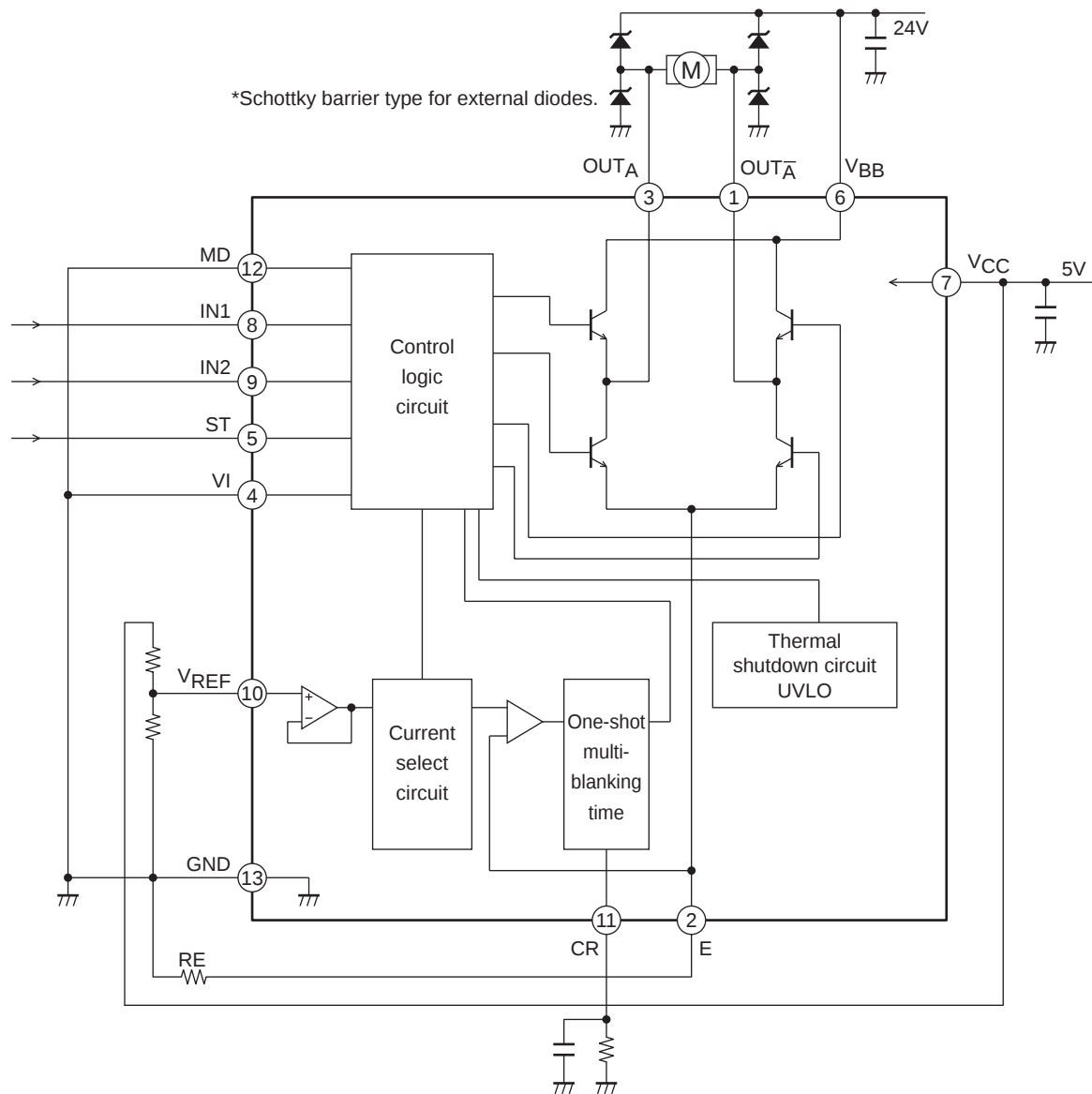
MD M: determined by external voltage.

Pin Function

Pin No.	Pin name	Function	Equivalent circuit
1 3	OUT _A OUT _A	Output pin.	
2	E	Sense voltage control pin.	
4	VI	High: sense voltage is 2/5 of V _{REF} Low: sense voltage is 1/5 of V _{REF}	
5	ST	High: circuit operation ON Low: standby mode	
8	IN1	High: rotation mode Low: brake mode	
9	IN2	High: reverse mode Low: forward mode	
6	V _{BB}	Motor power supply voltage.	
7	V _{CC}	Logic power supply voltage.	
10	V _{REF}	Output current setting reference pin. Setting range: 0 to (V _{CC} -2V)	
11	CR	Oscillator with self-excitation.	
12	MD	Current attenuation switching pin. Low : FAST DECAY High: SLOW DECAY M : MIX DECAY M is set by external power supply voltage. Range : 1.1 to 4.0V	
13	GND	Ground pin	

Sample Application Circuits

1. Forward/reverse motor with current limiter

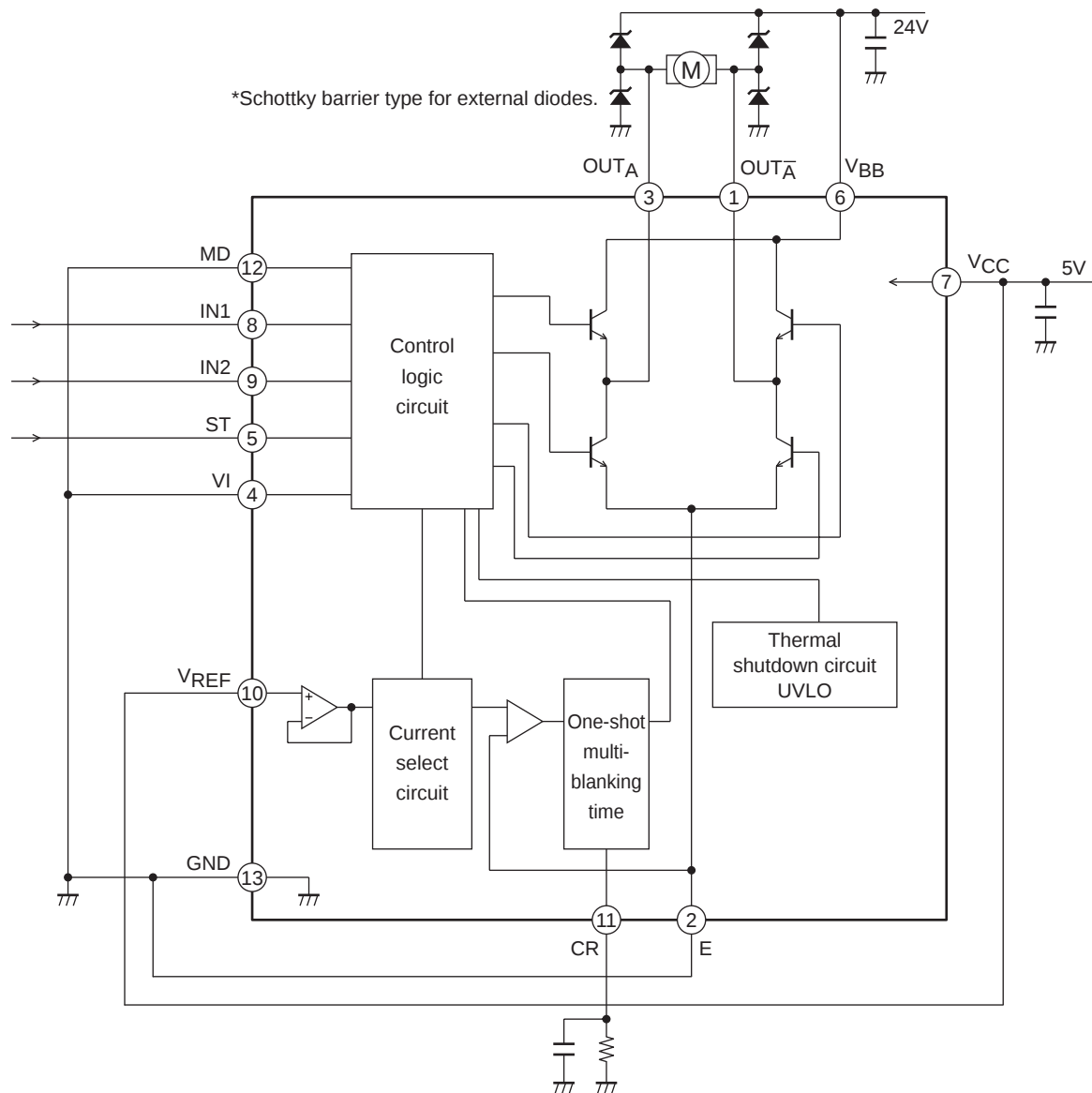


Limiter current setting method

$$I = V_{REF} / (5 \times RE)$$

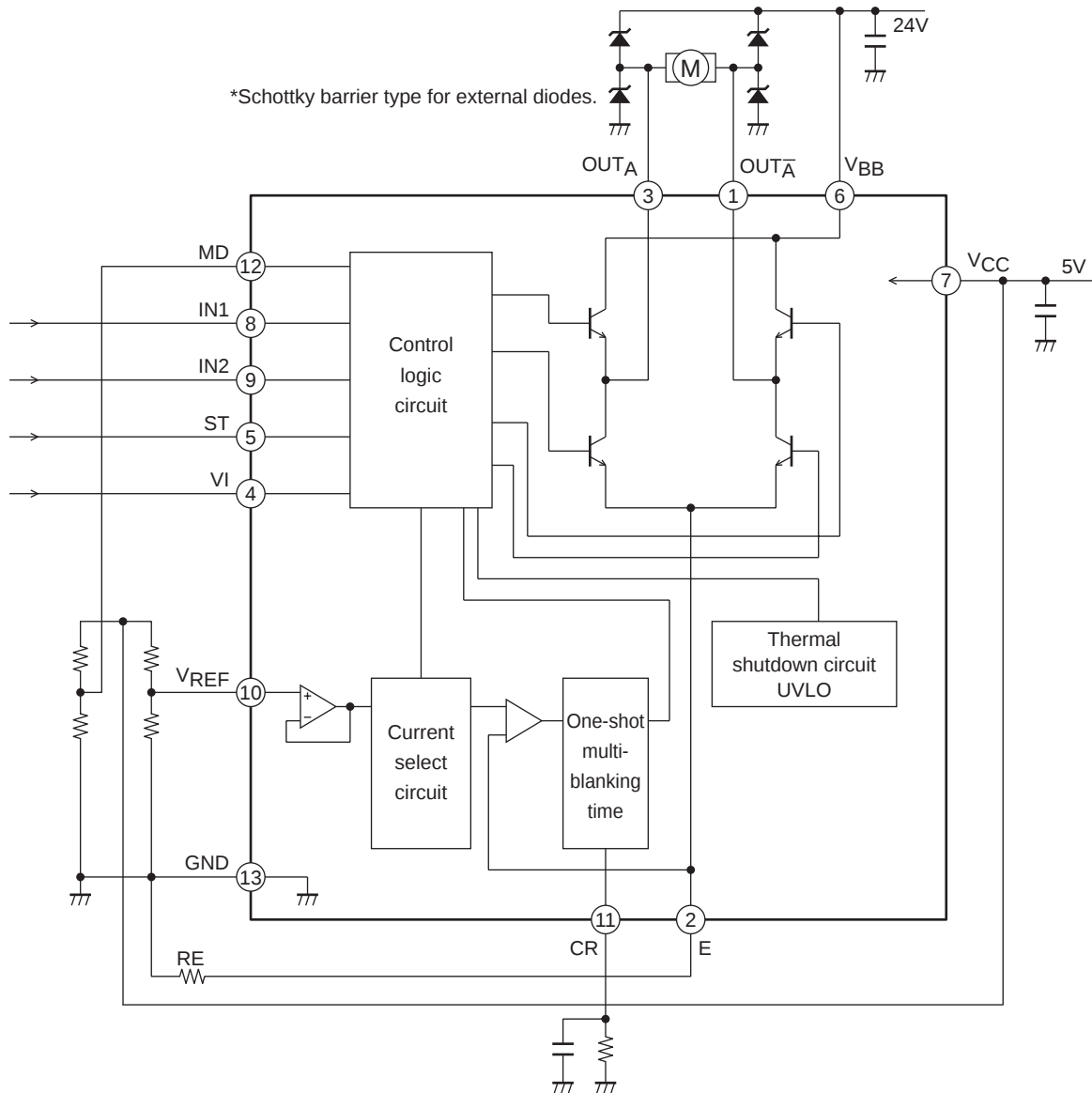
IN1	IN2	ST	OUT _A	OUT _A [̄]	Mode
H	L	H	H	L	Forward
H	H	H	L	H	Reverse
L	H	H	L	L	Brake
L	L	H	OFF	OFF	Output OFF
–	–	L	OFF	OFF	Standby mode

2. Forward/reverse motor



IN1	IN2	ST	OUT _A	OUT _A [̄]	Mode
H	L	H	H	L	Forward
H	H	H	L	H	Reverse
L	H	H	L	L	Brake
L	L	H	OFF	OFF	Output OFF
–	–	L	OFF	OFF	Standby mode

3. PWM current control forward/reverse motor (MIX DECAY)



Notes on Usage

1. VREF pin

Because the VREF pin serves for input of the set current reference voltage, precautions against noise must be taken.

2. GND pin

The ground circuit for this IC must be designed so as to allow for high-current switching. Blocks where high current flows must use low-impedance patterns and must be removed from small-signal lines. Especially the ground connection for the sensing resistor RE at pin E, and the ground connection for the Schottky barrier diodes should be in close proximity to the IC ground.

The capacitors between VCC and ground, and VBB and ground should be placed close to the VCC and VBB pins, respectively.

3. CR pin setting (Switching off time, Noise cancel time)

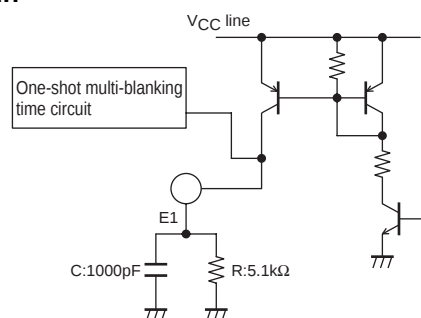
The noise cancel time (Tn) and the switching off time (Toff) are set by the following expressions:

$$\text{Noise cancel time: } T_n \approx C \times R \times \ln \{ (1.0 - R_I) / (4.0 - R_I) \} [\text{sec}]$$

CR charge current: 1.3mA

$$\text{Switching off time: } T_{\text{off}} \approx -C \times R \times \ln (1.0 / 4.8) [\text{sec}]$$

Internal configuration at CR pin



CR constant range: R = 4.7k to 100kΩ

C = 330pF to 2200pF

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