

Radiation Tolerant 20-Bit, 1MSPS, Low Power Plastic Package SAR ADC

FEATURES

- 10kRad (Si) Total Ionizing Dose (TID) per MIL-STD-883 TM1019 Condition A
- Single Event Latch-Up (SEL) Threshold Linear Energy Transfer (LET) $\geq 30.86\text{MeV}\cdot\text{cm}^2/\text{mg}$ at $T_{\text{CASE}} = 70^\circ\text{C}$
- Processed Using MIL-PRF-38535 Class N and PEM-INST-001 as a Guideline
- TID and SEL Reports Available
- 1MSPS Throughput Rate
- $\pm 0.5\text{ppm}$ INL (Typ)
- Guaranteed 20-Bit No Missing Codes
- Low Power: 21mW at 1MSPS, 21 μW at 1kSPS
- 104dB SNR (Typ) at $f_{\text{IN}} = 2\text{kHz}$
- -125dB THD (Typ) at $f_{\text{IN}} = 2\text{kHz}$
- Digital Gain Compression (DGC)
- 2.5V Supply
- Fully Differential Input Range $\pm V_{\text{REF}}$
- V_{REF} Input Range from 2.5V to 5.1V
- No Pipeline Delay, No Cycle Latency
- 1.8V to 5V I/O Voltages
- SPI-Compatible Serial I/O with Daisy-Chain Mode
- Internal Conversion Clock
- 16-Lead MSOP Package

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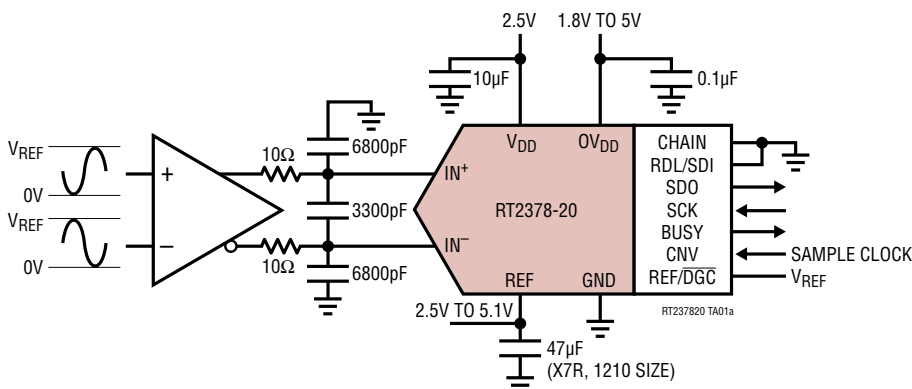
DESCRIPTION

The RT2378-20 is a low noise, low power, high speed 20-bit successive approximation register (SAR) ADC. Operating from a 2.5V supply, the RT2378-20 has a $\pm V_{\text{REF}}$ fully differential input range with V_{REF} ranging from 2.5V to 5.1V. The RT2378-20 consumes only 21mW and achieves $\pm 2\text{ppm}$ INL maximum, no missing codes at 20 bits with 104dB SNR.

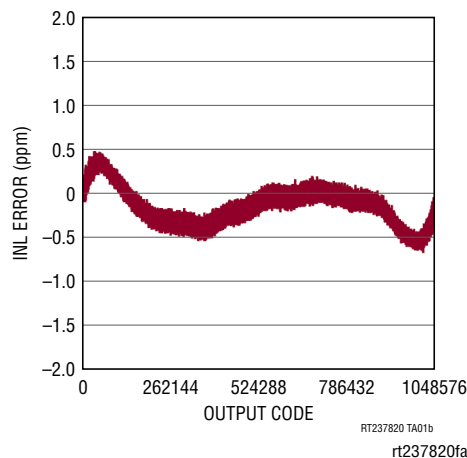
The RT2378-20 has a high speed SPI-compatible serial interface that supports 1.8V, 2.5V, 3.3V and 5V logic while also featuring a daisy-chain mode. The fast 1MSPS throughput with no cycle latency makes the RT2378-20 ideally suited for a wide variety of high speed applications. An internal oscillator sets the conversion time, easing external timing considerations. The RT2378-20 automatically powers down between conversions, leading to reduced power dissipation that scales with the sampling rate.

The RT2378-20 features a unique digital gain compression (DGC) function, which eliminates the driver amplifier's negative supply while preserving the full resolution of the ADC. When enabled, the ADC performs a digital scaling function that maps zero-scale code from 0V to $0.1 \cdot V_{\text{REF}}$ and full-scale code from V_{REF} to $0.9 \cdot V_{\text{REF}}$. For a typical reference voltage of 5V, the full-scale input range is now 0.5V to 4.5V, which provides adequate headroom for powering the driving amplifier from a single 5.5V supply.

TYPICAL APPLICATION



Integral Nonlinearity vs Output Code



RT2378-20

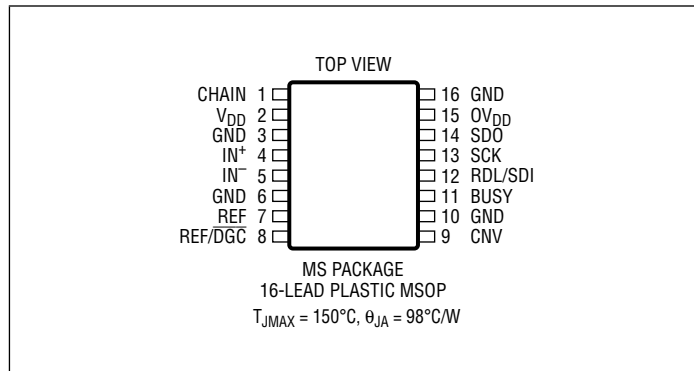
ABSOLUTE MAXIMUM RATINGS

(Notes 1, 2)

Supply Voltage (V_{DD})	2.8V
Supply Voltage (OV_{DD})	6V
Reference Input (REF)	6V
Analog Input Voltage (Note 3)	
IN^+ , IN^-	(GND – 0.3V) to (REF + 0.3V)
REF/DGC Input (Note 3)	(GND – 0.3V) to (REF + 0.3V)
Digital Input Voltage	
(Note 3)	(GND – 0.3V) to (OV_{DD} + 0.3V)

Digital Output Voltage	
(Note 3)	(GND – 0.3V) to (OV_{DD} + 0.3V)
Operating Temperature Range	
RT2378I	–40°C to 85°C
Storage Temperature Range	–65°C to 150°C

PIN CONFIGURATION



ORDER INFORMATION

LEAD FINISH	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	TEMPERATURE RANGE
RT2378IMS-20	RT2378IMS-20#TR	237820	16-Lead Plastic MSOP	–40°C to 85°C

Consult LTC Marketing for parts specified with wider operating temperature ranges.

For more information on lead free part marking, go to: <http://www.linear.com/leadfree/>

For more information on tape and reel specifications, go to: <http://www.linear.com/tapeandree/>. Some packages are available in 500 unit reels through designated sales channels with #TRMPBF suffix.

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS	SUB GROUP	MIN	TYP	MAX	UNITS
V_{IN+}	Absolute Input Range (IN^+)	(Note 5)	●	1, 2, 3	–0.1	$V_{REF} + 0.1$	V
V_{IN-}	Absolute Input Range (IN^-)	(Note 5)	●	1, 2, 3	–0.1	$V_{REF} + 0.1$	V
$V_{IN+} - V_{IN-}$	Input Differential Voltage Range	$V_{IN} = V_{IN+} - V_{IN-}$	●	1, 2, 3	– V_{REF}	$+V_{REF}$	V
V_{CM}	Common-Mode Input Range		●	1, 2, 3	$V_{REF}/2 - 0.1$	$V_{REF}/2 + 0.1$	V
I_{IN}	Analog Input Leakage Current				0.01		μA
C_{IN}	Analog Input Capacitance	Sample Mode Hold Mode			45 5		pF pF
CMRR	Input Common Mode Rejection Ratio	$f_{IN} = 500kHz$			86		dB

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CONVERTER CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
	Resolution		●		20			Bits
	No Missing Codes		●		20			Bits
	Transition Noise					2.3		ppm _{RMS}
INL	Integral Linearity Error	(Note 6) REF/DG $\overline{\text{C}}$ = GND (Note 6)	● ●	1, 2, 3 1, 2, 3	-2 -2	± 0.5 ± 0.5	2 2	ppm ppm
DNL	Differential Linearity Error	(Note 10)	●	1, 2, 3	-0.5	± 0.2	0.5	ppm
BZE	Bipolar Zero-Scale Error	(Note 7)	●	1, 2, 3	-13	0	13	ppm
	Bipolar Zero-Scale Error Drift					± 7		ppb/ $^\circ\text{C}$
FSE	Bipolar Full-Scale Error	(Note 7)	●	1, 2, 3	-100	± 10	100	ppm
	Bipolar Full-Scale Error Drift					± 0.05		ppm/ $^\circ\text{C}$

DYNAMIC ACCURACY

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$ and $A_{IN} = -1\text{dBFS}$. (Notes 4, 8)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
SINAD	Signal-to-(Noise + Distortion) Ratio	$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$	●	4, 5, 6	101	104		dB
SNR	Signal-to-Noise Ratio	$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$	●	4, 5, 6	101	104		dB
		$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$, REF/DG $\overline{\text{C}}$ = GND	●	4, 5, 6	99	102		dB
		$f_{IN} = 2\text{kHz}$, $V_{REF} = 2.5\text{V}$	●	4, 5, 6	95.4	98		dB
THD	Total Harmonic Distortion	$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$	●	4, 5, 6		-125	-114	dB
		$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$, REF/DG $\overline{\text{C}}$ = GND	●	4, 5, 6		-125	-114	dB
		$f_{IN} = 2\text{kHz}$, $V_{REF} = 2.5\text{V}$	●	4, 5, 6		-123	-113	dB
SFDR	Spurious Free Dynamic Range	$f_{IN} = 2\text{kHz}$, $V_{REF} = 5\text{V}$	●	4, 5, 6	115	128		dB
	-3dB Input Bandwidth					34		MHz
	Aperture Delay					500		ps
	Aperture Jitter					4		ps
	Transient Response	Full-Scale Step				312		ns

REFERENCE INPUT

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
V_{REF}	Reference Voltage	(Note 5)	●	1, 2, 3	2.5		5.1	V
I_{REF}	Reference Input Current	(Note 9)	●	1, 2, 3		0.94	1.1	mA
$V_{IH\overline{\text{DG}}\overline{\text{C}}}$	High Level Input Voltage REF/DG $\overline{\text{C}}$ Pin		●	1, 2, 3	$0.8V_{REF}$			V
$V_{IL\overline{\text{DG}}\overline{\text{C}}}$	Low Level Input Voltage REF/DG $\overline{\text{C}}$ Pin		●	1, 2, 3			$0.2V_{REF}$	V

DIGITAL INPUTS AND DIGITAL OUTPUTS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
V_{IH}	High Level Input Voltage		●	1, 2, 3	$0.8 \cdot OV_{DD}$			V
V_{IL}	Low Level Input Voltage		●	1, 2, 3			$0.2 \cdot OV_{DD}$	V
I_{IN}	Digital Input Current	$V_{IN} = 0V$ to OV_{DD}	●	1, 2, 3	-10		10	μA
C_{IN}	Digital Input Capacitance					5		pF
V_{OH}	High Level Output Voltage	$I_O = -500\mu\text{A}$	●	1, 2, 3	$OV_{DD} - 0.2$			V
V_{OL}	Low Level Output Voltage	$I_O = 500\mu\text{A}$	●	1, 2, 3			0.2	V
I_{OZ}	Hi-Z Output Leakage Current	$V_{OUT} = 0V$ to OV_{DD}	●	1, 2, 3	-10		10	μA
I_{SOURCE}	Output Source Current	$V_{OUT} = 0V$				-10		mA
I_{SINK}	Output Sink Current	$V_{OUT} = OV_{DD}$				10		mA

POWER REQUIREMENTS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
V_{DD}	Supply Voltage		●	1, 2, 3	2.375	2.5	2.625	V
OV_{DD}	Supply Voltage		●	1, 2, 3	1.71		5.25	V
I_{VDD}	Supply Current	1Msps Sample Rate	●	1, 2, 3		8.4	10	mA
I_{OVDD}	Supply Current	1Msps Sample Rate ($C_L = 20\text{pF}$)				0.2		mA
I_{PD}	Power Down Mode	Conversion Done ($I_{VDD} + I_{OVDD} + I_{REF}$)	●	1, 2, 3		1	90	μA
P_D	Power Dissipation	1Msps Sample Rate		1		21	25	mW
	Power Down Mode	Conversion Done ($I_{VDD} + I_{OVDD} + I_{REF}$)		1		2.5	225	μW

ADC TIMING CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
f_{SMPL}	Maximum Sampling Frequency		●	9, 10, 11			1	Msps
t_{CONV}	Conversion Time		●	9, 10, 11	615		675	ns
t_{ACQ}	Acquisition Time	$t_{ACQ} = t_{CYC} - t_{CONV} - t_{BUSYLH}$ (Note 10)	●		312			ns
t_{CYC}	Time Between Conversions		●	9, 10, 11	1			μs
t_{CNVH}	CNV High Time		●	9, 10, 11	20			ns
t_{BUSYLH}	CNV $\uparrow$ to BUSY Delay	$C_L = 20\text{pF}$	●	9, 10, 11			13	ns
t_{CNVL}	Minimum Low Time for CNV	(Note 11)	●	9, 10, 11	20			ns
t_{QUIET}	SCK Quiet Time from CNV $\uparrow$	(Note 10)	●		20			ns
t_{SCK}	SCK Period	(Notes 11, 12)	●	9, 10, 11	10			ns
t_{SCKH}	SCK High Time		●	9, 10, 11	4			ns
t_{SCKL}	SCK Low Time		●	9, 10, 11	4			ns
$t_{SSDISCK}$	SDI Setup Time From SCK $\uparrow$	(Note 11)	●	9, 10, 11	4			ns
$t_{HSDISCK}$	SDI Hold Time From SCK $\uparrow$	(Note 11)	●	9, 10, 11	1			ns
t_{SCKCH}	SCK Period in Chain Mode	$t_{SCKCH} = t_{SSDISCK} + t_{DSDO}$ (Note 11)	●	9, 10, 11	13.5			ns

ADC TIMING CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

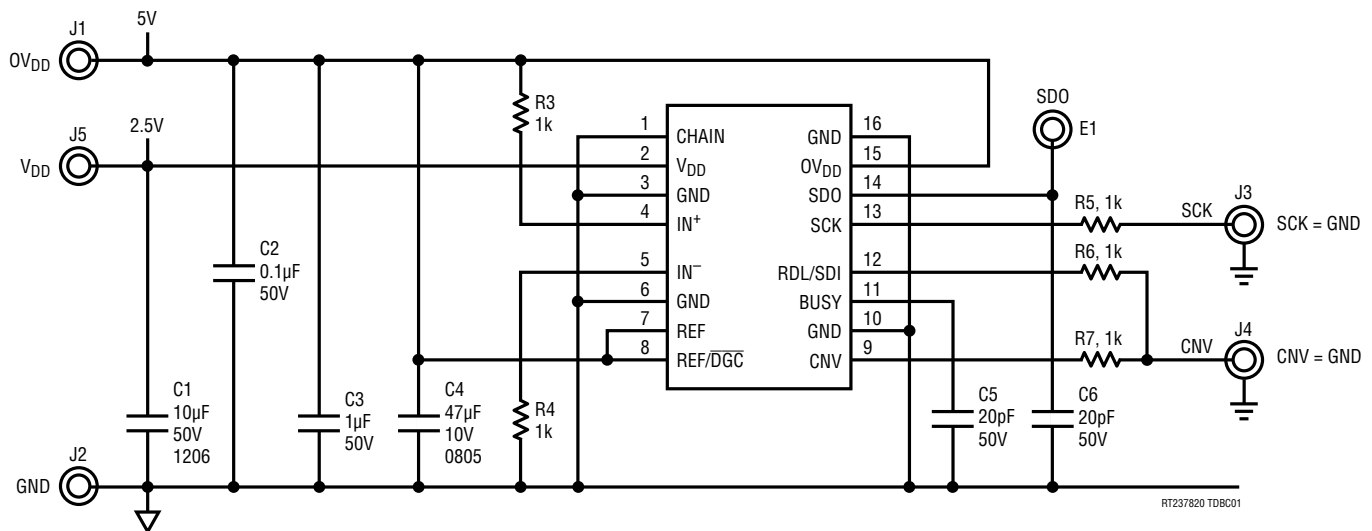
SYMBOL	PARAMETER	CONDITIONS		SUB GROUP	MIN	TYP	MAX	UNITS
t_{DSO}	SDO Data Valid Delay from SCK $\uparrow$	$C_L = 20\text{pF}$, $\text{OV}_{\text{DD}} = 5.25\text{V}$	●	9, 10, 11			7.5	ns
		$C_L = 20\text{pF}$, $\text{OV}_{\text{DD}} = 2.5\text{V}$	●	9, 10, 11			8	ns
		$C_L = 20\text{pF}$, $\text{OV}_{\text{DD}} = 1.71\text{V}$	●	9, 10, 11			9.5	ns
t_{HSDO}	SDO Data Remains Valid Delay from SCK $\uparrow$	$C_L = 20\text{pF}$ (Note 10)	●		1			ns
$t_{\text{DSDOBUSYL}}$	SDO Data Valid Delay from BUSY $\downarrow$	$C_L = 20\text{pF}$ (Note 10)	●				5	ns
t_{EN}	Bus Enable Time After RDL $\downarrow$	(Note 11)	●	9, 10, 11			16	ns
t_{DIS}	Bus Relinquish Time After RDL $\uparrow$	(Note 11)	●	9, 10, 11			13	ns

CONVERTER CHARACTERISTICS

(Post-Irradiation) The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. (Note 4)

SYMBOL	PARAMETER	CONDITIONS	10kRAD(SI)			UNITS
			MIN	TYP	TYP	
INL	Integral Linearity Error	(Note 6) REF/DGC = GND (Note 6)	-3.5 -3.5		3.5 3.5	ppm ppm

TOTAL DOSE BIAS CIRCUIT



ELECTRICAL TEST REQUIREMENTS

MIL-STD-883 TEST REQUIREMENTS	SUBGROUP
Final Electrical Test Requirements (Method 5004)	1, * 2, 3, 4, 5, 6, 9, 10, 11
Group A Test Requirements (Method 5005)	1, 2, 3, 4, 5, 6, 9, 10, 11
Group B and D for Class S. End Point Electrical Parameters (Method 5005)	1, 2, 3

*PDA applies to subgroup 1. See PDA Test Notes.

PDA Test Notes

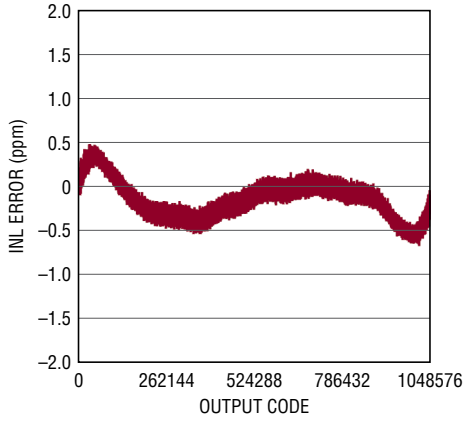
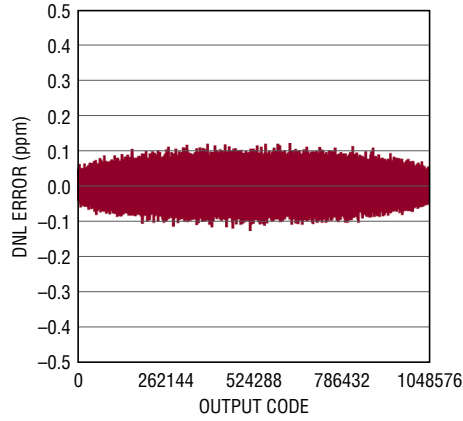
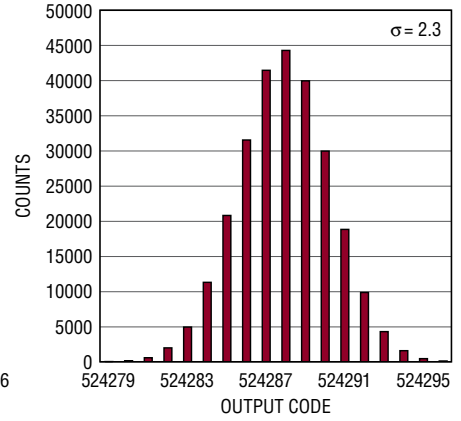
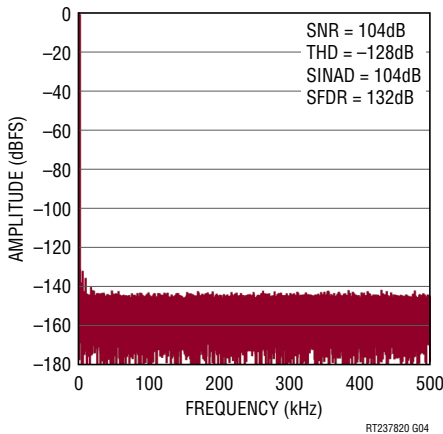
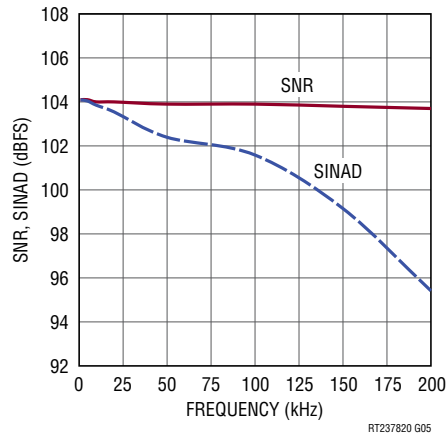
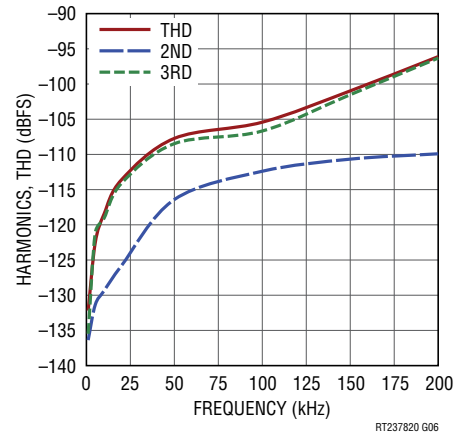
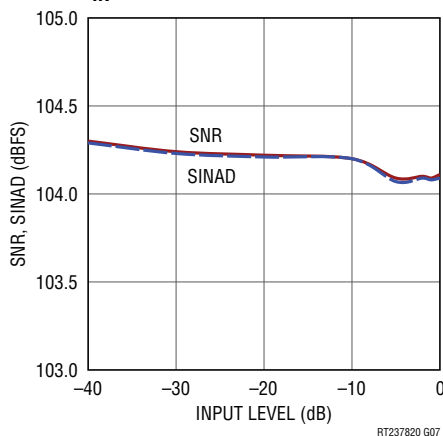
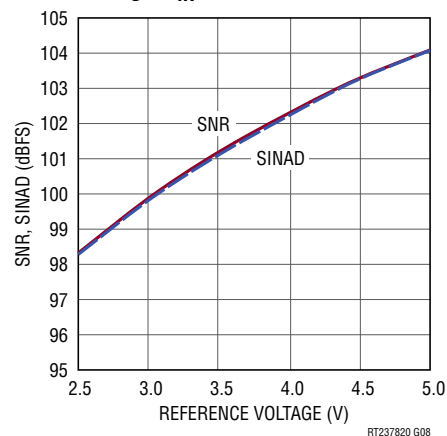
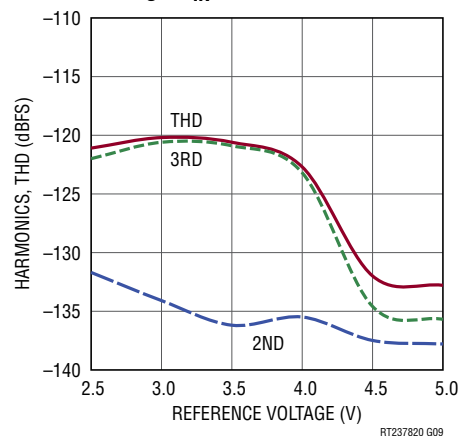
The PDA is specified as 5% based on failures from Group A, Subgroup 1, tests after cooldown as the final electrical test in accordance with method 5004 of MIL-STD-883. The verified failures of Group A, Subgroup 1, after burn-in divided by the total number of devices submitted for burn-in in that lot shall be used to determine the percent for the lot.

Linear Technology Corporation reserves the right to test to tighter limits than those given.

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TYPICAL PERFORMANCE CHARACTERISTICS

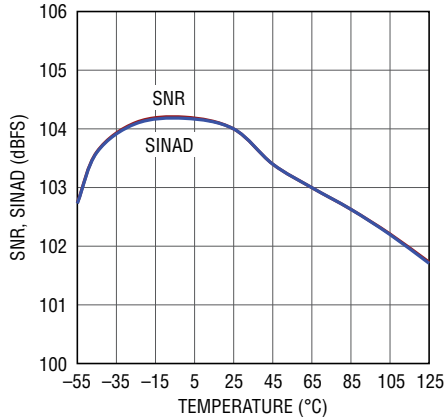
$T_A = 25^\circ\text{C}$, $V_{DD} = 2.5\text{V}$, $0V_{DD} = 2.5\text{V}$, $V_{CM} = 2.5\text{V}$,
 $REF = 5\text{V}$, $f_{SAMPL} = 1\text{Msps}$, unless otherwise noted.

**Integral Nonlinearity
vs Output Code****Differential Nonlinearity
vs Output Code****DC Histogram****128k Point FFT $f_{SAMPL} = 1\text{Msps}$,
 $f_{IN} = 2\text{kHz}$** **SNR, SINAD vs Input Frequency****THD, Harmonics
vs Input Frequency****SNR, SINAD vs Input level,
 $f_{IN} = 2\text{kHz}$** **SNR, SINAD vs Reference
Voltage, $f_{IN} = 2\text{kHz}$** **THD, Harmonics vs Reference
Voltage, $f_{IN} = 2\text{kHz}$** 

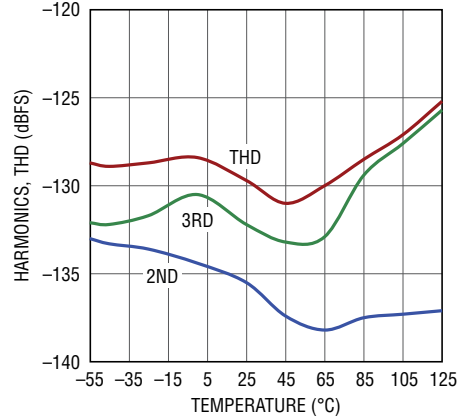
TYPICAL PERFORMANCE CHARACTERISTICS

$T_A = 25^\circ\text{C}$, $V_{DD} = 2.5\text{V}$, $0V_{DD} = 2.5\text{V}$, $V_{CM} = 2.5\text{V}$,
REF = 5V, $f_{SAMPL} = 1\text{Msps}$, unless otherwise noted.

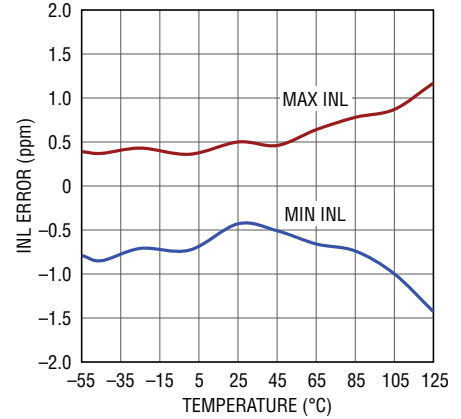
SNR, SINAD vs Temperature,
 $f_{IN} = 2\text{kHz}$



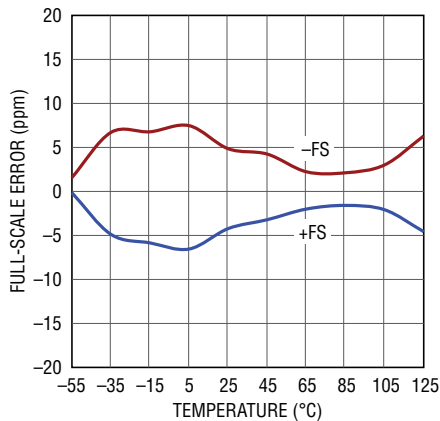
THD, Harmonics vs Temperature,
 $f_{IN} = 2\text{kHz}$



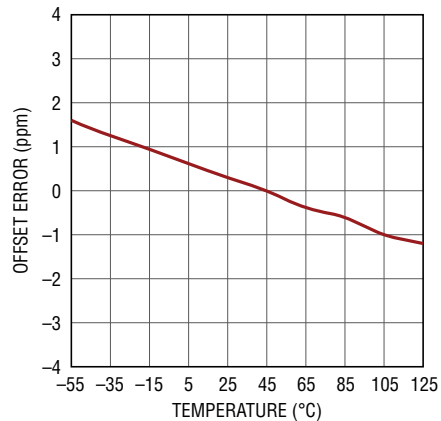
INL vs Temperature



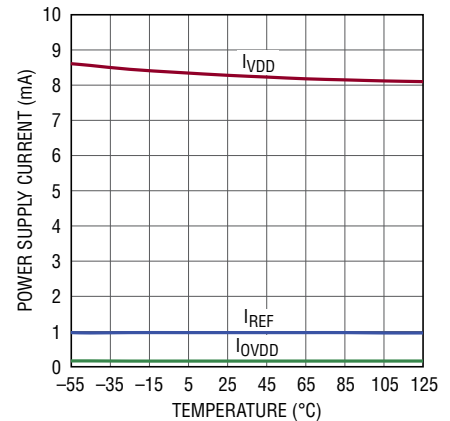
Full-Scale Error vs Temperature



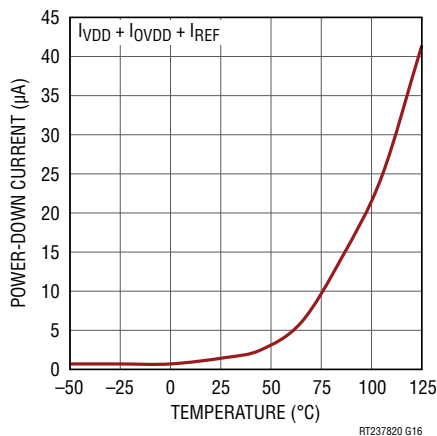
Offset Error vs Temperature



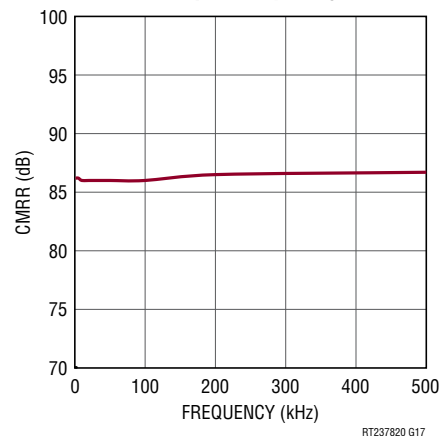
Supply Current vs Temperature



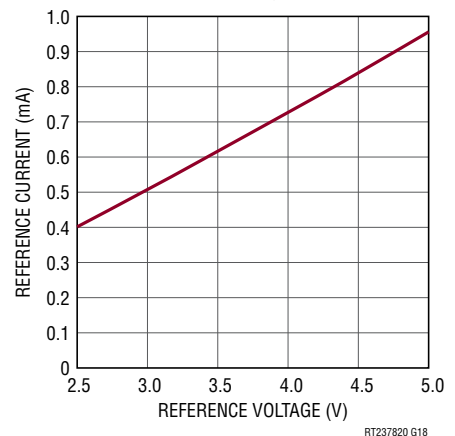
Shutdown Current vs Temperature



CMRR vs Input Frequency



Reference Current vs Reference Voltage



REVISION HISTORY

REV	DATE	DESCRIPTION	PAGE NUMBER
A	11/16	Updated subgroup under Power Requirements	4

PACKAGE DESCRIPTION

Please refer to <http://www.linear.com/designtools/packaging/> for the most recent package drawings.

MS Package 16-Lead Plastic MSOP (Reference LTC DWG # 05-08-1669 Rev A)

