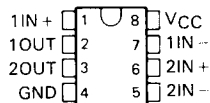


SN55157, SN75157 DUAL DIFFERENTIAL LINE RECEIVER

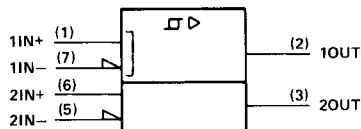
D2300, SEPTEMBER 1980—REVISED SEPTEMBER 1986

- Meets EIA Standards RS-422-A and RS-423-A
- Meets Federal Standards 1020 and 1030
- Operates from Single 5-V Power Supply
- Wide Common-Mode Voltage Range
- High Input Impedance
- TTL-Compatible Outputs
- High-Speed Schottky Circuitry
- 8-Pin Dual-In-Line Package
- Similar to uA9637AC except for Corner VCC and Ground Pin Positions

SN55157 . . . JG PACKAGE
SN75157 . . . D, JG, OR P PACKAGE
(TOP VIEW)



logic symbol†



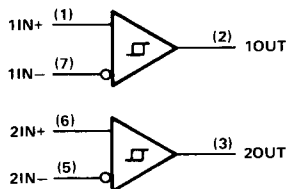
†This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

description

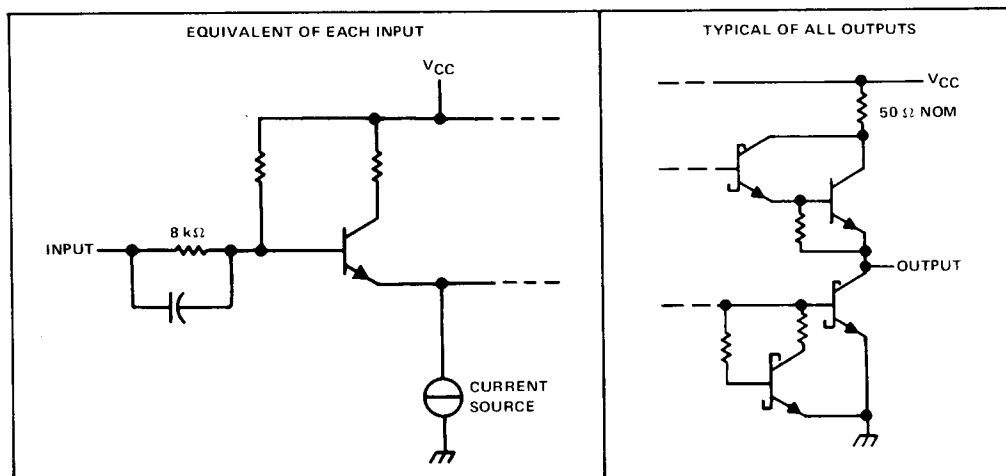
The SN75157 is a dual differential line receiver designed to meet EIA standards RS-422-A and RS-423-A and Federal Standards 1020 and 1030. It utilizes Schottky circuitry and has TTL-compatible outputs. The inputs are compatible with either a single-ended or a differential-line system. The device operates from a single 5-volt power supply and is supplied in an 8-pin dual-in-line package and small outline package.

The SN55157 is characterized over the full military temperature range of -55°C to 125°C . The SN75157 is characterized for operation from 0°C to 70°C .

logic diagram



schematics of inputs and outputs



PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS
INSTRUMENTS

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1980, Texas Instruments Incorporated

SN55157, SN75157

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	−0.5 V to 7 V
Input voltage	±15 V
Differential input voltage (see Note 2)	±15 V
Output voltage (see Note 1)	−0.5 V to 5.5 V
Low-level output current	50 mA
Continuous total dissipation at (or below) 25°C free-air temperature (see Note 3):	
SN55157 JG package	1050 mW
SN75157 D package	725 mW
JG package	825 mW
P package	1000 mW
Operating free-air temperature range: SN55157	−55°C to 125°C
SN75157	0°C to 70°C
Storage temperature range	−65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds JG package	300°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds D or P package	260°C

NOTES: 1. All voltage values, except differential input voltage, are with respect to the network ground terminal.
2. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.
3. For operation above 25 °C free-air temperature, derate the SN55157 JG package to 672 mW at 70 °C at the rate of 8.4 mW/°C, the SN75157 JG package to 528 mW at 70 °C at the rate of 6.6 mW/°C, the D package to 464 mW at 70 °C at the rate of 5.8 mW/°C, and the P package to 640 mW at 70 °C at the rate of 8.0 mW/°C. In the JG package, SN55157 chips are alloy mounted and SN75157 chips are glass mounted.

recommended operating conditions

		MIN	NOM	MAX	UNIT
Supply voltage, V_{CC}		4.75	5	5.25	V
Common-mode input voltage, V_{IC}		± 7			V
Operating free-air temperature, T_A	SN55157	-55	25	125	°C
	SN75157	0	25	70	

electrical characteristics over recommended ranges of supply voltage, common-mode input voltage, and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN TYP [†] MAX See Note 4		UNIT
V _T	Threshold voltage (V _{T+} and V _{T−})			−0.2	0.2	V
		See Note 5		−0.4	0.4	
V _{hys}	Hysteresis (V _{T+} − V _{T−})			70		mV
V _{OH}	High-level output voltage	V _{ID} = 0.2 V,	I _O = −1 mA	2.5	3.5	V
V _{OL}	Low-level output voltage	V _{ID} = −0.2 V,	I _O = 20 mA	0.35	0.5	V
I _I	Input current	V _{CC} = 0 to 5.5 V, See Note 6	V _I = 10 V V _I = −10 V	1.1	3.25 −1.6 − 3.25	mA
I _{OS}	Short-circuit output current [‡]	V _O = 0,	V _{ID} = 0.2 V	−40	−75 − 100	
I _{CC}	Supply current	V _{ID} = −0.5 V,	No load	35	50	mA

[†]All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

[‡]Only one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

NOTES: 4. The algebraic convention, where the less-positive (more-negative) limit is designated as minimum, is used in this data sheet for threshold levels only.

5. The expanded threshold parameter is tested with a 500- Ω resistor in series with each input.

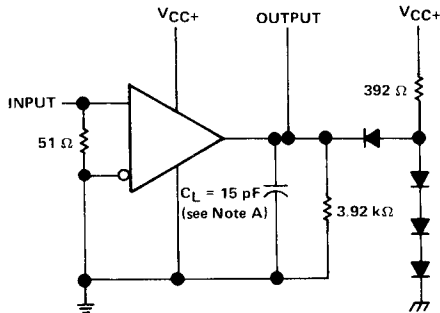
6. The input not under test is grounded.

SN55157, SN75157 DUAL DIFFERENTIAL LINE RECEIVER

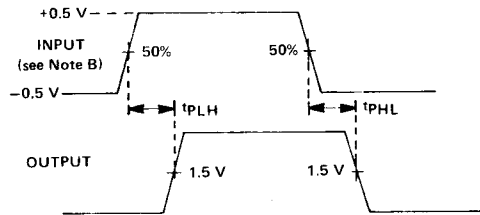
switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
t_{PLH} Propagation delay time, low-to-high-level output	$C_L = 15 \text{ pF}$, See Figure 1		15	25	ns
t_{PHL} Propagation delay time, high-to-low-level output			13	25	ns

PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE WAVEFORMS

NOTES: A. C_L includes probe and jig capacitance.

B. The input pulse is supplied by a generator having the following characteristics: $t_r \leq 5 \text{ ns}$, $t_f \leq 5 \text{ ns}$, $\text{PRR} \leq 5 \text{ MHz}$, duty cycle = 50%.

FIGURE 1. TRANSITION TIMES

TYPICAL CHARACTERISTICS

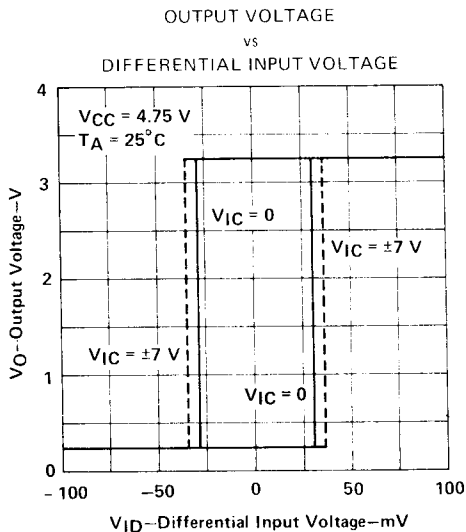


FIGURE 2

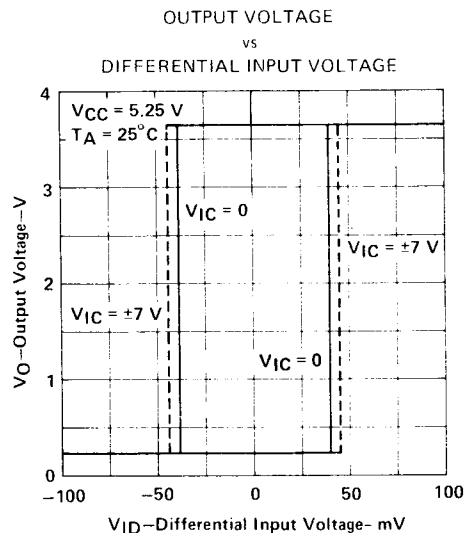


FIGURE 3

TYPICAL CHARACTERISTICS

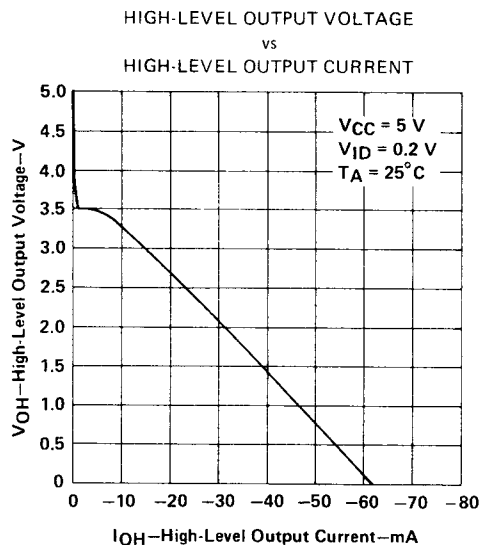


FIGURE 4

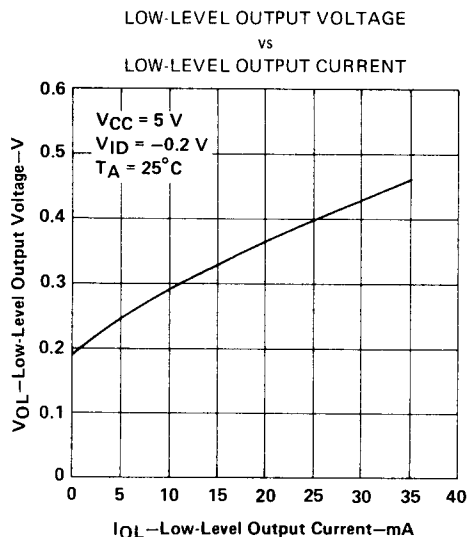


FIGURE 5

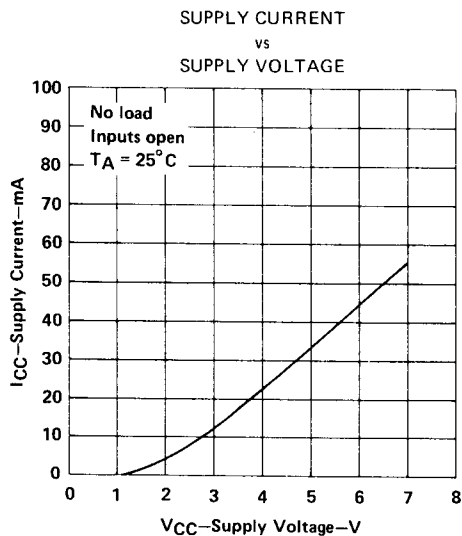


FIGURE 6

TYPICAL APPLICATION DATA

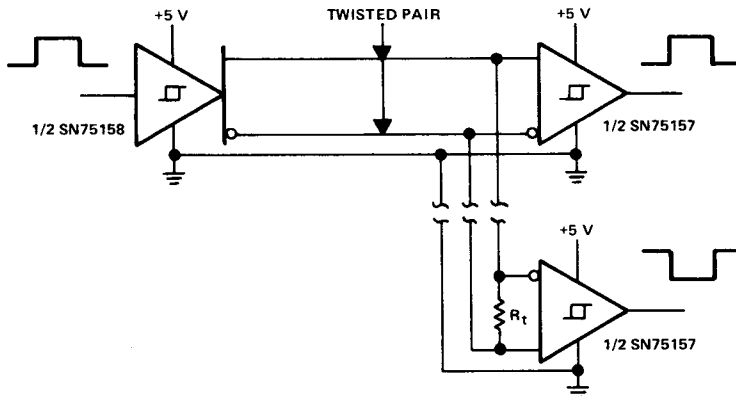


FIGURE 7. RS-422-A SYSTEM APPLICATIONS