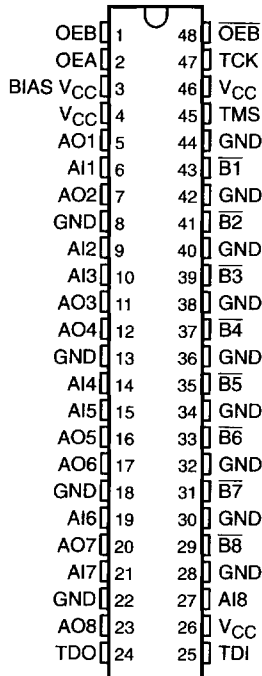


SN54FB2040, SN74FB2040 8-BIT TTL/BTL TRANSCEIVERS

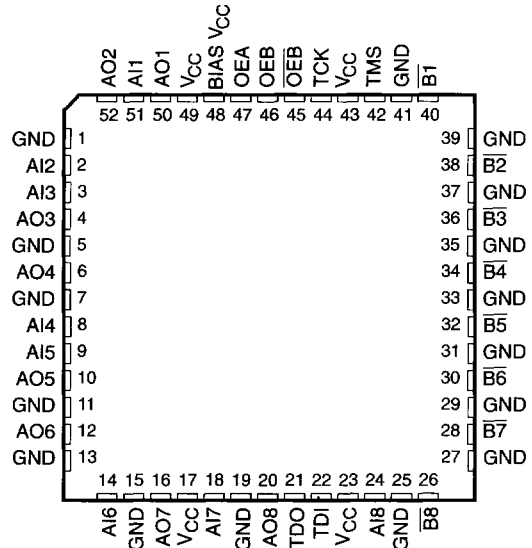
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- Compatible With IEEE 1194.1-1991 (BTL) and IEEE 896.2-1991 (Futurebus+) Standards
- TTL A Port, Backplane Transceiver Logic $\bar{B}$ Port
- Open-Collector $\bar{B}$ -Port Outputs Sink 100 mA
- Minimum $\bar{B}$ -Port Edge Rate = 2 ns
- Isolated Logic-Ground and Bus-Ground Pins Reduce Noise
- BIAS V_{CC} Pin Minimizes Signal Distortion During Live Insertion/Withdrawal
- Available in Plastic Quad Flatpack (RC) and Ceramic Flatpack (WD) Packages
- $\bar{B}$ -Port Biasing Network Preconditions the Connector and PC Trace to the Backplane Transceiver Logic High-Level Voltage

SN54FB2040 . . . WD PACKAGE
(TOP VIEW)



SN74FB2040 . . . RC PACKAGE
(TOP VIEW)



description

The 'FB2040 is an 8-bit transceiver designed to translate signals between TTL and backplane transceiver logic (BTL) environments. It is specifically designed to be compatible with IEEE 1194.1-1991 (BTL) and IEEE 896.2-1991 (Futurebus+) standards.

The $\bar{B}$ port operates at BTL-signal levels. The open-collector $\bar{B}$ ports are specified to sink 100 mA and have minimum output edge rates of 2 ns. Two output enables, OEB and $\bar{OEB}$, are provided for the $\bar{B}$ outputs. When OEB is high and $\bar{OEB}$ is low, the $\bar{B}$ port is active and reflects the inverse of the data present at the A-input pins. When OEB is low, $\bar{OEB}$ is high, or V_{CC} is typically less than 2.5 V, the $\bar{B}$ port is turned off.

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description (continued)

The A port operates at TTL-signal levels and has split input and output pins. The A outputs reflect the inverse of the data at the $\bar{B}$ port when the A-port output enable, OEA, is high. When OEA is low or when V_{CC} is typically less than 2.5 V, the A outputs are in the high-impedance state.

Pins are allocated for the four-wire IEEE 1149.1 (JTAG) test bus, which will be implemented in a future version of the 'FB2040. Currently TMS and TCK are not connected and TDI is shorted to TDO.

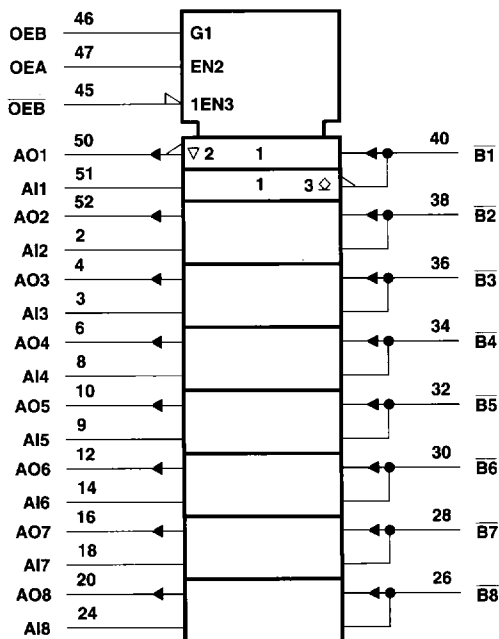
BIAS V_{CC} establishes a voltage between 1.62 V and 2.1 V on the BTL outputs when V_{CC} is not connected.

The SN54FB2040 is characterized for operation over the full military temperature range of -55°C to 125°C . The SN74FB2040 is characterized for operation from 0°C to 70°C .

FUNCTION TABLE

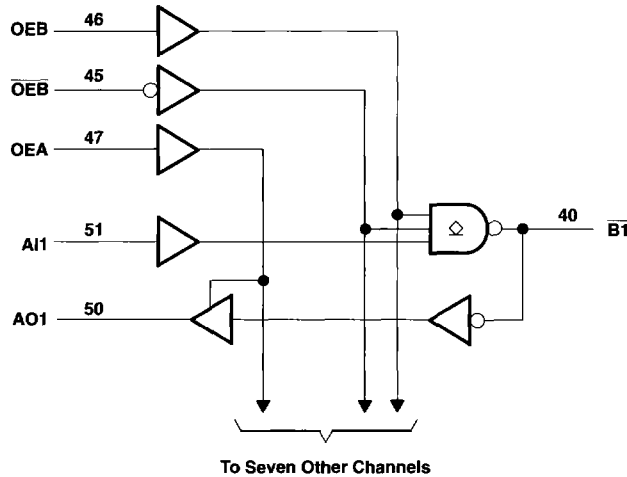
INPUTS			FUNCTION
OEB	$\bar{\text{OEB}}$	OEA	
L	X	L	Isolation
X	H	L	
L	X	H	$\bar{B}$ data to AO bus
X	H	H	
H	L	L	$\bar{A}$ data to B bus
H	L	H	$\bar{A}$ data to B bus, $\bar{B}$ data to AO bus

logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the RC package.

functional block diagram



Pin numbers shown are for the RC package.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC}	–0.5 V to 7 V
Input voltage range, V_I (except $\bar{B}$ port)	–1.2 V to 7 V
V_I ($\bar{B}$ port)	–1.2 V to 3.5 V
Input current range (except $\bar{B}$ port)	–18 mA to 5 mA
Voltage range applied to any $\bar{B}$ output in the disabled or power-off state	–0.5 V to 5.5 V
Voltage range applied to any output in the high state	–0.5 V to V_{CC}
Current applied to any single output in the low state: A port	96 mA
($\bar{B}$ port)	200 mA
Maximum power dissipation at $T_A = 55^\circ\text{C}$ (in still air): RC package	0.85 W
Storage temperature range	–65°C to 150°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

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recommended operating conditions (see Note 1)

			SN54FB2040			SN74FB2040			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} , BIAS V _{CC}	Supply voltage		4.5	5	5.5	4.5	5	5.5	V
V _{IH}	High-level input voltage	B port	1.62	2.3		1.62	2.3		V
		Except B port	2		2				
V _{IL}	Low-level input voltage	B port	0.75	1.47		0.75	1.47		V
		Except B port	0.8		0.8				
I _{IK}	Input clamp current		-18			-18			mA
I _{OH}	High-level output current	AO port				-3			mA
I _{OL}	Low-level output current	AO port				24			mA
		B port	100		100				
T _A	Operating free-air temperature		-55	125		0	70		°C

NOTE 1: Unused or floating pins (input or I/O) must be held high or low.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN54FB2040		SN74FB2040		UNIT	
				MIN	TYP†	MAX	MIN		TYP†
V _{IK}	B port	V _{CC} = 4.5 V,	I _I = -18 mA				-1.2	V	
	Except $\bar{B}$ port	V _{CC} = 4.5 V,	I _I = -40 mA				-0.5		
V _{OH}	AO port	V _{CC} = 4.5 V	I _{OH} = -1 mA					V	
			I _{OH} = -3 mA			2.5	3.3		
V _{OL}	AO port	V _{CC} = 4.5 V	I _{OL} = 20 mA					V	
			I _{OL} = 24 mA				0.35		0.5
	$\bar{B}$ port	V _{CC} = 4.5 V	I _{OL} = 80 mA				0.75		1.1
			I _{OL} = 100 mA						1.15
I _I	Except $\bar{B}$ port	V _{CC} = 5.5 V,	V _I = 5.5 V				50	μA	
I _{IH} ‡	Except $\bar{B}$ port	V _{CC} = 5.5 V,	V _I = 2.7 V				50	μA	
I _{IL} ‡	Except $\bar{B}$ port	V _{CC} = 5.5 V,	V _I = 0.5 V				-50	μA	
	B port†	V _{CC} = 5.5 V,	V _I = 0.75 V				-100		
I _{OH}	$\bar{B}$ port	V _{CC} = 0 to 5.5 V,	V _O = 2.1 V				100	μA	
I _{OZH}	AO port	V _{CC} = 5.5 V,	V _O = 2.7 V				50	μA	
I _{OZL}	AO port	V _{CC} = 5.5 V,	V _O = 0.5 V				-50	μA	
I _{OS} §	AO port	V _{CC} = 5.5 V,	V _O = 0				-30	-150	mA
I _{CC}	AI port to $\bar{B}$ port	V _{CC} = 5.5 V,	I _O = 0				25	mA	
	$\bar{B}$ port to AO port						60		
	Outputs disabled								
C _i	AI port and control inputs	V _I = V _{CC} or GND						pF	
C _o	AO port	V _O = V _{CC} or GND						pF	
C _{io}	$\bar{B}$ port per P1194.0	V _{CC} = 0 to 4.5 V					6	pF	
		V _{CC} = 4.5 V to 5.5 V					5		

† All typical values are at $V_{CC} = 5$ V, $T_A = 25^\circ\text{C}$.

‡ For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

§ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

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switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 5 V, T _A = 25°C			SN54FB2040		SN74FB2040		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A1	$\bar{B}$	3.9							ns
t _{PHL}			3.6							
t _{PLH}	$\bar{B}$	AO	3.9							ns
t _{PHL}			3.8							
t _{PLH}	OEB	$\bar{B}$	5.1							ns
t _{PHL}			4.3							
t _{PLH}	OEB	$\bar{B}$	4.4							ns
t _{PHL}			4.1							
t _{PZH}	OEA	AO	3.2							ns
t _{PZL}			3							
t _{PHZ}	OEA	AO	3.2							ns
t _{PLZ}			2.7							
t _{sk(p)}	Skew for any single channel t _{PHL} – t _{PLH}	A1 to $\bar{B}$ or $\bar{B}$ to AO						0.75		ns
t _{sk(o)}	Skew between drivers in the same package	A1 to $\bar{B}$ or $\bar{B}$ to AO	1 1.5					2		ns
t _t	Transition time, $\bar{B}$ outputs (1.3 V to 1.8 V)		2					1 3		ns
t _{PR}	$\bar{B}$ -port input pulse rejection							1		ns

live insertion specifications over recommended operating free-air temperature range

PARAMETER		TEST CONDITIONS		SN54FB2040		SN74FB2040		UNIT
				MIN	MAX	MIN	MAX	
I_{CC} (BIAS V_{CC})		$V_{CC} = 0$ to 4.5 V	$V_B = 0$ to 2 V, V_I (BIAS V_{CC}) = 4.5 V to 5.5 V				450	μA
		$V_{CC} = 4.5$ V to 5.5 V					10	
V_O	$\bar{B}$ port	$V_{CC} = 0$, V_I (BIAS V_{CC}) = 4.5 V to 5.5 V				1.62	2.1	V
I_O	$\bar{B}$ port	$V_{CC} = 0$, $V_B = 1$ V, V_I (BIAS V_{CC}) = 4.5 V to 5.5 V				-1		μA
		$V_{CC} = 0$ to 5.5 V, $OEB = 0$ to 0.8 V					100	
		$V_{CC} = 0$ to 2.2 V, $OEB = 0$ to 5 V					100	

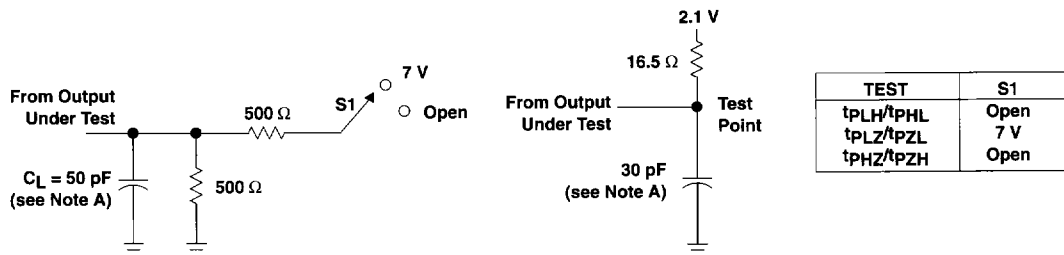
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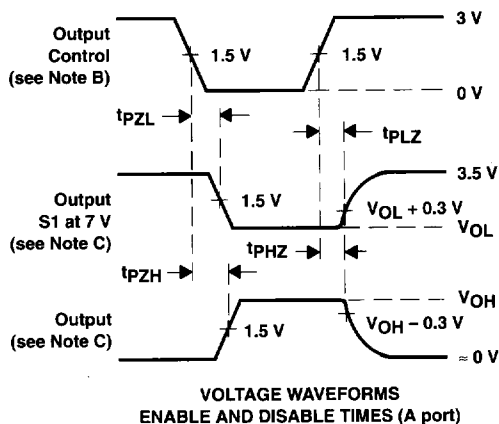
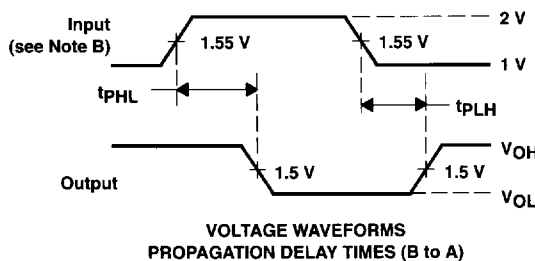
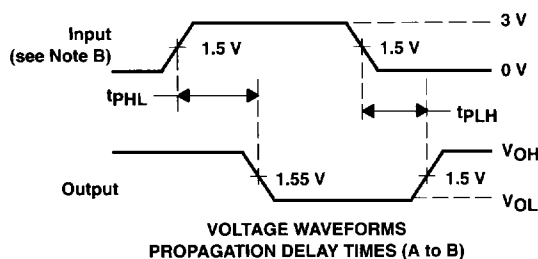
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PARAMETER MEASUREMENT INFORMATION



LOAD CIRCUIT FOR A OUTPUTS

LOAD CIRCUIT FOR B OUTPUTS



- NOTES: A. C_L includes probe and jig capacitance.
B. All input pulses are supplied by generators having the following characteristics: TTL Inputs - PRR $\leq$ 10 MHz, $Z_O = 50 \Omega$, $t_r \leq 2.5$ ns, $t_f \leq 2.5$ ns. BTL Inputs - PRR $\leq$ 10 MHz, $Z_O = 50 \Omega$, $t_r \leq 2.5$ ns, $t_f \leq 2.5$ ns.
C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
D. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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