

# TinyLogic ULP-A 1-of-2 Decoder/Demultiplexer

# NC7SP19

The NC7SP19 is a 1-of-2 decoder/demultiplexer in tiny footprint packages. The device is designed to operate for  $V_{CC} = 0.9 \text{ V}$  to 3.6 V.

#### **Features**

- Designed for 0.9 V to 3.6 V V<sub>CC</sub> Operation
- 2.3 ns t<sub>PD</sub> at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I<sub>OFF</sub> Supports Partial Power Down Protection
- Source/Sink 2.6 mA at 3.3 V
- Available in SC−88 and MicroPak<sup>™</sup> Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

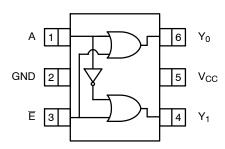


Figure 1. SC-88 (Top View)

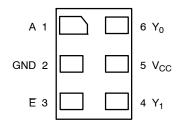


Figure 2. MicroPak (Top Through View)

# **PIN ASSIGNMENT**

Pin	Function
1	Α
2	GND
3	Ē
4	Y <sub>1</sub>
5	V <sub>CC</sub>
6	Y <sub>0</sub>

# MARKING DIAGRAMS



CC

SIP6 1.45X1.0 MicroPak CASE 127EB

CCKK Ak ZEB

= Specific Device Code

KK = 2-Digit Lot Run Traceability Code

XY = 2-Digit Date Code Z = Assembly Plant Code



SC-88 DF SUFFIX CASE 419B-02



XXX = Specific Device Code

1 = Date Code

= Pb-Free Package

#### ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 6 of this data sheet.

# **FUNCTION TABLE**

Inp	uts	Output		
Α	Ē	$Y_0 = A + \overline{E}$	$Y_1 = \overline{A} + \overline{E}$	
L	L	L	Н	
Н	L	Н	L	
Χ	Н	Н	Н	

H = HIGH Logic Level

L = LOW Logic Level

X = Don't Care

# **MAXIMUM RATINGS**

Symbol	Characterist	ics	Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +4.3	V
V <sub>IN</sub>	DC Input Voltage		-0.5 to +4.3	V
V <sub>OUT</sub>	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V <sub>CC</sub> = 0 V)	-0.5 to V <sub>CC</sub> + 0.5 -0.5 to +4.3 -0.5 to +4.3	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < GND	<b>–50</b>	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> < GND	-50	mA
l <sub>OUT</sub>	DC Output Source/Sink Current		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC Supply Current per Supply Pin or Ground	Pin	±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
TL	Lead Temperature, 1 mm from Case for 10 Se	econds	260	°C
TJ	Junction Temperature Under Bias		+150	°C
$\theta_{\sf JA}$	Thermal Resistance (Note 2)	SC-88 MicroPak	377 154	°C/W
P <sub>D</sub>	Power Dissipation in Still Air	SC-88 MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	-
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V <sub>ESD</sub>	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	2000 1000	V
I <sub>Latchup</sub>	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Applicable to devices with outputs that may be tri-stated.
   Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
   HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A. (Machine Model) be discontinued.
- 4. Tested to EIA/JESD78 Class II.

# RECOMMENDED OPERATING CONDITIONS

Symbol	Param	Min	Max	Unit	
V <sub>CC</sub>	Positive DC Supply Voltage		0.9	3.6	V
V <sub>IN</sub>	DC Input Voltage		0	3.6	٧
V <sub>OUT</sub>	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ( $V_{CC} = 0 \text{ V}$ )	0 0 0	V <sub>CC</sub> 3.6 3.6	
T <sub>A</sub>	Operating Temperature Range		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Transition Rise and Fall Time	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

# DC ELECTRICAL CHARACTERISTICS

				T	A = 25°	С	T <sub>A</sub> = -40°0	C to +85°C	
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
V <sub>IH</sub>	High-Level Input		0.9	_	0.5	_	-	-	V
	Voltage		1.1 to 1.3	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			1.4 to 1.6	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			1.65 to 1.95	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			2.3 to 2.7	1.6	-	-	1.6	-	
			3.0 to 3.6	2.1	_	-	2.1	-	
V <sub>IL</sub>	Low-Level Input		0.9	-	0.5	-	-	-	V
	Voltage		1.1 to 1.3	-	-	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			1.4 to 1.6	-	_	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			1.65 to 1.95	-	_	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			2.3 to 2.7	-	_	0.7	-	0.7	
			3.0 to 3.6	-	_	0.9	-	0.9	
V <sub>OH</sub>	High-Level Output	$V_{IN} = V_{IH}$ or $V_{IL}$							V
	Voltage	I <sub>OH</sub> = -20 μA	0.9	-	V <sub>CC</sub> - 0.1	-	-	-	
			1.1 to 1.3	V <sub>CC</sub> - 0.1	_	-	V <sub>CC</sub> - 0.1	-	
			1.4 to 1.6	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> - 0.1	-	
			1.65 to 1.95	V <sub>CC</sub> - 0.1	_	-	V <sub>CC</sub> - 0.1	-	
			2.3 to 2.7	V <sub>CC</sub> - 0.1	_	-	V <sub>CC</sub> - 0.1	-	
			3.0 to 3.6	V <sub>CC</sub> - 0.1	_	_	V <sub>CC</sub> - 0.1	-	
		$I_{OH} = -0.5 \text{ mA}$	1.1 to 1.3	0.75 x V <sub>CC</sub>	-	-	0.70 x V <sub>CC</sub>	-	
		I <sub>OH</sub> = -1 mA	1.4 to 1.6	1.07	-	-	0.99	-	
		I <sub>OH</sub> = -1.5 mA	1.65 to 1.95	1.24	-	-	1.22	-	
		I <sub>OH</sub> = -2.1 mA	2.3 to 2.7	1.95	-	-	1.87	-	
		I <sub>OH</sub> = -2.6 mA	3.0 to 3.6	2.61	_	-	2.55	-	
V <sub>OL</sub>	Low-Level Output	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>							V
	Voltage	I <sub>OL</sub> = 20 μA	0.9	-	0.1	-	-	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	_	0.1	-	0.1	
			1.65 to 1.95	-	-	0.1	-	0.1	
			2.3 to 2.7	-	_	0.1	-	0.1	
			3.0 to 3.6	-	_	0.1	-	0.1	
		I <sub>OL</sub> = 0.5 mA	1.1 to 1.3	_	-	0.3 x V <sub>CC</sub>	-	0.3 x V <sub>CC</sub>	
		I <sub>OL</sub> = 1 mA	1.4 to 1.6	-	_	0.31	-	0.37	
		I <sub>OL</sub> = 1.5 mA	1.65 to 1.95	_	-	0.31	-	0.35	
		I <sub>OL</sub> = 2.1 mA	2.3 to 2.7	_	-	0.31	-	0.33	
		I <sub>OL</sub> = 2.6 mA	3.0 to 3.6	_	-	0.31	-	0.33	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	-	-	±0.1	-	±0.5	μΑ
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V or V <sub>OUT</sub> = 0 V to 3.6 V	0	-	_	0.5	-	0.5	μΑ
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0.9 to 3.6	-	_	0.9	-	0.9	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

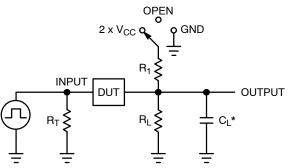
# **AC ELECTRICAL CHARACTERISTICS**

				T <sub>A</sub> = 25°C			T <sub>A</sub> = -40°C to +85°C		
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	$R_L = 1 \text{ M}\Omega$ , $C_L = 10 \text{ pF}$	0.9	_	51.4	-	-	-	ns
	(Figures 3 and 4)		1.10 to 1.30	_	12.8	28.8	-	37.7	
			1.40 to 1.60	-	6.3	15.1	_	16.8	
			1.65 to 1.95	-	4.2	11.5	_	12.5	
			2.3 to 2.7	_	2.9	8.1	-	9.1	
			3.0 to 3.6	-	2.3	6.6	_	7.7	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay		0.9	-	53.0	-	_	_	ns
	(Figures 3 and 4)		1.10 to 1.30	_	13.3	30.3	-	39.7	
			1.40 to 1.60	-	6.7	16.0	_	17.2	
			1.65 to 1.95	_	4.6	12.1	-	13.1	
			2.3 to 2.7	-	3.2	8.6	_	9.7	
			3.0 to 3.6	-	2.5	7.0	_	8.1	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	$R_L = 1 M\Omega$ , $C_L = 30 pF$	0.9	_	57.9	-	-	ı	ns
	(Figures 3 and 4)		1.10 to 1.30	-	15.0	34.7	_	48.3	
			1.40 to 1.60	-	8.1	18.6	-	19.5	
			1.65 to 1.95	-	5.7	14.1	-	15.3	
			2.3 to 2.7	-	3.9	10.0	-	11.2	
			3.0 to 3.6	_	3.0	8.2	-	9.3	

# **CAPACITIVE CHARACTERISTICS**

Symbol	Parameter	Test Condition	Typical (T <sub>A</sub> = 25°C)	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 0 V	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	$f = 10 \text{ MHz}, V_{CC} = 0.9 \text{ to } 3.6 \text{ V}, V_{IN} = 0 \text{ V or } V_{CC}$	8.0	pF

<sup>5.</sup> C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no–load dynamic power consumption: P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

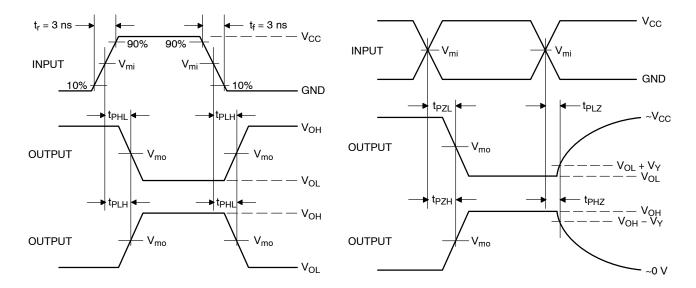


Test	Switch Position
t <sub>PLH</sub> / t <sub>PHL</sub>	Open
t <sub>PLZ</sub> / t <sub>PZL</sub>	2 x V <sub>CC</sub>
t <sub>PHZ</sub> / t <sub>PZH</sub>	GND

C<sub>L</sub> includes probe and jig capacitance

 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ ) f=1 MHz

Figure 3. Test Circuit



V <sub>CC</sub> , V	V <sub>mi</sub> , V	V <sub>mo</sub> , V	V <sub>Y</sub> , V
0.9	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.1 to 1.3	V <sub>CC</sub> /2	V <sub>CC</sub> /2	0.1
1.4 to 1.6	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.65 to 1.95	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
2.3 to 2.7	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

# **ORDERING INFORMATION**

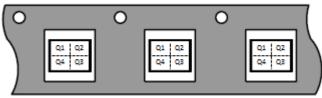
Device	Package	Marking	Pin 1 Orientation (See below)	Shipping <sup>†</sup>
NC7SP19P6X	SC-88	P19	Q4	3000 / Tape & Reel
NC7SP19L6X	MicroPak	L5	Q4	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# Pin 1 Orientation in Tape and Reel

# Direction of Feed

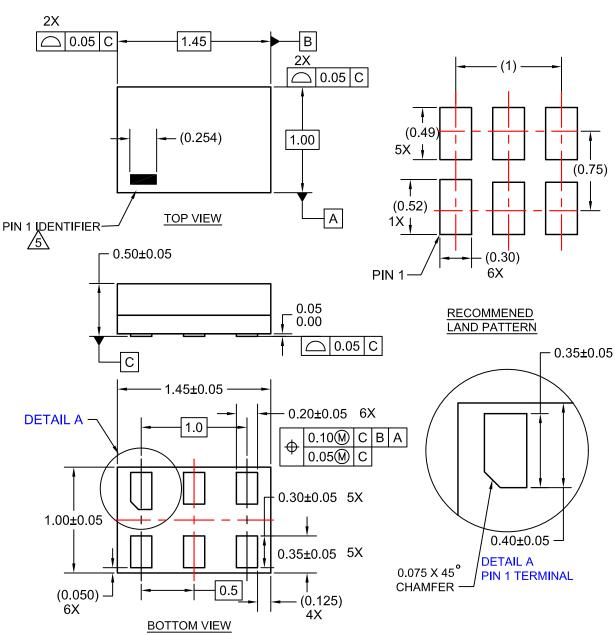




MicroPak is a trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.

## **PACKAGE DIMENSIONS**

SIP6 1.45X1.0 CASE 127EB ISSUE O



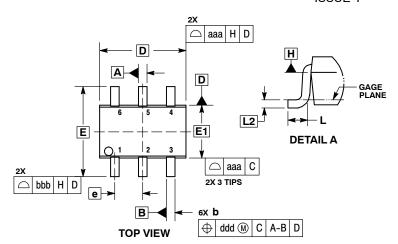
- NOTES:
- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
- 4.PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

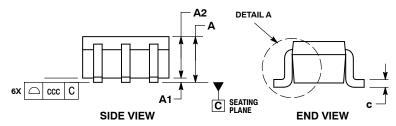
OTHER LINE IN THE MARK CODE LAYOUT.

## PACKAGE DIMENSIONS

# SC-88/SC70-6/SOT-363

CASE 419B-02 **ISSUE Y** 





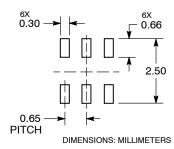
- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
  4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  5. DATUMS A AND B ARE DETERMINED AT DATUM H.
  6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
  7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION

- DIMENSION 5 DOES NOT INCLUDE DAMBAR PROTRUSION.
  ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN
  EXCESS OF DIMENSION 5 AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER

	MIL	LIMETE	RS		INCHES	3	
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α			1.10			0.043	
A1	0.00		0.10	0.000		0.004	
A2	0.70	0.90	1.00	0.027	0.035	0.039	
b	0.15	0.20	0.25	0.006	0.008	0.010	
С	0.08	0.15	0.22	0.003	0.006	0.009	
D	1.80	2.00	2.20	0.070	0.078	0.086	
E	2.00	2.10	2.20	0.078	0.082	0.086	
E1	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65 BSC			0	.026 BS	С	
L	0.26	0.36	0.46	0.010	0.014	0.018	
L2	0.15 BSC			0.006 BSC			
aaa	0.15			0.006			
bbb	0.30 0.012						
ccc		0.10		0.004			
ddd		0.10			0.004		

#### **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting
Techniques Reference Manual, SOLDERRM/D.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative