

Not Recommended
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For New Designs
For 82C82/82C83H

# HD-6436

CMOS Octal Bus Buffer/Driver

#### Features

- SINGLE POWER SUPPLY
- HIGH NOISE IMMUNITY
- INDUSTRIAL AND MILITARY GRADES
- DRIVE CAPACITY
- SOURCE CURRENT
- SINK CURRENT
- PROPAGATION DELAY

300pF

6mA 9mA

55nsec MAX.

## Description

The HD-6436 is a self-aligned silicon gate CMOS Three State buffer driver. The circuit consists of 8 noninverting buffers with separate inputs and outputs which permit this driver to be used for bi-directional or uni-directional busing. A high on either Three State control line  $\overline{E}_1$  or  $\overline{E}_2$  will force the drivers to the high impedance mode.

Outputs guaranteed valid at VCC = 2.0V for Battery Backup Applications.

## Pinout

7 111001				
TOP VIEW				
4y□	1	20 1 VCC		
3 <b>⋎</b> [	2	19 35Y		
2ү□	3	18 GY		
1 <b>⋎</b> □	4	17 7Y		
1 A 🗀	5	16 38		
2⊿□	6	15 38 <sub>A</sub>		
3⊿□	7	14 7 <sub>A</sub>		
4∧□	8	13 🗆 6 <sub>A</sub>		
Ē1 🗆	9	12 5 <sub>A</sub>		
GND□	10	11 🗖 \overline{E}_2		

### Truth Table

	TROL UTS	INPUT	OUTPUT
E <sub>1</sub>	Ē <sub>2</sub>	Α	Y
L	٢	L	L
L	Ļ	н	н
L	н	X	Hi-Z
н	L	X	Hi-Z
н	н	×	Hi-Z

L = Low, H = High X = Don't Care Hi-Z = High Impedance

## Functional Diagram

