

# BQ27427 System-Side Impedance Track™ Fuel Gauge With Integrated Sense Resistor

## 1 Features

- Single-cell Li-ion battery fuel gauge
  - Resides on system board
  - Supports embedded or removable batteries
  - Powers directly from the battery with integrated LDO
  - Integrates low-value sense resistor (7 mΩ)
- Ultra low power consumption in NORMAL (50 μA) and SLEEP (9 μA) modes
- Battery fuel gauging based on patented Impedance Track™ technology
  - Provides three selectable preprogrammed profiles for 4.2-V, 4.35-V, and 4.4-V cells
  - Reports remaining capacity and state-of-charge (SOC) with smoothing filter
  - Adjusts automatically for battery aging, self-discharge, temperature, and rate changes
  - Estimates battery state-of-health (aging)
- Microcontroller peripheral interface supports:
  - 400-kHz I<sup>2</sup>C serial interface
  - Configurable SOC interrupt or battery low digital output warning
  - Internal temperature sensor or host reported temperature or external thermistor

## 2 Applications

- Smartphones, feature phones, and tablets
- Wearables
- Building automation
- Portable medical/industrial handsets
- Portable audio
- Gaming

## 3 Description

The Texas Instruments BQ27427 battery fuel gauge is a single-cell gauge that requires minimal user-configuration and system microcontroller firmware development, leading to quick system bring-up.

Three chemistry profiles are preprogrammed to enable minimum user-configuration, and to help manage customer inventory across projects with different battery chemistries. The BQ27427 battery fuel gauge has very low sleep power consumption leading to longer battery run time. Configurable interrupts help save system power and free up the host from continuous polling. Accurate temperature sensing is supported via an external thermistor.

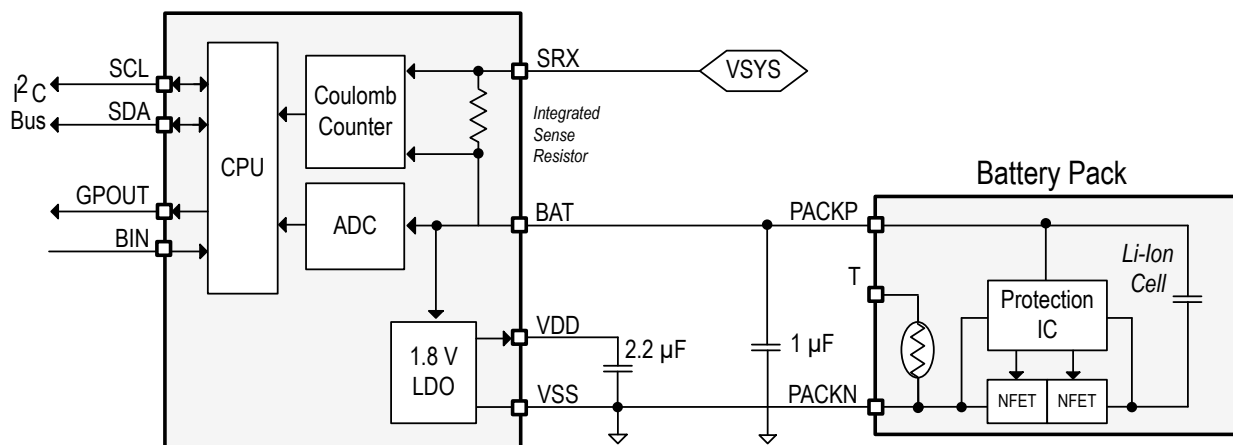
The BQ27427 battery fuel gauge uses the patented Impedance Track™ algorithm for fuel gauging, and provides information such as remaining battery capacity (mAh), state-of-charge (%), and battery voltage (mV).

Battery fuel gauging with the BQ27427 fuel gauge requires connections only to PACK+ (P+) and PACK– (P–) for a removable battery pack or embedded battery circuit. The tiny, 9-ball, 1.62 mm x 1.58 mm, 0.5 mm pitch NanoFree™ chip scale package (DSBGA) is ideal for space-constrained applications.

### Device Information

| PART NUMBER | PACKAGE                | BODY SIZE (NOM)   |
|-------------|------------------------|-------------------|
| BQ27427     | YZF (9) <sup>(1)</sup> | 1.62 mm × 1.58 mm |

(1) For all available packages, see the orderable addendum at the end of the data sheet.



**Simplified Schematic**



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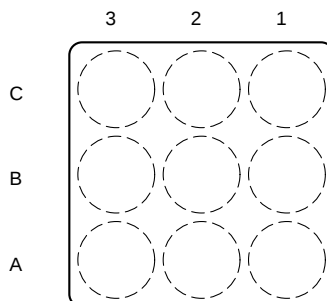
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## 4 Revision History

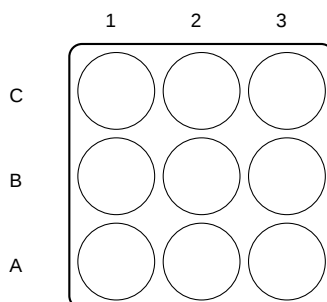
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| DATE          | REVISION | NOTES           |
|---------------|----------|-----------------|
| December 2022 | *        | Initial Release |

## 5 Pin Configuration and Functions



**Figure 5-1. Top View**



**Figure 5-2. Bottom View**

**Table 5-1. Pin Functions**

| PIN   |        | TYPE <sup>(1)</sup> | DESCRIPTION                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|-------|--------|---------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| NAME  | NUMBER |                     |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| BAT   | C3     | PI, AI              | LDO regulator input and battery voltage measurement input. Kelvin sense connect to positive battery terminal (PACKP). Connect a capacitor (1 $\mu$ F) between BAT and V <sub>SS</sub> . Place the capacitor close to the gauge.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| BIN   | B1     | DI                  | Battery insertion detection input. If <b>OpConfig</b> [BI_PU_EN] = 1 (default), a logic low on the pin is detected as battery insertion. For a removable pack, the BIN pin can be connected to V <sub>SS</sub> through a pulldown resistor on the pack, typically the 10-k $\Omega$ thermistor; the system board should use a 1.8-M $\Omega$ pullup resistor to V <sub>DD</sub> to ensure the BIN pin is high when a battery is removed. If the battery is embedded in the system, it is recommended to leave [BI_PU_EN] = 1 and use a 10-k $\Omega$ pulldown resistor from BIN to V <sub>SS</sub> . If [BI_PU_EN] = 0, then the host must inform the gauge of battery insertion and removal with the <b>BAT_INSERT</b> and <b>BAT_REMOVE</b> subcommands. A 10-k $\Omega$ pulldown resistor should be placed between BIN and V <sub>SS</sub> , even if this pin is unused.<br><b>NOTE:</b> The BIN pin must not be shorted directly to V <sub>CC</sub> or V <sub>SS</sub> and any pullup resistor on the BIN pin must be connected only to V <sub>DD</sub> and not an external voltage rail. If an external thermistor is used for temperature input, the thermistor should be connected between this pin and V <sub>SS</sub> . |
| GPOUT | A1     | DO                  | This open-drain output can be configured to indicate BAT_LOW when the <b>OpConfig</b> [BATLOWEN] bit is set. By default [BATLOWEN] is cleared and this pin performs an interrupt function (SOC_INT) by pulsing for specific events, such as a change in state-of-charge. Signal polarity for these functions is controlled by the [GPIOPOL] configuration bit. This pin should not be left floating, even if unused; therefore, a 10-k $\Omega$ pullup resistor is recommended. If the device is in SHUTDOWN mode, toggling GPOUT will make the gauge exit SHUTDOWN. It is recommended to connect GPOUT to a GPIO of the host MCU so that in case of any inadvertent shutdown condition, the gauge can be commanded to come out of SHUTDOWN.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

**Table 5-1. Pin Functions (continued)**

| PIN             |        | TYPE <sup>(1)</sup> | DESCRIPTION                                                                                                                                                                                                                                                                                                                                                                                |
|-----------------|--------|---------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| NAME            | NUMBER |                     |                                                                                                                                                                                                                                                                                                                                                                                            |
| SCL             | A3     | DIO                 | Peripheral I <sup>2</sup> C serial bus for communication with system (primary). Open-drain pins. Use with external 10-k $\Omega$ pullup resistors (typical) for each pin. If the external pullup resistors will be disconnected from these pins during normal operation, recommend using external 1-M $\Omega$ pulldown resistors to V <sub>SS</sub> at each pin to avoid floating inputs. |
| SDA             | A2     | DIO                 |                                                                                                                                                                                                                                                                                                                                                                                            |
| SRX             | C2     | AI                  | Integrated high-side sense resistor and coulomb counter input, connected between battery pack and system power rail VSYS.                                                                                                                                                                                                                                                                  |
| V <sub>DD</sub> | B3     | PO                  | 1.8-V regulator output. Decouple with 2.2- $\mu$ F ceramic capacitor to V <sub>SS</sub> . This pin is not intended to provide power for other devices in the system.                                                                                                                                                                                                                       |
| V <sub>SS</sub> | B2, C1 | PI                  | Ground pin                                                                                                                                                                                                                                                                                                                                                                                 |

(1) IO = Digital input-output, AI = Analog input, P = Power connection

## 6 Specifications

### 6.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

|                                       |                                                       | MIN                    | MAX                    | UNIT |
|---------------------------------------|-------------------------------------------------------|------------------------|------------------------|------|
| V <sub>BAT</sub>                      | BAT pin input voltage range                           | −0.3                   | 6                      | V    |
| V <sub>SRX</sub>                      | SRX pin input voltage range                           | V <sub>BAT</sub> − 0.3 | V <sub>BAT</sub> + 0.3 | V    |
| V <sub>DD</sub>                       | V <sub>DD</sub> pin supply voltage range (LDO output) | −0.3                   | 2                      | V    |
| V <sub>IOD</sub>                      | Open-drain IO pins (SDA, SCL)                         | −0.3                   | 6                      | V    |
| V <sub>IOPP</sub>                     | Push-pull IO pins (BIN)                               | −0.3                   | V <sub>DD</sub> + 0.3  | V    |
| T <sub>A</sub>                        | Operating free-air temperature range                  | −40                    | 85                     | °C   |
| Storage temperature, T <sub>stg</sub> |                                                       | −65                    | 150                    | °C   |

- (1) Operation outside the Absolute Maximum Ratings may cause permanent device damage. Absolute Maximum Ratings do not imply functional operation of the device at these or any other conditions beyond those listed under Recommended Operating Conditions. If outside the Recommended Operating Conditions but within the Absolute Maximum Ratings, the device may not be fully functional, and this may affect device reliability, functionality, performance, and shorten the device lifetime.

### 6.2 ESD Ratings

|                    |                         | VALUE                                                                                     | UNIT  |
|--------------------|-------------------------|-------------------------------------------------------------------------------------------|-------|
| V <sub>(ESD)</sub> | Electrostatic discharge | Human body model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>                         | ±1500 |
|                    |                         | Charged-device model (CDM), per JEDEC specification ANSI/ESDA/JEDEC JS-002 <sup>(2)</sup> | ±250  |

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### 6.3 Recommended Operating Conditions

T<sub>A</sub> = 30°C and V<sub>REGIN</sub> = V<sub>BAT</sub> = 3.6 V (unless otherwise noted)

|                                   |                                                                                        |                                                                                                            | MIN  | NOM | MAX | UNIT |
|-----------------------------------|----------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------|------|-----|-----|------|
| C <sub>BAT</sub> <sup>(1)</sup>   | External input capacitor for internal LDO between BAT and V <sub>SS</sub>              | Nominal capacitor values specified. Recommend a 5% ceramic X5R-type capacitor located close to the device. |      | 0.1 |     | μF   |
| C <sub>LDO18</sub> <sup>(1)</sup> | External output capacitor for internal LDO between V <sub>DD</sub> and V <sub>SS</sub> |                                                                                                            |      | 2.2 |     | μF   |
| V <sub>PU</sub> <sup>(1)</sup>    | External pullup voltage for open-drain pins (SDA, SCL, GPOUT)                          |                                                                                                            | 1.62 |     | 3.6 | V    |

- (1) Specified by design. Not production tested.

### 6.4 Thermal Information

| THERMAL METRIC <sup>(1)</sup> |                                              | BQ27427     | UNIT |
|-------------------------------|----------------------------------------------|-------------|------|
|                               |                                              | YZF (DSBGA) |      |
|                               |                                              | 9 PINS      |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance       | 107.8       | °C/W |
| R <sub>θJCTop</sub>           | Junction-to-case (top) thermal resistance    | 0.7         | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance         | 60.4        | °C/W |
| ψ <sub>JT</sub>               | Junction-to-top characterization parameter   | 3.5         | °C/W |
| ψ <sub>JB</sub>               | Junction-to-board characterization parameter | 60.4        | °C/W |
| R <sub>θJCbott</sub>          | Junction-to-case (bottom) thermal resistance | NA          | °C/W |

- (1) For more information about traditional and new thermal metrics, see the [IC Package Thermal Metrics Application Report, SPRA953](#).

## 6.5 Supply Current

$T_A = 30^\circ\text{C}$  and  $V_{\text{REGIN}} = V_{\text{BAT}} = 3.6\text{ V}$  (unless otherwise noted)

| PARAMETER                       | TEST CONDITIONS                                                                | MIN | TYP | MAX | UNIT          |
|---------------------------------|--------------------------------------------------------------------------------|-----|-----|-----|---------------|
| $I_{\text{CC}}$ <sup>(1)</sup>  | NORMAL mode current                                                            |     | 50  |     | $\mu\text{A}$ |
| $I_{\text{SLP}}$ <sup>(1)</sup> | SLEEP mode current                                                             |     | 9   |     | $\mu\text{A}$ |
| $I_{\text{SD}}$ <sup>(1)</sup>  | SHUTDOWN mode current                                                          |     | 0.6 |     | $\mu\text{A}$ |
|                                 | Fuel gauge in host commanded SHUTDOWN mode.<br>(LDO regulator output disabled) |     |     |     |               |

(1) Specified by design. Not production tested.

(2) Wake Comparator Disabled.

## 6.6 Digital Input and Output DC Characteristics

$T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$ , typical values at  $T_A = 30^\circ\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

<sup>(1)</sup>

| PARAMETER                      | TEST CONDITIONS                              | MIN                                         | TYP                        | MAX | UNIT          |
|--------------------------------|----------------------------------------------|---------------------------------------------|----------------------------|-----|---------------|
| $V_{\text{IH(OD)}}$            | Input voltage, high <sup>(2)</sup>           | External pullup resistor to $V_{\text{PU}}$ | $V_{\text{PU}} \times 0.7$ |     | V             |
| $V_{\text{IH(PP)}}$            | Input voltage, high <sup>(3)</sup>           |                                             | 1.4                        |     | V             |
| $V_{\text{IL}}$                | Input voltage, low <sup>(2) (3)</sup>        |                                             |                            | 0.6 | V             |
| $V_{\text{OL}}$                | Output voltage, low <sup>(2)</sup>           |                                             |                            | 0.6 | V             |
| $I_{\text{OH}}$                | Output source current, high <sup>(2)</sup>   |                                             |                            | 0.5 | mA            |
| $I_{\text{OL(OD)}}$            | Output sink current, low <sup>(2)</sup>      |                                             |                            | –3  | mA            |
| $C_{\text{IN}}$ <sup>(1)</sup> | Input capacitance <sup>(2) (3)</sup>         |                                             |                            | 5   | pF            |
| $I_{\text{Ikg}}$               | Input Leakage Current (SCL, SDA, BIN, GPOUT) |                                             |                            | 1   | $\mu\text{A}$ |

(1) Specified by design. Not production tested.

(2) Open Drain pins: (SCL, SDA, GPOUT)

(3) Push-Pull pin: (BIN)

## 6.7 LDO Regulator, Wake-up, and Auto-Shutdown DC Characteristics

$T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$ , typical values at  $T_A = 30^\circ\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

<sup>(1)</sup>

| PARAMETER                       | TEST CONDITIONS                                                               | MIN                            | TYP  | MAX | UNIT |
|---------------------------------|-------------------------------------------------------------------------------|--------------------------------|------|-----|------|
| $V_{\text{BAT}}$                | BAT pin regulator input                                                       | 2.45                           |      | 4.5 | V    |
| $V_{\text{DD}}$                 | Regulator output voltage                                                      |                                | 1.85 |     | V    |
| $UVLO_{\text{IT+}}$             | $V_{\text{BAT}}$ undervoltage lock-out<br>LDO wake-up rising threshold        |                                | 2    |     | V    |
| $UVLO_{\text{IT-}}$             | $V_{\text{BAT}}$ undervoltage lock-out<br>LDO auto-shutdown falling threshold |                                | 1.95 |     | V    |
| $V_{\text{WU+}}$ <sup>(1)</sup> | GPOUT (input) LDO Wake-up rising edge threshold <sup>(2)</sup>                | LDO Wake-up from SHUTDOWN mode | 1.2  |     | V    |

(1) Specified by design. Not production tested.

(2) If the device is commanded to SHUTDOWN via I<sup>2</sup>C with  $V_{\text{BAT}} > UVLO_{\text{IT+}}$ , a wake-up rising edge trigger is required on GPOUT.

## 6.8 LDO Regulator, Wake-up, and Auto-Shutdown AC Characteristics

$T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$ , typical values at  $T_A = 30^\circ\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

| PARAMETER                        | TEST CONDITIONS                      | MIN                                                         | TYP | MAX | UNIT          |
|----------------------------------|--------------------------------------|-------------------------------------------------------------|-----|-----|---------------|
| $t_{\text{SHDN}}$ <sup>(1)</sup> | SHUTDOWN entry time                  | Time delay from SHUTDOWN command to LDO output disable.     |     | 250 | ms            |
| $t_{\text{SHUP}}$ <sup>(1)</sup> | SHUTDOWN GPOUT low time              | Minimum low time of GPOUT (input) in SHUTDOWN before WAKEUP | 10  |     | $\mu\text{s}$ |
| $t_{\text{VDD}}$ <sup>(1)</sup>  | Initial $V_{\text{DD}}$ output delay |                                                             | 13  |     | ms            |

## 6.8 LDO Regulator, Wake-up, and Auto-Shutdown AC Characteristics (continued)

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ , typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

| PARAMETER                                                              | TEST CONDITIONS                                                                                 | MIN | TYP | MAX | UNIT |
|------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------|-----|-----|-----|------|
| $t_{\text{WUVDD}}$ <sup>(1)</sup> Wake-up $V_{\text{DD}}$ output delay | Time delay from rising edge of GPOUT (input) to nominal $V_{\text{DD}}$ output                  |     | 8   |     | ms   |
| $t_{\text{PUCD}}$ Power-up communication delay                         | Time delay from rising edge of REGIN to the Active state. Includes firmware initialization time |     | 250 |     | ms   |

(1) Specified by design. Not production tested.

## 6.9 ADC (Temperature and Cell Measurement) Characteristics

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ; typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

(1)

| PARAMETER                                              | TEST CONDITIONS         | MIN  | TYP | MAX | UNIT |
|--------------------------------------------------------|-------------------------|------|-----|-----|------|
| $V_{\text{IN(BAT)}}$ BAT pin voltage measurement range | Voltage divider enabled | 2.45 |     | 4.5 | V    |
| $t_{\text{ADC_CONV}}$ Conversion time                  |                         |      | 125 |     | ms   |
| Effective resolution                                   |                         |      | 15  |     | bits |

(1) Specified by design. Not tested in production.

## 6.10 Integrating ADC (Coulomb Counter) Characteristics

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ; typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

(1)

| PARAMETER                                                | TEST CONDITIONS   | MIN | TYP          | MAX | UNIT |
|----------------------------------------------------------|-------------------|-----|--------------|-----|------|
| $V_{\text{SR}}$ Input voltage range from BAT to SRX pins |                   |     | BAT $\pm$ 25 |     | mV   |
| $t_{\text{SR_CONV}}$ Conversion time                     | Single conversion |     | 1            |     | s    |
| Effective Resolution                                     | Single conversion |     | 16           |     | bits |

(1) Specified by design. Not tested in production.

## 6.11 Integrated Sense Resistor Characteristics, $-40^{\circ}\text{C}$ to $85^{\circ}\text{C}$

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ; typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

(1)

| PARAMETER                                                                                                    | TEST CONDITIONS                                                                                                        | MIN | TYP | MAX  | UNIT             |
|--------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------|-----|-----|------|------------------|
| $\text{SRX}_{\text{RES}}$ <sup>(2)</sup> Resistance of Integrated Sense Resistor from SRX to $V_{\text{SS}}$ | $T_A = 30^{\circ}\text{C}$                                                                                             |     | 7   |      | $\text{m}\Omega$ |
| $I_{\text{SRX}}$ <sup>(1)</sup> Recommended Sense Resistor input current.                                    | Long term RMS, average device utilization.                                                                             |     |     | 2000 | mA               |
|                                                                                                              | Peak RMS current, 10% device utilization, $-40^{\circ}\text{C}$ to $70^{\circ}\text{C}$ . <sup>(3)</sup>               |     |     | 3500 |                  |
|                                                                                                              | Peak RMS current, 10% device utilization, $-40^{\circ}\text{C}$ to $85^{\circ}\text{C}$ . <sup>(3)</sup>               |     |     | 2500 | mA               |
|                                                                                                              | Peak pulsed current, 250 ms max, 1% device utilization, $-40^{\circ}\text{C}$ to $70^{\circ}\text{C}$ . <sup>(3)</sup> |     |     | 4500 |                  |
|                                                                                                              | Peak pulsed current, 250 ms max, 1% device utilization, $-40^{\circ}\text{C}$ to $85^{\circ}\text{C}$ . <sup>(3)</sup> |     |     | 3500 | mA               |

(1) Specified by design. Not tested in production.

(2) Firmware compensation applied for temperature coefficient of resistor.

(3) Device utilization is the long term usage profile at a specific condition compared to the average condition.

## 6.12 I<sup>2</sup>C-Compatible Interface Communication Timing Characteristics

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ; typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

(1)

|                         | MIN | NOM | MAX | UNIT |
|-------------------------|-----|-----|-----|------|
| Standard Mode (100 kHz) |     |     |     |      |

## 6.12 I<sup>2</sup>C-Compatible Interface Communication Timing Characteristics (continued)

$T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ; typical values at  $T_A = 30^{\circ}\text{C}$  and  $V_{\text{REGIN}} = 3.6\text{ V}$  (unless otherwise noted)

(1)

|                            |                                      | MIN                           | NOM | MAX | UNIT          |
|----------------------------|--------------------------------------|-------------------------------|-----|-----|---------------|
| $t_{d(\text{STA})}$        | Start to first falling edge of SCL   | 4                             |     |     | $\mu\text{s}$ |
| $t_{w(\text{L})}$          | SCL pulse duration (low)             | 4.7                           |     |     | $\mu\text{s}$ |
| $t_{w(\text{H})}$          | SCL pulse duration (high)            | 4                             |     |     | $\mu\text{s}$ |
| $t_{su(\text{STA})}$       | Setup for repeated start             | 4.7                           |     |     | $\mu\text{s}$ |
| $t_{su(\text{DAT})}$       | Data setup time                      | Host drives SDA               |     |     | ns            |
| $t_h(\text{DAT})$          | Data hold time                       | Host drives SDA               |     |     | ns            |
| $t_{su(\text{STOP})}$      | Setup time for stop                  | 4                             |     |     | $\mu\text{s}$ |
| $t_{(\text{BUF})}$         | Bus free time between stop and start | Includes Command Waiting Time |     |     | $\mu\text{s}$ |
| $t_f$                      | SCL or SDA fall time <sup>(1)</sup>  |                               |     | 300 | ns            |
| $t_r$                      | SCL or SDA rise time <sup>(1)</sup>  |                               |     | 300 | ns            |
| $f_{\text{SCL}}$           | Clock frequency <sup>(2)</sup>       |                               |     | 100 | kHz           |
| <b>Fast Mode (400 kHz)</b> |                                      |                               |     |     |               |
| $t_{d(\text{STA})}$        | Start to first falling edge of SCL   | 600                           |     |     | ns            |
| $t_{w(\text{L})}$          | SCL pulse duration (low)             | 1300                          |     |     | ns            |
| $t_{w(\text{H})}$          | SCL pulse duration (high)            | 600                           |     |     | ns            |
| $t_{su(\text{STA})}$       | Setup for repeated start             | 600                           |     |     | ns            |
| $t_{su(\text{DAT})}$       | Data setup time                      | Host drives SDA               |     | 100 | ns            |
| $t_h(\text{DAT})$          | Data hold time                       | Host drives SDA               |     | 0   | ns            |
| $t_{su(\text{STOP})}$      | Setup time for stop                  | 600                           |     |     | ns            |
| $t_{(\text{BUF})}$         | Bus free time between stop and start | Includes Command Waiting Time |     | 66  | $\mu\text{s}$ |
| $t_f$                      | SCL or SDA fall time <sup>(1)</sup>  |                               |     | 300 | ns            |
| $t_r$                      | SCL or SDA rise time <sup>(1)</sup>  |                               |     | 300 | ns            |
| $f_{\text{SCL}}$           | Clock frequency <sup>(2)</sup>       |                               |     | 400 | kHz           |

(1) Specified by design. Not production tested.

(2) If the clock frequency ( $f_{\text{SCL}}$ ) is  $> 100\text{ kHz}$ , use 1-byte write commands for proper operation. All other transactions types are supported at 400 kHz. (See [Section 7.3.1.1](#) and [Section 7.3.1.3](#).)

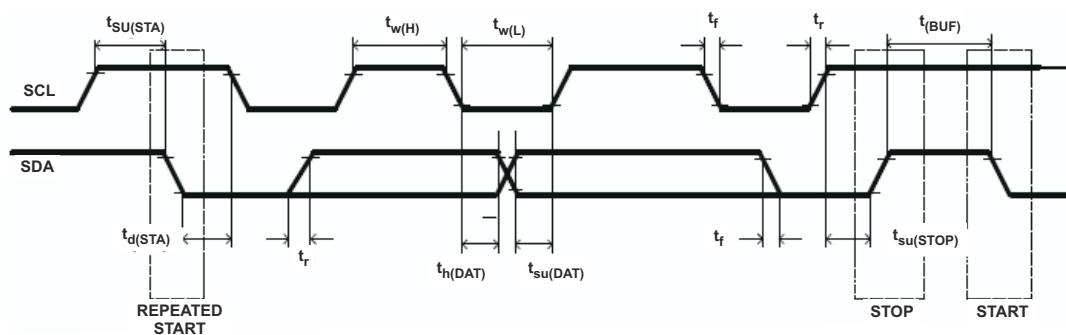
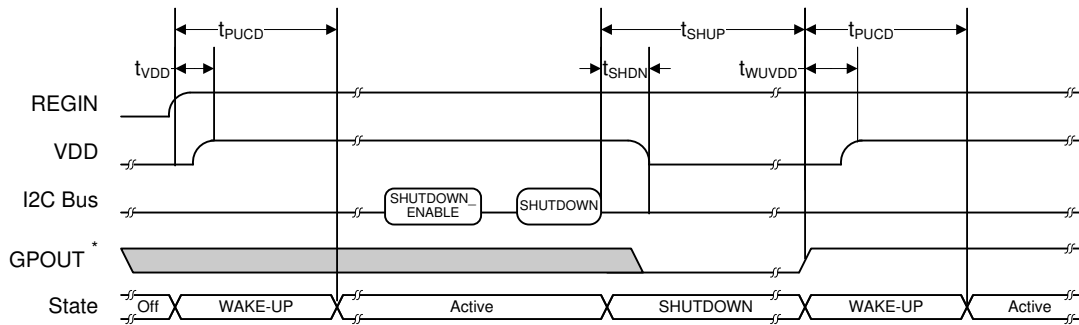


Figure 6-1. I<sup>2</sup>C-Compatible Interface Timing Diagrams

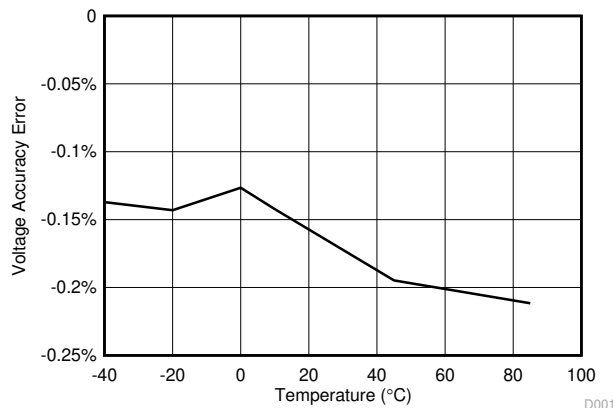
## 6.13 SHUTDOWN and WAKE-UP Timing



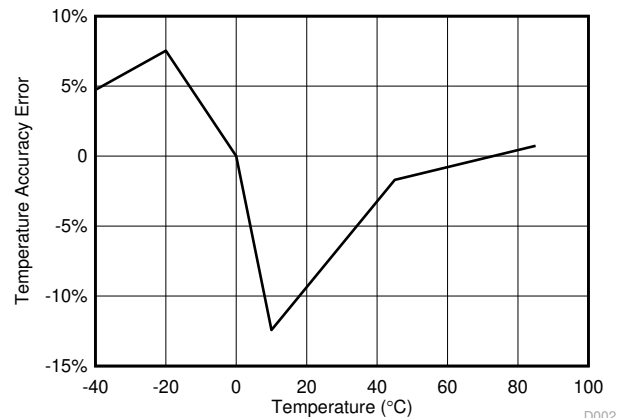
\* GPOUT is configured as an input for wake-up signaling.

**Figure 6-2. SHUTDOWN and WAKE-UP Timing Diagram**

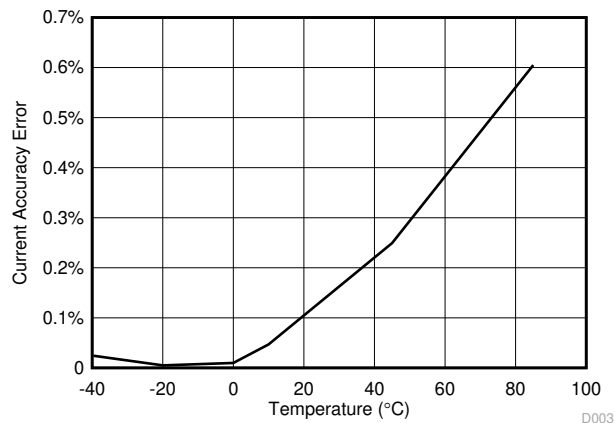
## 6.14 Typical Characteristics



**Figure 6-3. Voltage Accuracy Error**



**Figure 6-4. Internal Temperature Accuracy Error**



**Figure 6-5. Current Accuracy Error**

## 7 Detailed Description

### 7.1 Overview

The BQ27427 fuel gauge accurately predicts the battery capacity and other operational characteristics of a single Li-based rechargeable cell. It can be interrogated by a system processor to provide cell information, such as state-of-charge (SOC).

#### Note

The following formatting conventions are used in this document:

**Commands:** *italics* with parentheses() and no breaking spaces, for example, *Control()*.

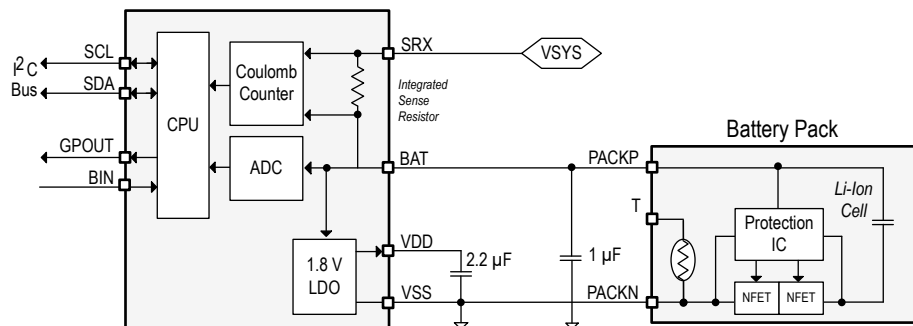
**Data flash:** *italics*, **bold**, and breaking spaces, for example, ***Design Capacity***.

**Register bits and flags:** *italics* with brackets [ ], for example, *[TDA]*

**Data flash bits:** *italics*, **bold**, and brackets [ ], for example, ***[LED1]***

**Modes and states:** ALL CAPITALS, for example, UNSEALED mode

### 7.2 Functional Block Diagram



### 7.3 Feature Description

Information is accessed through a series of commands, called *Standard Commands*. Further capabilities are provided by the additional *Extended Commands* set. Both sets of commands, indicated by the general format *Command*), are used to read and write information contained within the control and status registers, as well as its data locations. Commands are sent from system to gauge using the I<sup>2</sup>C serial communications engine, and can be executed during application development, system manufacture, or end-equipment operation.

The key to the high-accuracy gas gauging prediction is Texas Instruments proprietary Impedance Track™ algorithm. This algorithm uses cell measurements, characteristics, and properties to create state-of-charge predictions that can achieve high accuracy across a wide variety of operating conditions and over the lifetime of the battery.

The fuel gauge measures the charging and discharging of the battery by monitoring the voltage across an integrated small-value sense resistor. When a cell is attached to the fuel gauge, cell impedance is computed based on cell current, cell open-circuit voltage (OCV), and cell voltage under loading conditions.

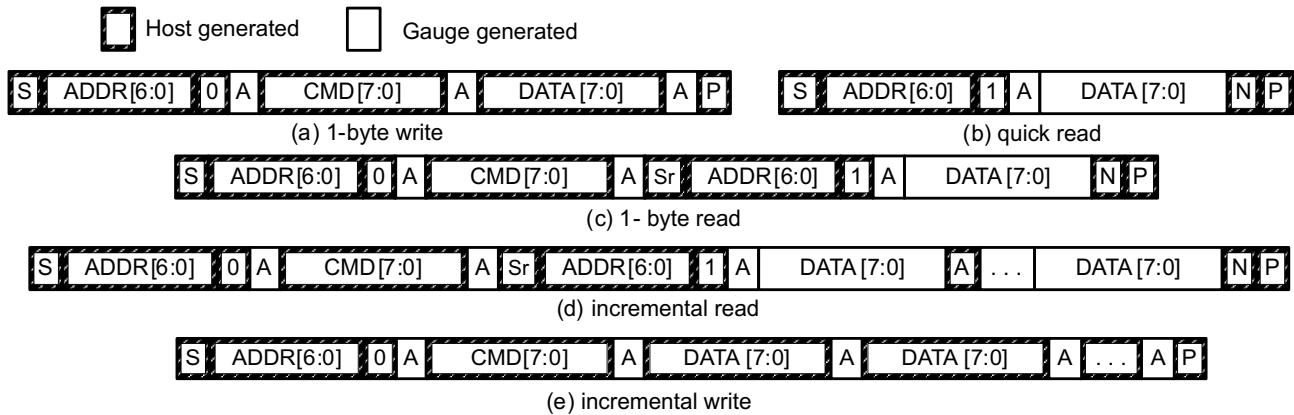
The fuel gauge uses an integrated temperature sensor for estimating cell temperature. Alternatively, the host processor can provide temperature data for the fuel gauge.

For more details, see the *BQ27427 Technical Reference Manual*.

## 7.3.1 Communications

### 7.3.1.1 I<sup>2</sup>C Interface

The fuel gauge supports the standard I<sup>2</sup>C read, incremental read, quick read, one-byte write, and incremental write functions. The 7-bit device address (ADDR) is the most significant 7 bits of the hex address and is fixed as 1010101. The first 8 bits of the I<sup>2</sup>C protocol are, therefore, 0xAA or 0xAB for write or read, respectively.

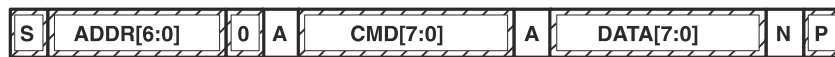


(S = Start, Sr = Repeated Start, A = Acknowledge, N = No Acknowledge, and P = Stop).

**Figure 7-1. I<sup>2</sup>C Interface**

The quick read returns data at the address indicated by the address pointer. The address pointer, a register internal to the I<sup>2</sup>C communication engine, increments whenever data is acknowledged by the fuel gauge or the I<sup>2</sup>C primary. "Quick writes" function in the same manner and are a convenient means of sending multiple bytes to consecutive command locations (such as two-byte commands that require two bytes of data).

The following command sequences are not supported:



**Figure 7-2. Attempt To Write a Read-only Address (NACK After Data Sent By Primary)**



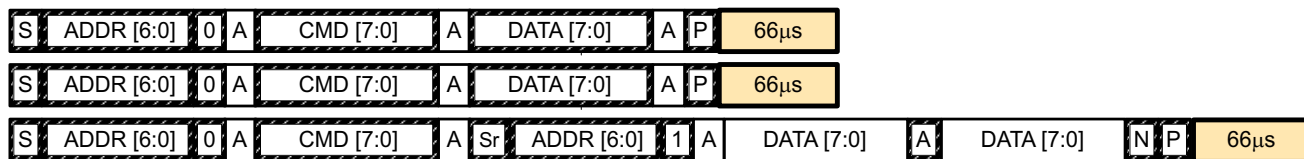
**Figure 7-3. Attempt To Read an Address Above 0x6B (NACK Command)**

#### 7.3.1.2 I<sup>2</sup>C Time Out

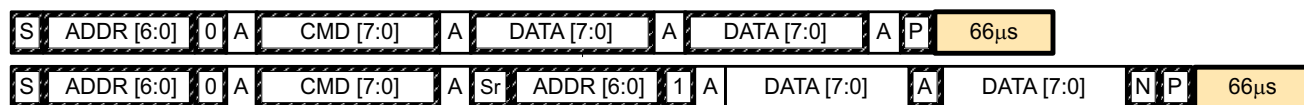
The I<sup>2</sup>C engine releases SDA and SCL if the I<sup>2</sup>C bus is held low for two seconds. If the fuel gauge is holding the lines, releasing them frees them for the primary to drive the lines. If an external condition is holding either of the lines low, the I<sup>2</sup>C engine enters the low-power SLEEP mode.

#### 7.3.1.3 I<sup>2</sup>C Command Waiting Time

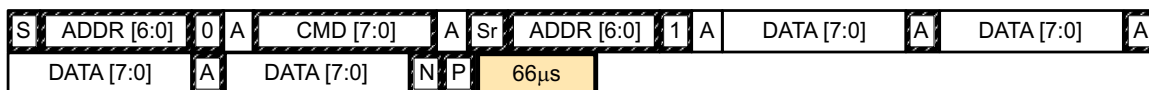
To ensure proper operation at 400 kHz, a  $t_{BUF} \geq 66 \mu s$  bus-free waiting time must be inserted between all packets addressed to the fuel gauge. In addition, if the SCL clock frequency ( $f_{SCL}$ ) is  $> 100$  kHz, use individual 1-byte write commands for proper data flow control. The following diagram shows the standard waiting time required between issuing the control subcommand the reading the status result. For read-write standard command, a minimum of 2 seconds is required to get the result updated. For read-only standard commands, there is no waiting time required, but the host must not issue any standard command more than two times per second. Otherwise, the gauge could result in a reset issue due to the expiration of the watchdog timer.



Waiting time inserted between two 1-byte write packets for a subcommand and reading results  
(required for  $100 \text{ kHz} < f_{\text{SCL}} \leq 400 \text{ kHz}$ )



Waiting time inserted between incremental 2-byte write packet for a subcommand and reading results  
(acceptable for  $f_{\text{SCL}} \leq 100 \text{ kHz}$ )



Waiting time inserted after incremental read

**Figure 7-4. I<sup>2</sup>C Command Waiting Time**

### 7.3.1.4 I<sup>2</sup>C Clock Stretching

A clock stretch can occur during all modes of fuel gauge operation. In SLEEP mode, a short  $\leq 100\text{-}\mu\text{s}$  clock stretch occurs on all I<sup>2</sup>C traffic as the device must wake-up to process the packet. In the other modes (INITIALIZATION, NORMAL), a  $\leq 4\text{-ms}$  clock stretching period may occur within packets addressed for the fuel gauge as the I<sup>2</sup>C interface performs normal data flow control.

## 7.4 Device Functional Modes

To minimize power consumption, the fuel gauge has several power modes:

- INITIALIZATION
- NORMAL
- SLEEP
- and SHUTDOWN

The fuel gauge passes automatically between these modes, depending upon the occurrence of specific events, though a system processor can initiate some of these modes directly. For more details, see the *BQ27427 Technical Reference Manual*.

## 8 Application and Implementation

### Note

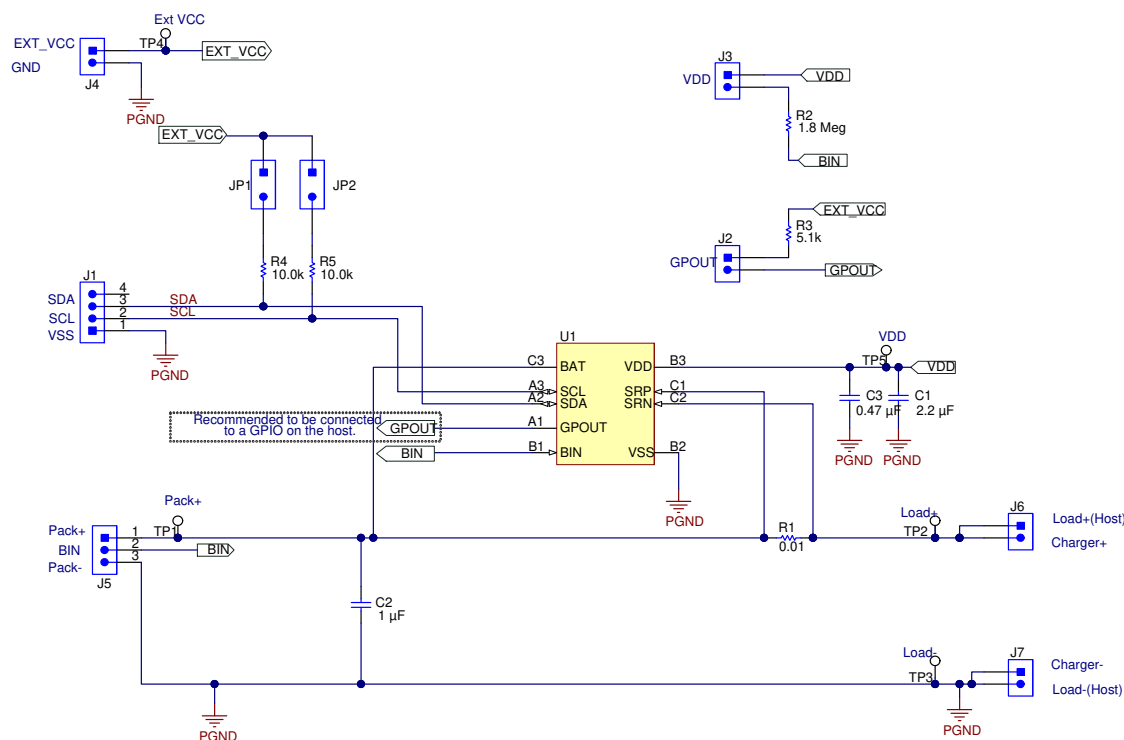
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

## 8.1 Application Information

The BQ27427 fuel gauge is a microcontroller peripheral that provides system-side fuel gauging for single-cell Li-Ion batteries. Battery fuel gauging with the fuel gauge requires connections only to PACK+ and PACK– for a removable battery pack or embedded battery circuit. To allow for optimal performance in the end application, special considerations must be taken to ensure minimization of measurement error through proper printed circuit board (PCB) board layout. Such requirements are detailed in [Section 8.2.1](#).

## 8.2 Typical Applications

The BQ27427 device can be used with a high-side current sense resistor (as shown in the schematic below).



### Figure 8-1. Typical Application with High-Side Current Sense Resistor

### 8.2.1 Design Requirements

As shipped from the Texas Instruments factory, the BQ27427 fuel gauge comes with three preprogrammed chemistry profiles and gauging parameters in ROM. Upon device reset, the contents of ROM are copied to associated volatile RAM-based data memory blocks. For proper operation, all parameters in RAM-based data memory require initialization. This can be done by updating data memory parameters in a lab/evaluation situation or by downloading the parameters from a host. The *BQ27427 Technical Reference Manual* shows the default and typically expected values appropriate for most applications.

## 8.2.2 Detailed Design Procedure

### 8.2.2.1 BAT Voltage Sense Input

A ceramic capacitor at the input to the BAT pin is used to bypass AC voltage ripple to ground, greatly reducing its influence on battery voltage measurements. It proves most effective in applications with load profiles that exhibit high-frequency current pulses (that is, cell phones) but is recommended for use in all applications to reduce noise on this sensitive high-impedance measurement node.

### 8.2.2.2 Integrated LDO Capacitor

The fuel gauge has an integrated LDO with an output on the  $V_{DD}$  pin of approximately 1.8 V. A capacitor of value at least 2.2  $\mu\text{F}$  should be connected between the  $V_{DD}$  pin and  $V_{SS}$ . The capacitor must be placed close to the gauge IC and have short traces to both the  $V_{DD}$  pin and  $V_{SS}$ . This regulator must not be used to provide power for other devices in the system.

## 8.2.3 External Thermistor Support

The fuel gauge temperature sensing circuitry is designed to work with a negative temperature coefficient-type (NTC) thermistor with a characteristic 10-k $\Omega$  resistance at room temperature (25°C). The default curve-fitting coefficients configured in the fuel gauge specifically assume a Semitec 103AT type thermistor profile and so that is the default recommendation for thermistor selection purposes. Moving to a separate thermistor resistance profile (for example, JT-2 or others) requires an update to the default thermistor coefficients which can be modified in RAM to ensure highest accuracy temperature measurement performance.

## 8.2.4 Application Curves

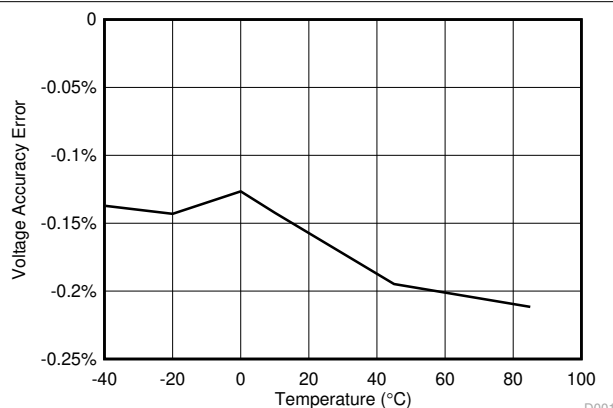


Figure 8-2. Voltage Accuracy Error

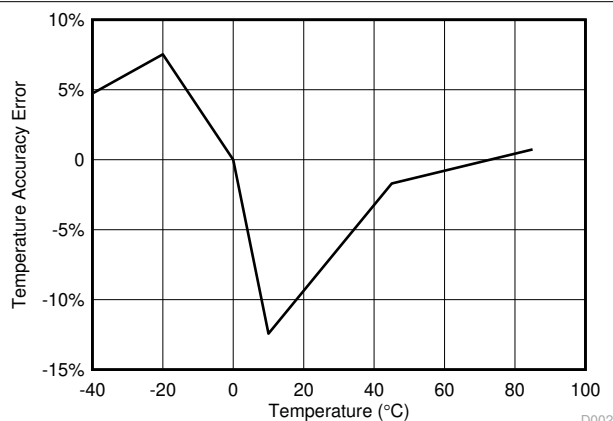


Figure 8-3. Internal Temperature Accuracy Error

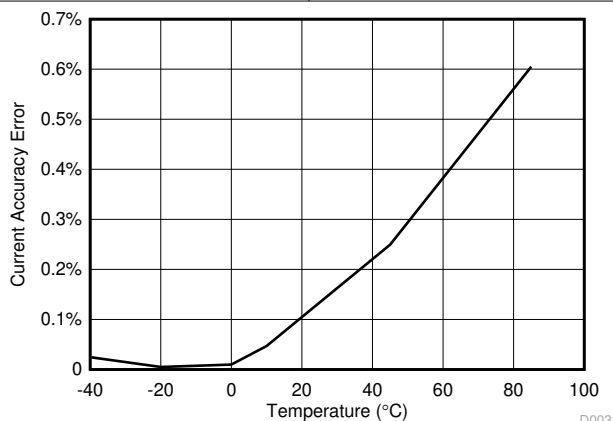


Figure 8-4. Current Accuracy Error

## 9 Power Supply Recommendation

### 9.1 Power Supply Decoupling

The battery connection on the BAT pin is used for two purposes:

- To supply power to the fuel gauge
- To provide an input for voltage measurement of the battery.

A capacitor of value of at least 1  $\mu\text{F}$  should be connected between BAT and  $V_{\text{SS}}$ . The capacitor should be placed close to the gauge IC and have short traces to both the BAT pin and  $V_{\text{SS}}$ .

The fuel gauge has an integrated LDO with an output on the  $V_{\text{DD}}$  pin of approximately 1.8 V. A capacitor of value at least 0.47  $\mu\text{F}$  should be connected between the  $V_{\text{DD}}$  pin and  $V_{\text{SS}}$ . The capacitor should be placed close to the gauge IC and have short traces to both the  $V_{\text{DD}}$  pin and  $V_{\text{SS}}$ . This regulator must not be used to provide power for other devices in the system.

## 10 Layout

### 10.1 Layout Guidelines

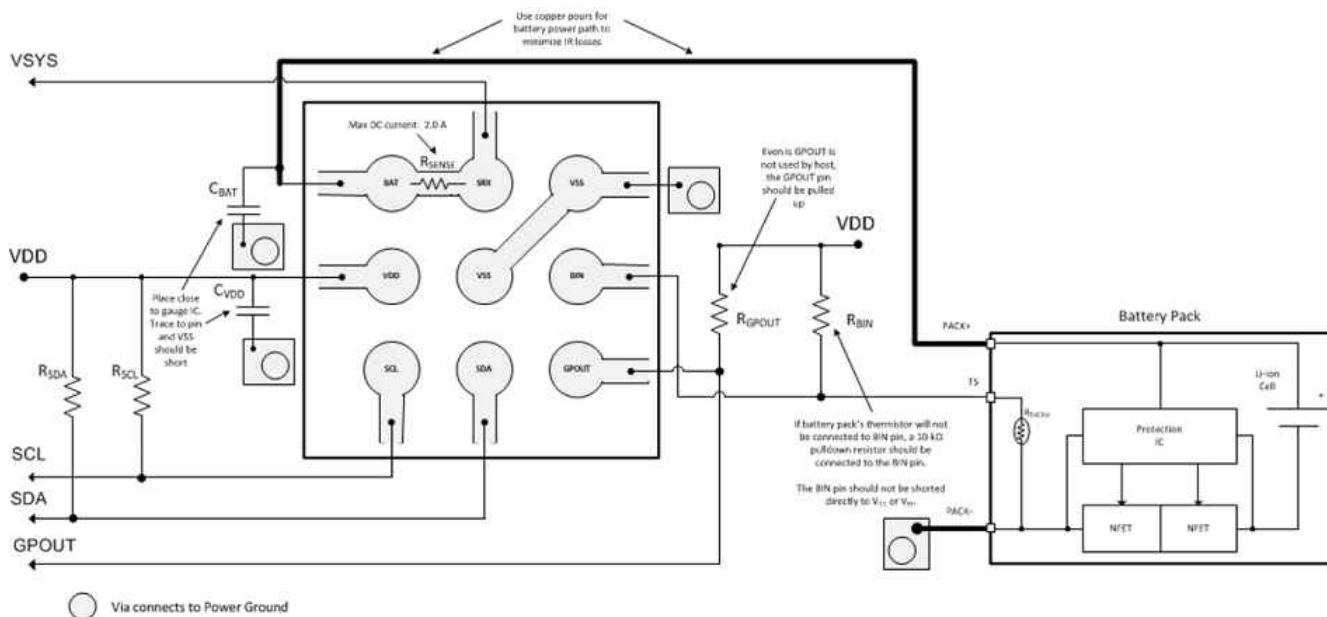
- A capacitor of a value of at least 2.2  $\mu\text{F}$  is connected between the  $V_{\text{DD}}$  pin and  $V_{\text{SS}}$ . The capacitor should be placed close to the gauge IC and have short traces to both the  $V_{\text{DD}}$  pin and  $V_{\text{SS}}$ . This regulator must not be used to provide power for other devices in the system.
- It is required to have a capacitor of at least 1.0  $\mu\text{F}$  connect between the BAT pin and  $V_{\text{SS}}$  if the connection between the battery pack and the gauge BAT pin has the potential to pick up noise. The capacitor should be placed close to the gauge IC and have short traces to both the  $V_{\text{DD}}$  pin and  $V_{\text{SS}}$ .
- If the external pullup resistors on the SCL and SDA lines will be disconnected from the host during low-power operation, it is recommended to use external 1-M $\Omega$  pulldown resistors to  $V_{\text{SS}}$  to avoid floating inputs to the I<sup>2</sup>C engine.
- The value of the SCL and SDA pullup resistors should take into consideration the pullup voltage and the bus capacitance. Some recommended values, assuming a bus capacitance of 10 pF, can be seen in [Table 10-1](#).

**Table 10-1. Recommended Values for SCL and SDA Pullup Resistors**

| VPU             | 1.8 V                                                      |               | 3.3 V                                                      |                |
|-----------------|------------------------------------------------------------|---------------|------------------------------------------------------------|----------------|
|                 | Range                                                      | Typical       | Range                                                      | Typical        |
| $R_{\text{PU}}$ | $400\ \Omega \leq R_{\text{PU}} \leq 37.6\ \text{k}\Omega$ | 10 k $\Omega$ | $900\ \Omega \leq R_{\text{PU}} \leq 29.2\ \text{k}\Omega$ | 5.1 k $\Omega$ |

- If the host is not using the GPOUT functionality, then it is recommended that GPOUT be connected to a GPIO of the host so that in cases where the device is in SHUTDOWN, toggling GPOUT can wake the gauge up from the SHUTDOWN state.
- If the battery pack thermistor is not connected to the BIN pin, the BIN pin should be pulled down to  $V_{\text{SS}}$  with a 10-k $\Omega$  resistor.
- The BIN pin should not be shorted directly to  $V_{\text{DD}}$  or  $V_{\text{SS}}$ .
- The actual device ground is pin 3 ( $V_{\text{SS}}$ ).
- Kelvin connects the BAT pin to the battery PACKP terminal.

### 10.2 Layout Example



**Figure 10-1. BQ27427 Board Layout**

## 11 Device and Documentation Support

### 11.1 Documentation Support

#### 11.1.1 Third-Party Products Disclaimer

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#### 11.1.2 Related Documentation

- [BQ27427 Technical Reference Manual](#)
- [Single Cell Gas Gauge Circuit Design](#)
- [Single Cell Impedance Track Printed-Circuit Board Layout Guide](#)
- [ESD and RF Mitigation in Handheld Battery Electronics](#)

### 11.2 Trademarks

Impedance Track™ and NanoFree™ are trademarks of Texas Instruments.  
All trademarks are the property of their respective owners.

### 11.3 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 11.4 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

## 12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

## PACKAGING INFORMATION

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2) | Lead finish/<br>Ball material<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|-----------------|--------------------------------------|----------------------|--------------|-------------------------|-------------------------|
| BQ27427YZFR      | ACTIVE        | DSBGA        | YZF                | 9    | 3000           | RoHS & Green    | SNAGCU                               | Level-1-260C-UNLIM   | -40 to 85    | BQ27427                 | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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## TAPE AND REEL INFORMATION



\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| BQ27427YZFR | DSBGA        | YZF             | 9    | 3000 | 180.0              | 8.4                | 1.78    | 1.78    | 0.69    | 4.0     | 8.0    | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-------------|--------------|-----------------|------|------|-------------|------------|-------------|
| BQ27427YZFR | DSBGA        | YZF             | 9    | 3000 | 182.0       | 182.0      | 20.0        |

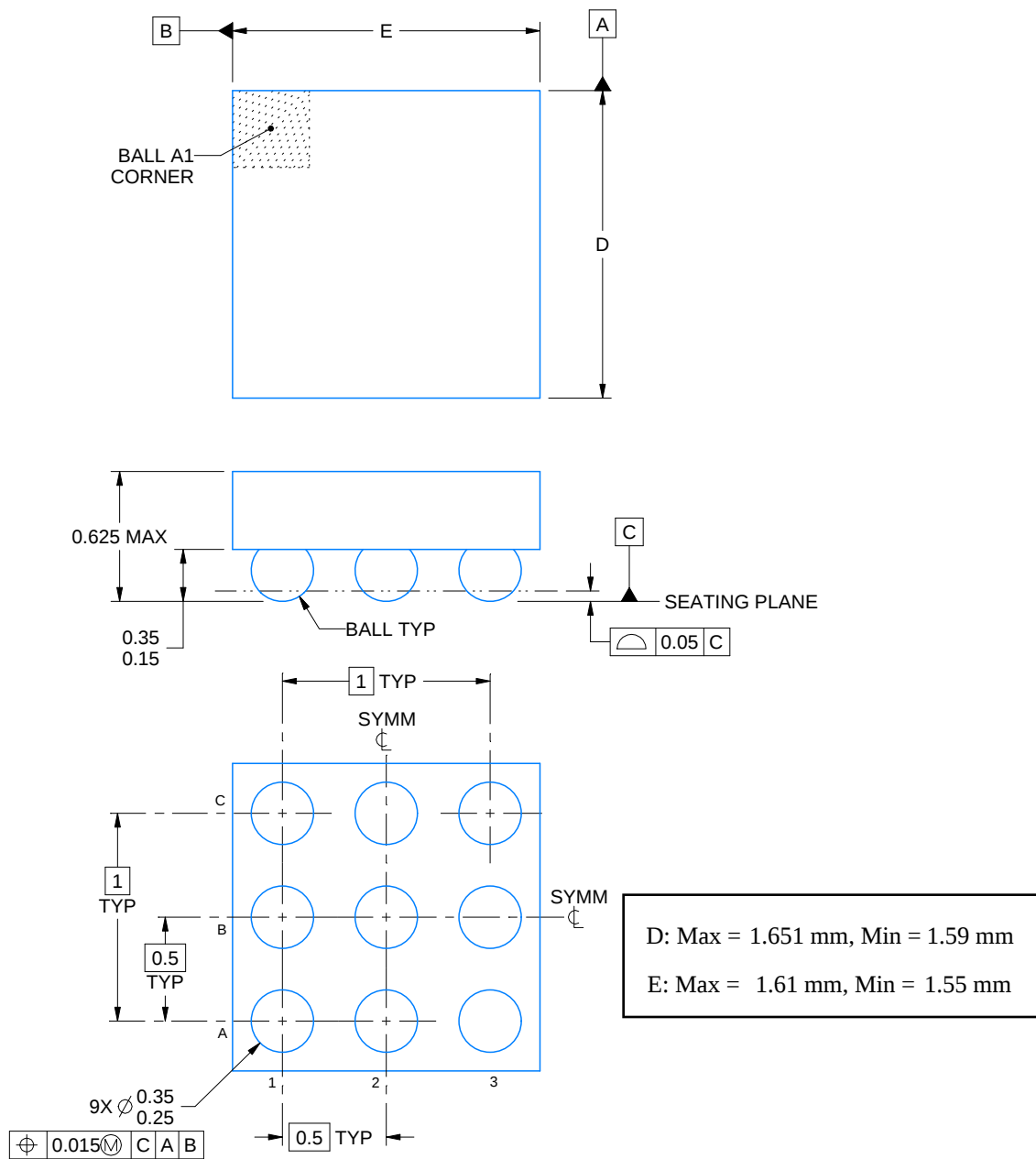
YZF0009



# PACKAGE OUTLINE

DSBGA - 0.625 mm max height

DIE SIZE BALL GRID ARRAY



4219558/A 10/2018

## NOTES:

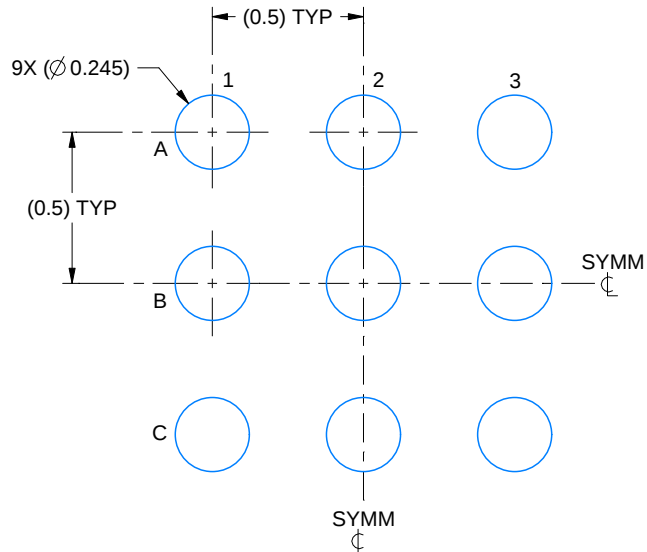
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.

# EXAMPLE BOARD LAYOUT

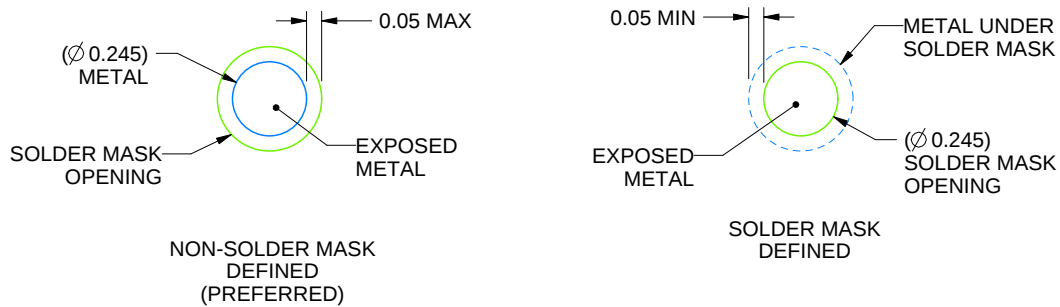
YZF0009

DSBGA - 0.625 mm max height

DIE SIZE BALL GRID ARRAY



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE: 40X



SOLDER MASK DETAILS  
NOT TO SCALE

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NOTES: (continued)

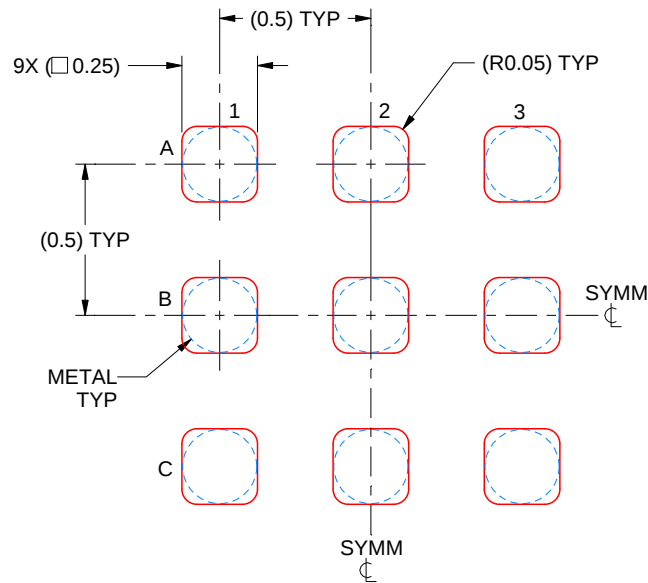
- Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. See Texas Instruments Literature No. SNVA009 ([www.ti.com/lit/snva009](http://www.ti.com/lit/snva009)).

# EXAMPLE STENCIL DESIGN

YZF0009

DSBGA - 0.625 mm max height

DIE SIZE BALL GRID ARRAY



SOLDER PASTE EXAMPLE  
BASED ON 0.1 mm THICK STENCIL  
SCALE: 40X

4219558/A 10/2018

NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.

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