

74AUP1G158

Low-power 2-input multiplexer; inverting

Rev. 7 — 19 October 2021

Product data sheet

1. General description

The 74AUP1G158 is a single 2-input inverting multiplexer. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; $I_{CC} = 0.9 \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G158GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2
74AUP1G158GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1G158GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115
74AUP1G158GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202

4. Marking

Type number	Marking code [1]
74AUP1G158GW	aU
74AUP1G158GM	aU
74AUP1G158GN	aU
74AUP1G158GS	aU

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

Fig. 1. Logic symbol

Fig. 2. IEC logic symbol

Fig. 3. Functional diagram

Fig. 4. Logic diagram

6. Pinning information

6.1. Pinning

Fig. 5. Pin configuration SOT363-2 (TSSOP6)

Fig. 6. Pin configuration SOT886 (XSON6)

Fig. 7. Pin configuration SOT1115 and SOT1202 (XSON6)

6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
I1	1	data input from source 1
GND	2	ground (0 V)
I0	3	data input from source 0
$\bar{Y}$	4	multiplexer output
V _{CC}	5	supply voltage
S	6	common data select input

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Input			Output
S	I1	I0	$\bar{Y}$
L	X	L	H
L	X	H	L
H	L	X	H
H	H	X	L

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
V _I	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
V _O	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
I _O	output current	V _O = 0 V to V _{CC}	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [2]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT363-2 (TSSOP6) package: P_{tot} derates linearly with 3.7 mW/K above 83 °C.

For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: P_{tot} derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
V_I	input voltage		0	3.6	V
V_O	output voltage	Active mode	0	V_{CC}	V
		Power-down mode; $V_{CC} = 0$ V	0	3.6	V
T_{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8$ V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$T_{amb} = 25$ °C						
V_{IH}	HIGH-level input voltage	$V_{CC} = 0.8$ V	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9$ V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3$ V to 2.7 V	1.6	-	-	V
		$V_{CC} = 3.0$ V to 3.6 V	2.0	-	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 0.8$ V	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9$ V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3$ V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0$ V to 3.6 V	-	-	0.9	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = -20$ μ A; $V_{CC} = 0.8$ V to 3.6 V	$V_{CC} - 0.1$	-	-	V
		$I_O = -1.1$ mA; $V_{CC} = 1.1$ V	$0.75 \times V_{CC}$	-	-	V
		$I_O = -1.7$ mA; $V_{CC} = 1.4$ V	1.11	-	-	V
		$I_O = -1.9$ mA; $V_{CC} = 1.65$ V	1.32	-	-	V
		$I_O = -2.3$ mA; $V_{CC} = 2.3$ V	2.05	-	-	V
		$I_O = -3.1$ mA; $V_{CC} = 2.3$ V	1.9	-	-	V
		$I_O = -2.7$ mA; $V_{CC} = 3.0$ V	2.72	-	-	V
		$I_O = -4.0$ mA; $V_{CC} = 3.0$ V	2.6	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20$ μ A; $V_{CC} = 0.8$ V to 3.6 V	-	-	0.1	V
		$I_O = 1.1$ mA; $V_{CC} = 1.1$ V	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7$ mA; $V_{CC} = 1.4$ V	-	-	0.31	V
		$I_O = 1.9$ mA; $V_{CC} = 1.65$ V	-	-	0.31	V
		$I_O = 2.3$ mA; $V_{CC} = 2.3$ V	-	-	0.31	V
		$I_O = 3.1$ mA; $V_{CC} = 2.3$ V	-	-	0.44	V
		$I_O = 2.7$ mA; $V_{CC} = 3.0$ V	-	-	0.31	V
		$I_O = 4.0$ mA; $V_{CC} = 3.0$ V	-	-	0.44	V
I_I	input leakage current	$V_I = \text{GND}$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	± 0.1	μ A
I_{OFF}	power-off leakage current	V_I or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	± 0.2	μ A

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
ΔI_{OFF}	additional power-off leakage current	V_I or $V_O = 0\text{ V}$ to 3.6 V ; $V_{CC} = 0\text{ V}$ to 0.2 V	-	-	± 0.2	μA
I_{CC}	supply current	$V_I = \text{GND}$ or V_{CC} ; $I_O = 0\text{ A}$; $V_{CC} = 0.8\text{ V}$ to 3.6 V	-	-	0.5	μA
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6\text{ V}$; $I_O = 0\text{ A}$; $V_{CC} = 3.3\text{ V}$ [1]	-	-	40	μA
C_I	input capacitance	$V_{CC} = 0\text{ V}$ to 3.6 V ; $V_I = \text{GND}$ or V_{CC}	-	0.8	-	pF
C_O	output capacitance	$V_O = \text{GND}$; $V_{CC} = 0\text{ V}$	-	1.7	-	pF
$T_{amb} = -40\text{ }^\circ\text{C}$ to $+85\text{ }^\circ\text{C}$						
V_{IH}	HIGH-level input voltage	$V_{CC} = 0.8\text{ V}$	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 0.9\text{ V}$ to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3\text{ V}$ to 2.7 V	1.6	-	-	V
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	2.0	-	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 0.8\text{ V}$	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 0.9\text{ V}$ to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3\text{ V}$ to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0\text{ V}$ to 3.6 V	-	-	0.9	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = -20\text{ }\mu\text{A}$; $V_{CC} = 0.8\text{ V}$ to 3.6 V	$V_{CC} - 0.1$	-	-	V
		$I_O = -1.1\text{ mA}$; $V_{CC} = 1.1\text{ V}$	$0.7 \times V_{CC}$	-	-	V
		$I_O = -1.7\text{ mA}$; $V_{CC} = 1.4\text{ V}$	1.03	-	-	V
		$I_O = -1.9\text{ mA}$; $V_{CC} = 1.65\text{ V}$	1.30	-	-	V
		$I_O = -2.3\text{ mA}$; $V_{CC} = 2.3\text{ V}$	1.97	-	-	V
		$I_O = -3.1\text{ mA}$; $V_{CC} = 2.3\text{ V}$	1.85	-	-	V
		$I_O = -2.7\text{ mA}$; $V_{CC} = 3.0\text{ V}$	2.67	-	-	V
		$I_O = -4.0\text{ mA}$; $V_{CC} = 3.0\text{ V}$	2.55	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20\text{ }\mu\text{A}$; $V_{CC} = 0.8\text{ V}$ to 3.6 V	-	-	0.1	V
		$I_O = 1.1\text{ mA}$; $V_{CC} = 1.1\text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7\text{ mA}$; $V_{CC} = 1.4\text{ V}$	-	-	0.37	V
		$I_O = 1.9\text{ mA}$; $V_{CC} = 1.65\text{ V}$	-	-	0.35	V
		$I_O = 2.3\text{ mA}$; $V_{CC} = 2.3\text{ V}$	-	-	0.33	V
		$I_O = 3.1\text{ mA}$; $V_{CC} = 2.3\text{ V}$	-	-	0.45	V
		$I_O = 2.7\text{ mA}$; $V_{CC} = 3.0\text{ V}$	-	-	0.33	V
		$I_O = 4.0\text{ mA}$; $V_{CC} = 3.0\text{ V}$	-	-	0.45	V
I_I	input leakage current	$V_I = \text{GND}$ to 3.6 V ; $V_{CC} = 0\text{ V}$ to 3.6 V	-	-	± 0.5	μA
I_{OFF}	power-off leakage current	V_I or $V_O = 0\text{ V}$ to 3.6 V ; $V_{CC} = 0\text{ V}$	-	-	± 0.5	μA
ΔI_{OFF}	additional power-off leakage current	V_I or $V_O = 0\text{ V}$ to 3.6 V ; $V_{CC} = 0\text{ V}$ to 0.2 V	-	-	± 0.6	μA
I_{CC}	supply current	$V_I = \text{GND}$ or V_{CC} ; $I_O = 0\text{ A}$; $V_{CC} = 0.8\text{ V}$ to 3.6 V	-	-	0.9	μA
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 0.6\text{ V}$; $I_O = 0\text{ A}$; $V_{CC} = 3.3\text{ V}$ [1]	-	-	50	μA

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = -40 °C to +125 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	µA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.75	µA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	1.4	µA
ΔI _{CC}	additional supply current	V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V [1]	-	-	75	µA

[1] One input at V_{CC} - 0.6 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 pF										
t _{pd}	propagation delay	I0, I1 or S to $\bar{Y}$; see Fig. 8 [2]								
		V _{CC} = 0.8 V	-	20.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.4	5.7	11.2	2.2	11.6	2.2	11.8	ns
		V _{CC} = 1.4 V to 1.6 V	1.8	4.0	6.6	1.9	7.2	1.9	7.5	ns
		V _{CC} = 1.65 V to 1.95 V	1.6	3.2	5.2	1.4	5.9	1.4	6.2	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	2.5	3.7	1.2	4.2	1.2	4.4	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	2.2	3.2	0.9	3.5	0.9	3.6	ns
C _L = 10 pF										
t _{pd}	propagation delay	I0, I1 or S to $\bar{Y}$; see Fig. 8 [2]								
		V _{CC} = 0.8 V	-	23.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	6.6	13.0	2.5	13.6	2.5	13.8	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.6	7.9	2.0	8.5	2.0	8.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.8	6.1	1.9	6.7	1.9	7.0	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	3.1	4.4	1.5	4.9	1.5	5.2	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	2.8	4.0	1.4	4.2	1.4	4.4	ns
C _L = 15 pF										
t _{pd}	propagation delay	I0, I1 or S to $\bar{Y}$; see Fig. 8 [2]								
		V _{CC} = 0.8 V	-	27.1	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.0	7.4	14.7	2.8	15.4	2.8	15.7	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	5.2	8.8	2.4	9.7	2.4	10.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.3	6.8	2.2	7.7	2.2	8.1	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.5	5.1	1.9	5.6	1.9	5.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.9	3.2	4.5	1.6	4.9	1.6	5.1	ns
C _L = 30 pF										
t _{pd}	propagation delay	I0, I1 or S to $\bar{Y}$; see Fig. 8 [2]								
		V _{CC} = 0.8 V	-	35.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	9.7	19.6	3.5	20.5	3.5	21.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	6.7	11.4	3.2	12.6	3.2	13.2	ns
		V _{CC} = 1.65 V to 1.95 V	3.0	5.6	8.9	2.9	10.0	2.9	10.5	ns
		V _{CC} = 2.3 V to 2.7 V	2.9	4.6	6.5	2.6	7.1	2.6	7.6	ns
		V _{CC} = 3.0 V to 3.6 V	2.7	4.3	5.8	2.5	6.7	2.5	7.1	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 pF, 10 pF, 15 pF and 30 pF										
C _{PD}	power dissipation capacitance	f _i = 1 MHz; V _I = GND to V _{CC} [3]								
		V _{CC} = 0.8 V	-	2.6	-	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.8	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.1	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.6	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.2	-	-	-	-	-	pF

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

11.1. Waveforms

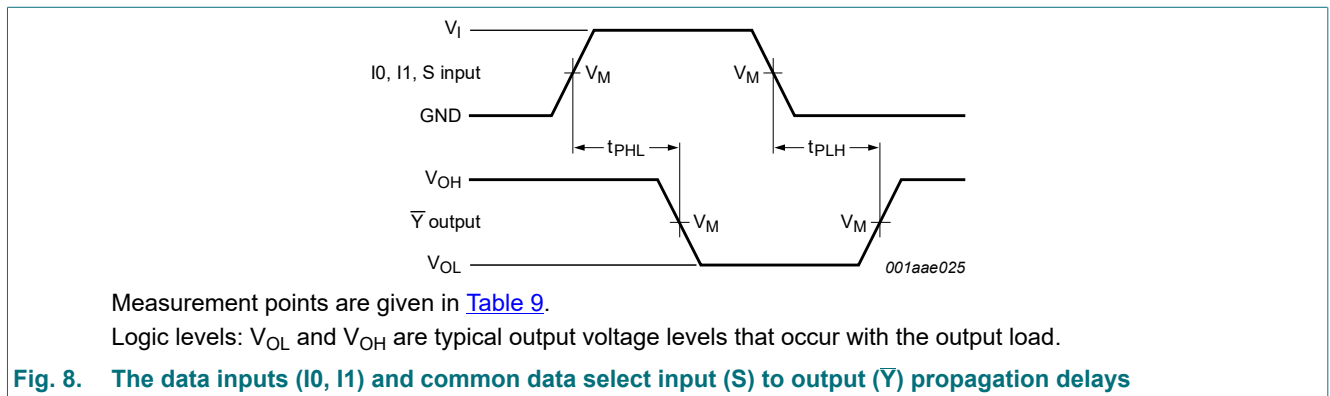
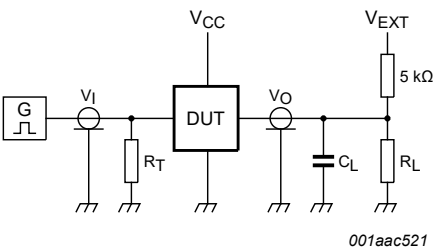


Table 9. Measurement points

Supply voltage	Output	Input		
V_{CC}	V_M	V_M	V_I	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V_{CC}	$\leq 3.0 \text{ ns}$



Test data is given in [Table 10](#).
Definitions for test circuit:
 R_L = Load resistance;
 C_L = Load capacitance including jig and probe capacitance;
 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;
 V_{EXT} = External voltage for measuring switching times.

Fig. 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V _{EXT}		
V _{CC}	C _L	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times, $R_L = 5\text{ k}\Omega$.
For measuring propagation delays, setup and hold times and pulse width, $R_L = 1\text{ M}\Omega$.

12. Package outline

TSSOP6: plastic thin shrink small outline package; 6 leads; body width 1.25 mm SOT363-2

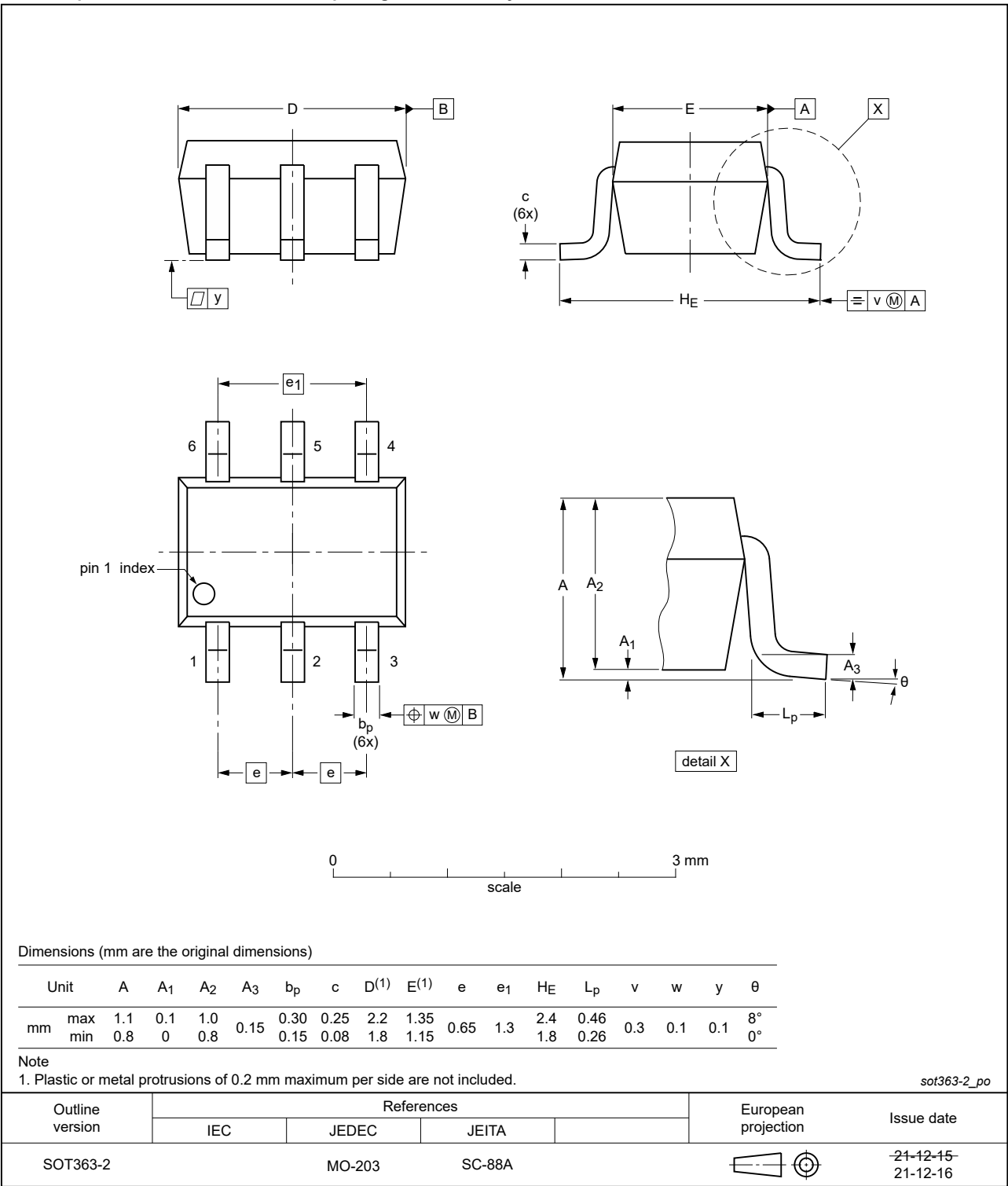


Fig. 10. Package outline SOT363-2 (TSSOP6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886



Fig. 11. Package outline SOT886 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115

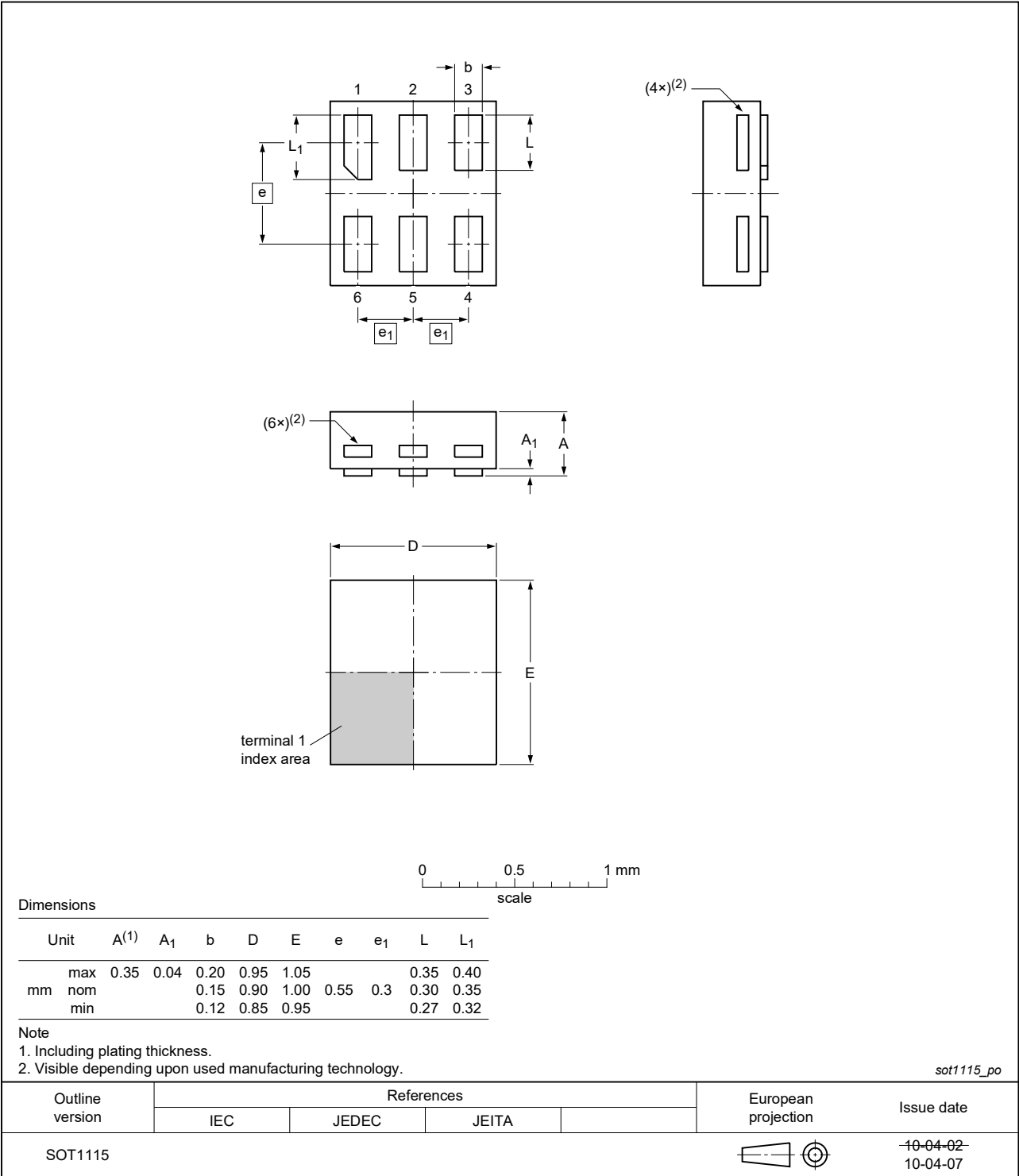
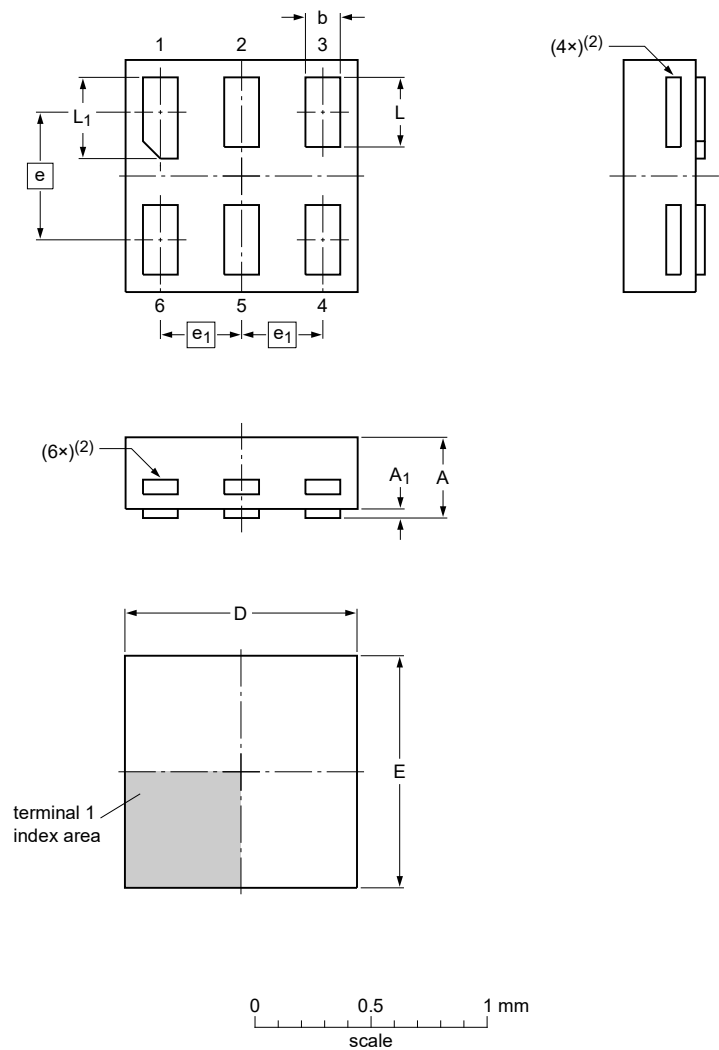


Fig. 12. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Dimensions

Unit	A ⁽¹⁾	A ₁	b	D	E	e	e ₁	L	L ₁
mm	max	0.35	0.04	0.20	1.05	1.05		0.35	0.40
	nom			0.15	1.00	1.00	0.55	0.30	0.35
	min			0.12	0.95	0.95		0.27	0.32

Note

1. Including plating thickness.
2. Visible depending upon used manufacturing technology.

sot1202_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1202						10-04-02 10-04-06

Fig. 13. Package outline SOT1202 (XSON6)

13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G158 v.7	20211019	Product data sheet	-	74AUP1G158 v.6
Modifications:	• Section 2 updated. • SOT363 (SC-88) package changed to SOT363-2 (TSSOP6).			
74AUP1G158 v.6	20201026	Product data sheet	-	74AUP1G158 v.5
Modifications:	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate. • Type number 74AUP1G158GF (SOT891 / XSON6) removed. • Section 1 and Section 2 updated. • Table 5: Derating values for P_{tot} total power dissipation updated.			
74AUP1G158 v.5	20120803	Product data sheet	-	74AUP1G158 v.4
Modifications:	• Package outline drawing of SOT886 (Fig. 11) modified.			
74AUP1G158 v.4	20111124	Product data sheet	-	74AUP1G158 v.3
Modifications:	• Legal pages updated.			
74AUP1G158 v.3	20100713	Product data sheet	-	74AUP1G158 v.2
74AUP1G158 v.2	20090702	Product data sheet	-	74AUP1G158 v.1
74AUP1G158 v.1	20061107	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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