

SOTiny™ Low Resistance, Low -Voltage Single-Supply SPDT Switch

Features

- Low On-Resistance: 10-ohm max.
- R_{ON} Matching: 2-ohm max.
- R_{ON} Flatness: 3.5-ohm max.
- Low 0.5nA Input Leakage at 25 °C
- 2V to 6V Single-Supply Operation
- Fast Switching Time
 - 15ns t_{ON}
 - 7ns t_{OFF}
- Break-Before-Make Switching Guaranteed
- 5pC max Charge Injection
- 225MHz Channel Bandwidth
- 76dB Off-Isolation at 1MHz
- TTL/CMOS Logic Compatible
- Low Power Consumption: 5μW
- Improved Direct Replacement for MAX4599
- Packaging (Pb-free & Green available):
 - 6-pin Small Compact SC70 (C)
 - 6-pin SOT23 (T)

Applications

- Communication Circuits
- Cellular Phones
- Audio and Video Signal Routing
- Portable Battery-Operated Equipment
- Data Acquisition Systems
- Computer Peripherals
- Telecommunications
- Relay Replacement
- Wireless Terminals and Peripherals

Truth Table

	PI5A4599A	
Logic	NC	NO
0	ON	OFF
1	OFF	ON

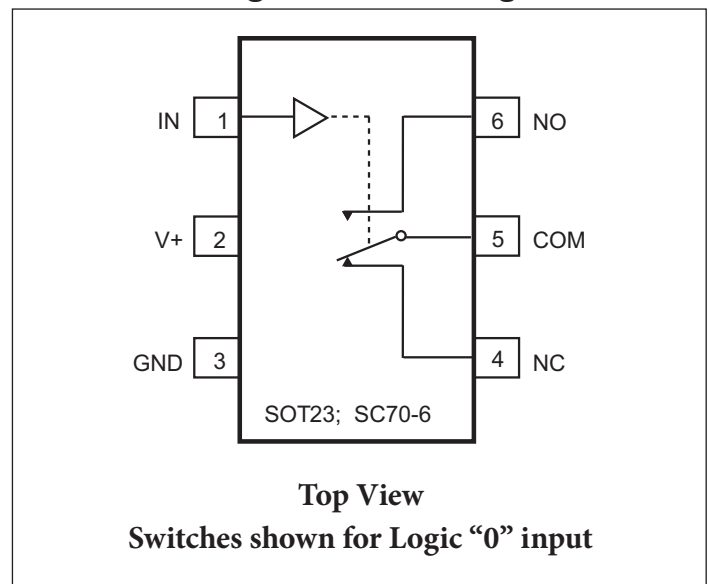
Description

The PI5A4599A is an improved, direct replacement for the MAX4599 single-pole, double-throw (SPDT) analog switch. Improved specifications include a low maximum ON resistance of 10-ohm and fast switching times (t_{ON} = 15ns max., t_{OFF} = 7ns max.) with 5V supply operation. With a 2.5V supply, resistance is a low 400-ohm max.

Specifications are given for 2.5V, 3.3V and 5V power supply operation. Operating voltage range is 2.0V to 6.0V.

To minimize PC board area use, the PI5A4599A is available in a compact 6-pin SC70 package. Operating temperature range is -40°C to 85°C.

Functional Diagram, Pin Configuration



Absolute Maximum Ratings

Voltages Referenced to GND

V+.....-0.5V to +7V

V_{IN}, V_{COM}, V_{NC}, V_{NO} (Note 1).....-0.5V to V_{CC} +2V
or 30mA, whichever occurs first

Current (any terminal).....±30mA

Peak Current, COM, NO, NC

(Pulsed at 1ms, 10% duty cycle).....±30mA

Thermal Information

Continuous Power Dissipation

SC70-6 (derate 3.1mW/°C above +70°C).....245mW

Storage Temperature.....-65°C to +150°C

Lead Temperature (soldering, 10s).....+300°C

Note: Signals on NC, NO, COM, or IN exceeding V+ or GND are clamped by internal diodes. Limit forward diode current to 30mA.

Caution: Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.

Electrical Specifications - Single +5V Supply (V+ = +5V ±10%, GND = 0V, VINH = 2.4V, VINL = 0.8V)

Parameter	Symbol	Conditions	Temp. (C°)	Min. ⁽¹⁾	Typ. ⁽²⁾	Max. ⁽¹⁾	Units
Analog Switch							
Analog Signal Range ⁽³⁾	V _{ANALOG}		Full			V+	V
On Resistance	R _{ON}	V+ = 4.5V, I _{COM} = -30mA, V _{NO} or V _{NC} = +2.5V	25		7	8	Ω
			Full			10	
On-Resistance Match Between Channels ⁽⁴⁾	ΔR _{ON}		25		0.1	0.5	
			Full			1	
On-Resistance Flatness ⁽⁵⁾	RFLAT _(ON)	V+ = 5V, I _{COM} = -30mA, V _{NO} or V _{NC} = 1V, 2.5V, 4V	25		2.72	3.5	
			Full			4	
NO or NC Off Leakage Current ⁽⁶⁾	I _{NO(OFF)} or I _{NC(OFF)}	V+ = 5.5V, V _{COM} = 0V, V _{NO} or V _{NC} = 4.5V	25	-0.5	0.18	0.5	nA
			Full	-5		5	
COM Off Leakage Cur- rent ⁽⁶⁾	I _{COM(OFF)}	V+ = 5.5V, V _{COM} = +4.5V, V _{NO} or V _{NC} = ± 0V	25	-1.0	20	1.0	
			Full	-10		10	
On Leakage Current ⁽⁶⁾	I _{COM(ON)}	V+ = 5.5V, V _{COM} = +4.5V V _{NO} or V _{NC} = +4.5V	25	-1.0	20	1.0	
			Full	-10		10	

Continued

Electrical Specifications - Single +5V Supply ($V_+ = +5V \pm 10\%$, $GND = 0V$, $V_{INH} = 2.4V$, $V_{INL} = 0.8V$)

Parameter	Symbol	Conditions	Temp. (C°)	Min. ⁽¹⁾	Typ. ⁽²⁾	Max. ⁽¹⁾	Units
Logic Input							
Input High Voltage	V _{IH}	Guaranteed logic High Level	Full	2			V
Input Low Voltage	V _{IL}	Guaranteed logic Low Level				0.8	
Input Current with Voltage High	I _{INH}	V _{IN} = 2.4V, all others = 0.8V		-1	0.005	1	μA
Input Current with Voltage Low	I _{INL}	V _{IN} = 0.8V, all others = 2.4V		-1	0.005	1	
Dynamic							
Turn-On Time	t _{ON}	V _{CC} = 5V, Figure 1	25		7	15	ns
			Full				
Turn-Off Time	t _{OFF}		25		1	7	
			Full			10	
Break-Before-Make	t _{BBM}	Figure 3	25			10	
			Full	5			
Charger Injection ⁽³⁾	Q	C _L = 1nF, V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 2	25		1.5	5	pC
Off Isolation	OIRR	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 4			80		dB
Crosstalk ⁽⁸⁾	X _{TALK}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 5			80		
NC or NO Capacitance	C _(OFF)	f = 1MHz, Figure 6			5.0		pF
COM Off Capacitance	C _{COM(OFF)}				5.0		
COM ON Capacitance	C _{COM(ON)}	f = 1MHz, Figure 7			13		
-3dB Bandwidth	BW	R _L = 50Ω, Figure 8	Full		300		MHz
Supply							
Power Supply Range	V+	V _{CC} = 5.5V, V _{IN} = 0V or V+	Full	2		6	V
Positive Supply Current	I+					1	μA

1. The algebraic convention, where most negative value is a minimum and most positive is a maximum, is used in this data sheet.

2. Typical values are for DESIGN AID ONLY, not guaranteed or subject to production testing.

3. Guaranteed by design.

4. $\Delta R_{ON} = R_{ON \text{ max.}} - R_{ON \text{ min.}}$

5. Flatness is defined as the difference between the maximum and minimum value of On-resistance measured.

6. Leakage parameters are 100% tested at maximum rated hot temperature and guaranteed by correlation at +25°C.

7. Off Isolation = $20\log_{10} [V_{COM} / (V_{NO} \text{ or } V_{NC})]$. See Figure 3.

8. Between any two switches. See Figure 4.

Electrical Specifications - Single +3.3V Supply

(V+ = +3.3V ±10%, GND = 0V, VINH = 2.4V, VINL = 0.8V)

Parameter	Symbol	Test Conditions	Temp. (C°)	Min. ⁽¹⁾	Typ. ⁽²⁾	Max. ⁽¹⁾	Units
Analog Switch							
Analog Signal Range ⁽³⁾	V _{ANALOG}			0		V+	V
On Resistance	R _{ON}	V+ = 3V, I _{COM} = -30mA, V _{NO} or V _{NC} = 1.5V	25 Full		12	14.0 17	Ω
On-Resistance Match Between Channels ⁽⁴⁾	ΔR _{ON}	V+ = 3.3V, I _{COM} = -30mA, V _{NO} or V _{NC} = 0.8V, 2.5V	25 Full		0.2	0.5 1	
On-Resistance Flatness ^(3, 5)	R _{FLAT(ON)}		25 Full		5	4 5	
Logic Input							
Input High Voltage	V _{IH}	Guaranteed logic High Level	Full	2			V
Input Low Voltage	V _{IL}	Guaranteed logic Low Level				0.8	
Input High Current	I _{INH}	V _{IN} = 2.4V, all others = 0.8V		-1		1	μA
Input Low Current	I _{INL}	V _{IN} = 0.8V, all others = 2.4V		-1		1	
Dynamic							
Turn-On Time	t _{ON}	V+ = 3.3V, V _{NO} or V _{NC} = 1.5V, Figure 1	25 Full		15	25	ns
Turn-Off Time	t _{OFF}		25 Full		1.5	12 20	
Break-Before-Make	t _{BBM}		25 Full		10		
				5			
Charger Injection ⁽³⁾	Q	C _L = 1nF, V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 2	25		1.3	5	pC
Supply							
Positive Supply Current	I+	V+ = 3.6V, V _{IN} = 0V or V+ All channels on or off	Full			1	μA

- The algebraic convention, where most negative value is a minimum and most positive is a maximum, is used in this data sheet.
- Typical values are for DESIGN AID ONLY, not guaranteed or subject to production testing.
- Guaranteed by design.
- ΔR_{ON} = R_{ON} max. - R_{ON} min.
- Flatness is defined as the difference between the maximum and minimum value of On-resistance measured.
- Leakage parameters are 100% tested at maximum rated hot temperature and guaranteed by correlation at +25°C.
- Off Isolation = 20log₁₀ [V_{COM} / (V_{NO} or V_{NC})]. See Figure 4.
- Between any two switches. See Figure 5.

Electrical Specifications - Single +2.5V Supply

(V+ = +2.5V ±10%, GND = 0V, VINH = 2.4V, VINL = 0.8V)

Parameter	Symbol	Test Conditions	Temp. (C°)	Min. ⁽¹⁾	Typ. ⁽²⁾	Max. ⁽¹⁾	Units
Analog Switch							
Analog Signal Range ⁽³⁾	V _{ANALOG}			0		V+	V
On Resistance	R _{ON}	V+ = 2.5V, I _{COM} = -30mA, V _{NO} or V _{NC} = 1.5V	25		20	22	Ω
			Full			26	
On-Resistance Match Between Channels ⁽⁴⁾	ΔR _{ON}	V+ = 2.5V, I _{COM} = -30mA, V _{NO} or V _{NC} = 0.8V, 2.5V	25		0.3	0.5	
			Full		1		
On-Resistance Flatness ^(3, 5)	RFLAT _(ON)		25		0.5	6	
			Full			6	
Logic Input							
Input High Voltage	V _{IH}	Guaranteed logic High Level	Full	2			V
Input Low Voltage	V _{IL}	Guaranteed logic Low Level				0.8	
Input High Current	I _{INH}	V _{IN} = 2.4V, all others = 0.8V		-1		1	μA
Input Low Current	I _{INL}	V _{IN} = 0.8V, all others = 2.4V		-1		1	
Dynamic							
Turn-On Time	t _{ON}	V+ = 2.5V, V _{NO} or V _{NC} = 1.5V, Figure 1	25		20	30	ns
			Full		–	45	
Turn-Off Time	t _{OFF}		25			20	
			Full		–	30	
Break-Before-Make	t _{BBM}	Figure 3	25		10		
			Full	5			
Charger Injection ⁽³⁾	Q	C _L = 1nF, V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 2	25		0.9	5	pC
Supply							
Positive Supply Current	I+	V+ = 2.5V, V _{IN} = 0V or V+ All channels on or off	Full			1	μA

- The algebraic convention, where most negative value is a minimum and most positive is a maximum, is used in this data sheet.
- Typical values are for DESIGN AID ONLY, not guaranteed or subject to production testing.
- Guaranteed by design.
- ΔR_{ON} = R_{ON} max. - R_{ON} min.
- Flatness is defined as the difference between the maximum and minimum value of On-resistance measured.
- Leakage parameters are 100% tested at maximum rated hot temperature and guaranteed by correlation at +25°C.
- Off Isolation = 20log10 [V_{COM} / (V_{NO} or V_{NC})]. See Figure 4.
- Between any two switches. See Figure 5.

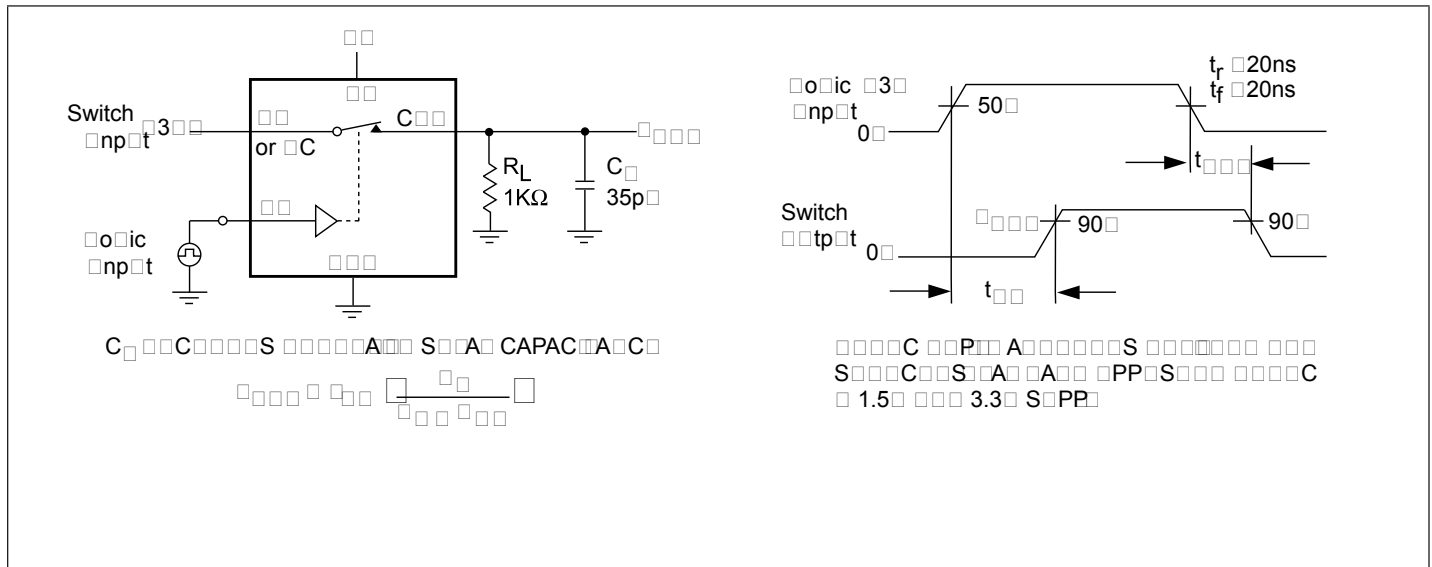


Figure 1. Switching Time

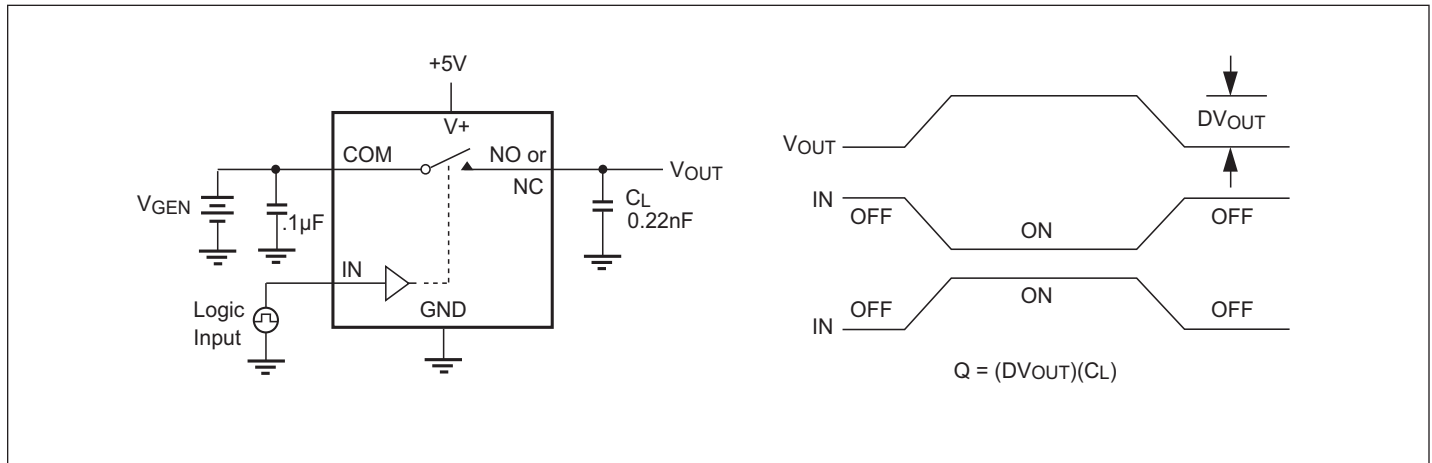


Figure 2. Charge Injection

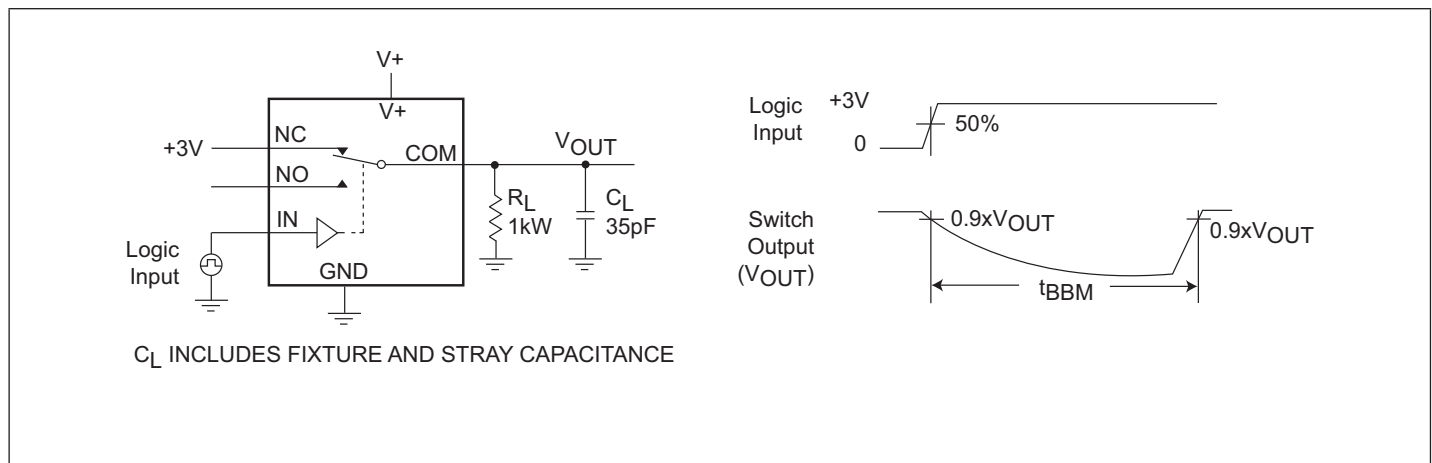


Figure 3. Break-Before-Make Interval

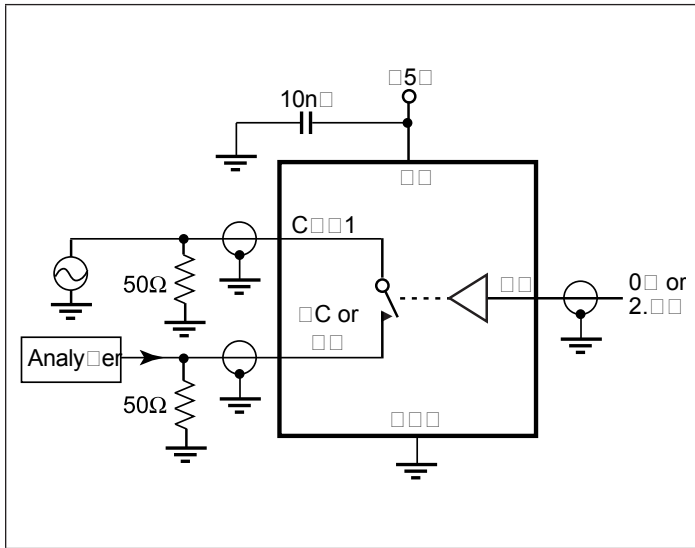


Figure 4. Off Isolation/On-Channel Bandwidth

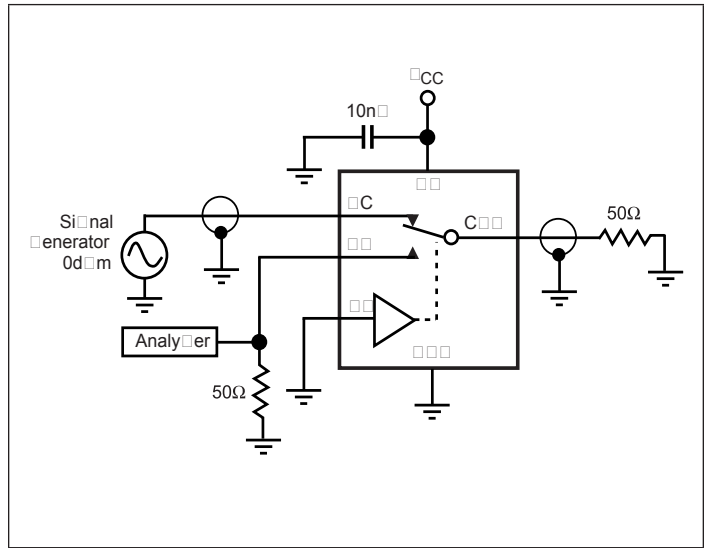


Figure 5. Crosstalk

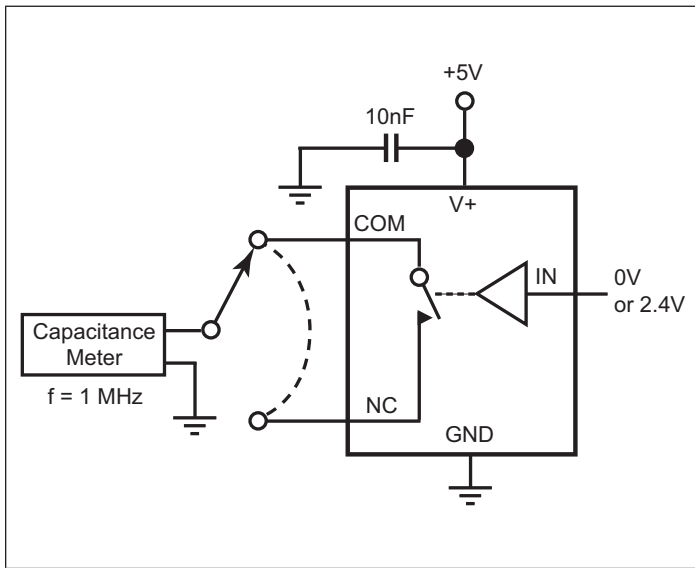


Figure 6. Channel-Off Capacitance

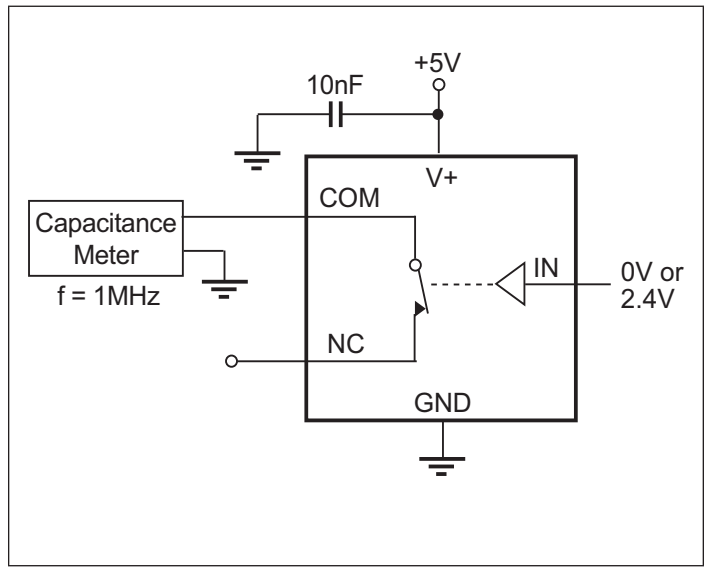


Figure 7. Channel-On Capacitance

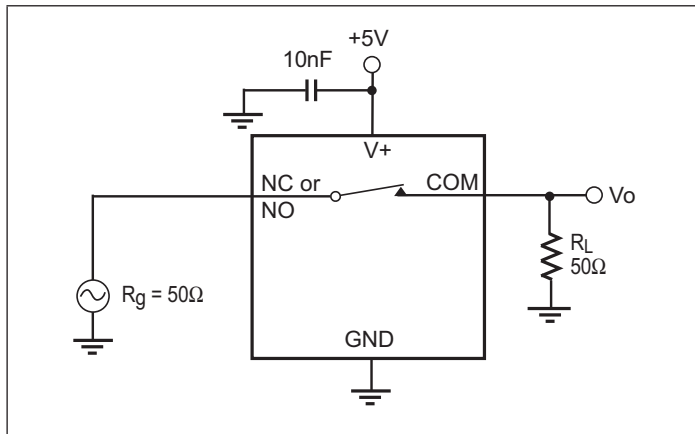
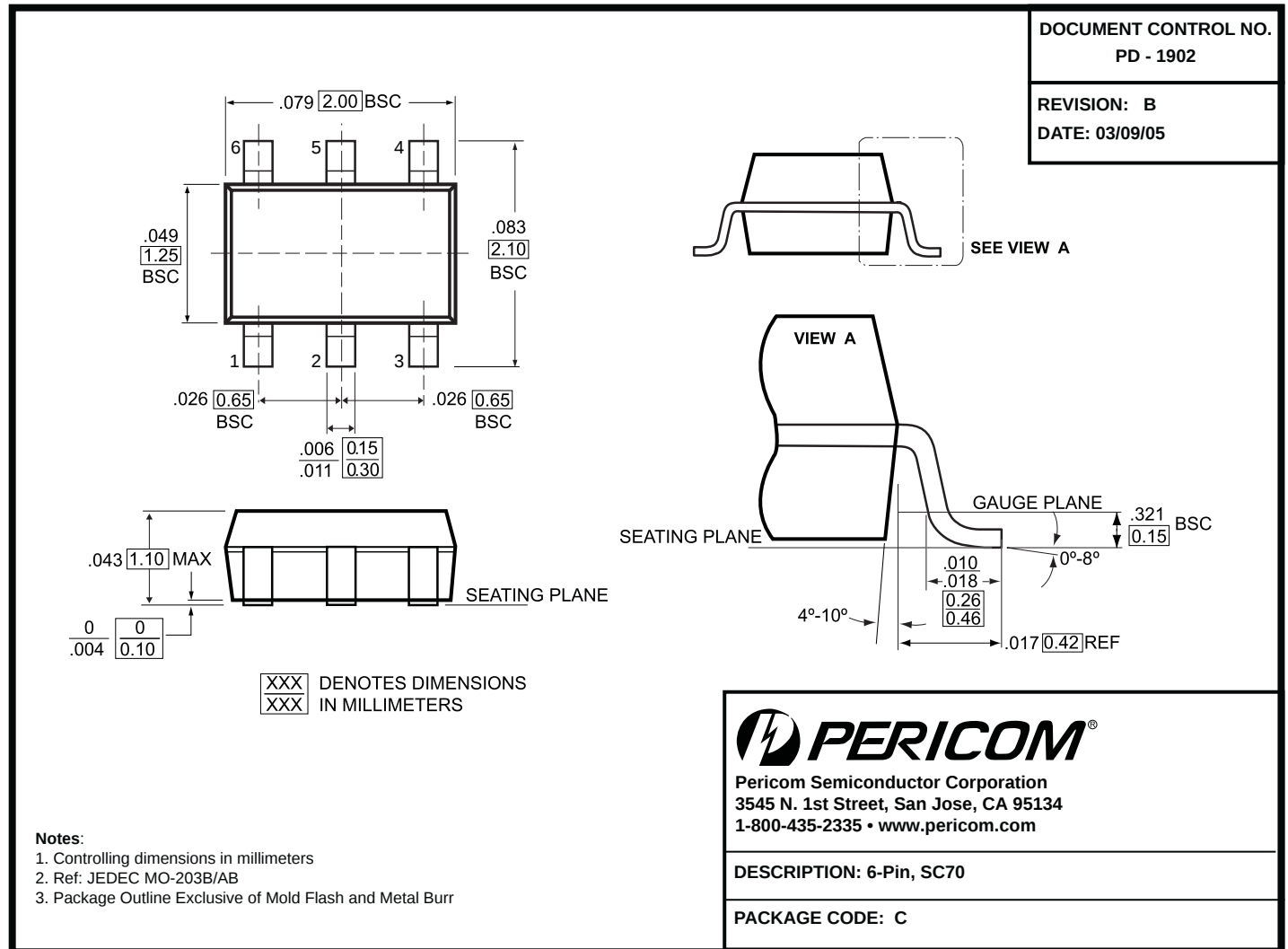


Figure 8. Bandwidth

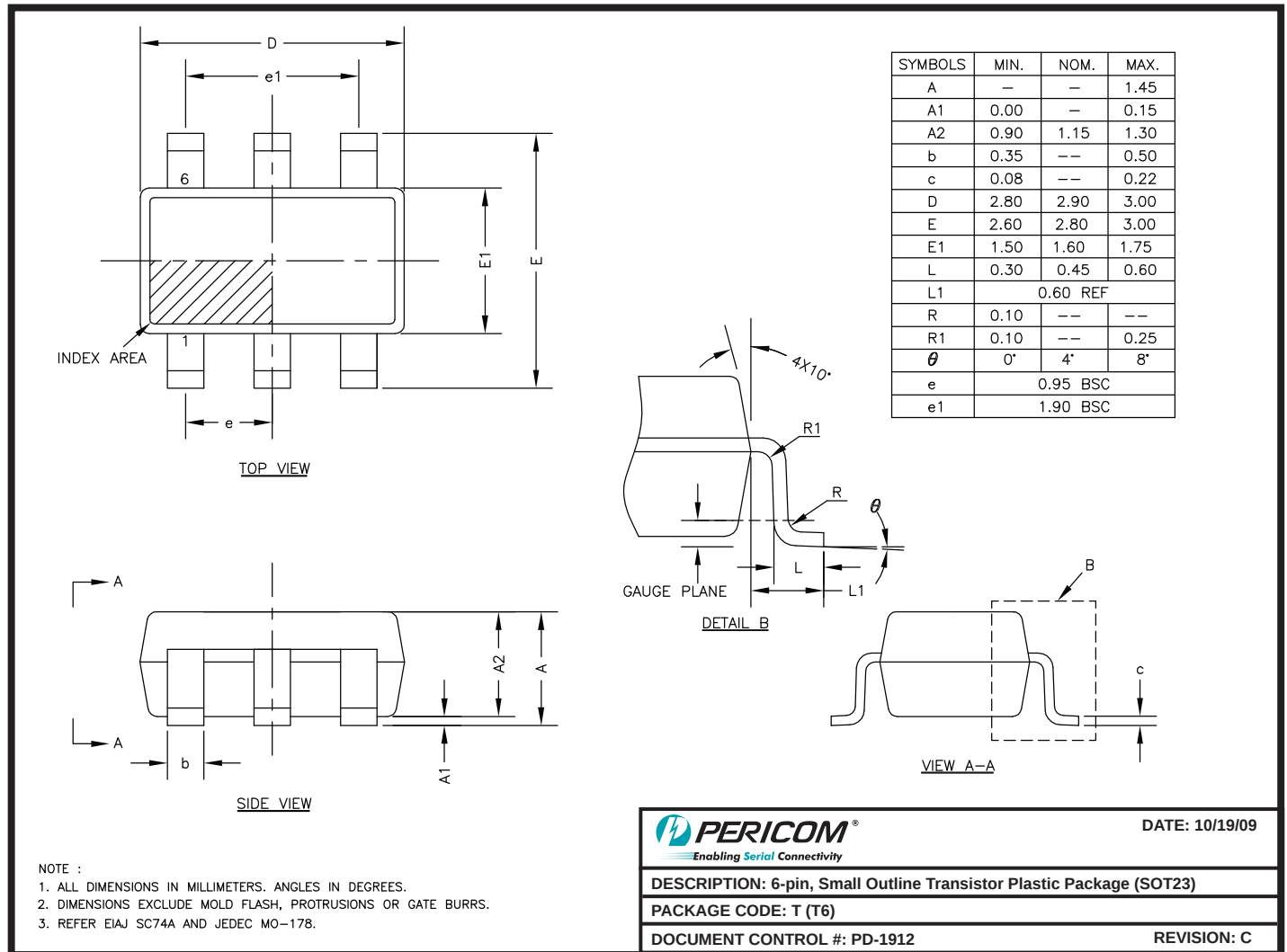
Packaging Mechanical: 6-Pin SC70 (C)



Note:

For latest package info, please check: <http://www.pericom.com/products/packaging/mechanicals.php>

Packaging Mechanical: 6-Pin SOT23 (T)



09-0131

Note:

For latest package info, please check: <http://www.pericom.com/products/packaging/mechanicals.php>

Ordering Information

Ordering Code	Package Code	Package Type	Top Mark
PI5A4599ACEX	C	Pb-free & Green, 6-pin SC70	$\overline{\text{Z}}\text{N}$
PI5A4599ATX	T	6-pin SOT23	ZN
PI5A4599ATEX	T	Pb-free & Green, 6-pin SOT23	$\overline{\text{Z}}\text{N}$

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
2. "E" denotes Pb-free and Green
3. Adding an "X" at the end of the ordering code denotes tape and reel packaging