

RJK1535DPJ, RJK1535DPE, RJK1535DPF

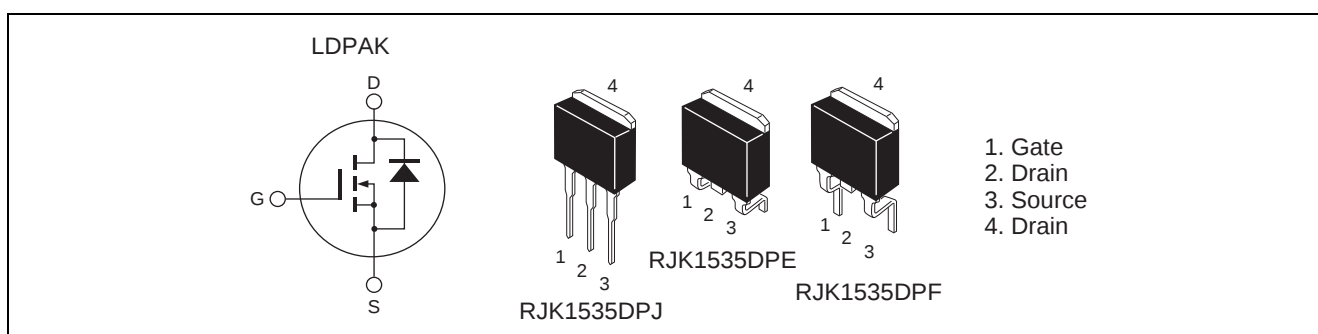
Silicon N Channel MOS FET
High Speed Power Switching

REJ03G0479-0300
Rev.3.00
Jun 30, 2010

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source voltage	V_{DS}	150	V
Gate to Source voltage	V_{GS}	± 30	V
Drain current	I_D	40	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	100	A
Body-Drain diode reverse Drain current	I_{DR}	40	A
Body-Drain diode reverse Drain peak current	$I_{DR(pulse)}$ ^{Note1}	100	A
Avalanche current	I_{AP} ^{Note3}	30	A
Avalanche energy	E_{AR} ^{Note3}	67.5	mJ
Channel dissipation	P_{ch} ^{Note2}	100	W
Channel to case thermal impedance	θ_{ch-c}	1.25	°C/W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
 2. Value at $T_c = 25^\circ C$
 3. $STch = 25^\circ C$, $T_{ch} \leq 150^\circ C$

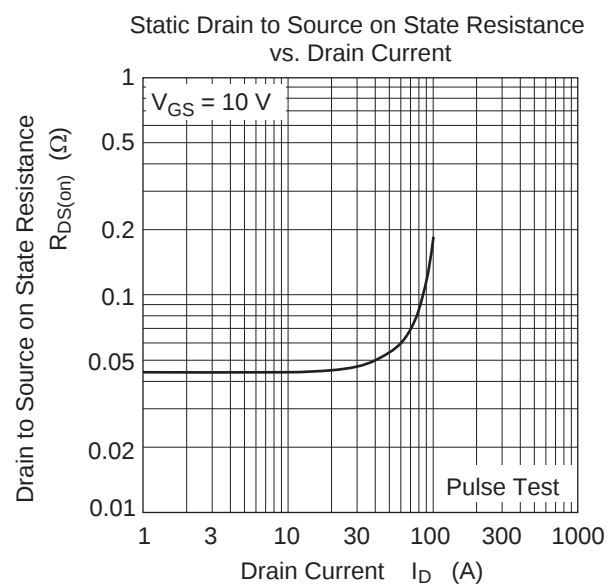
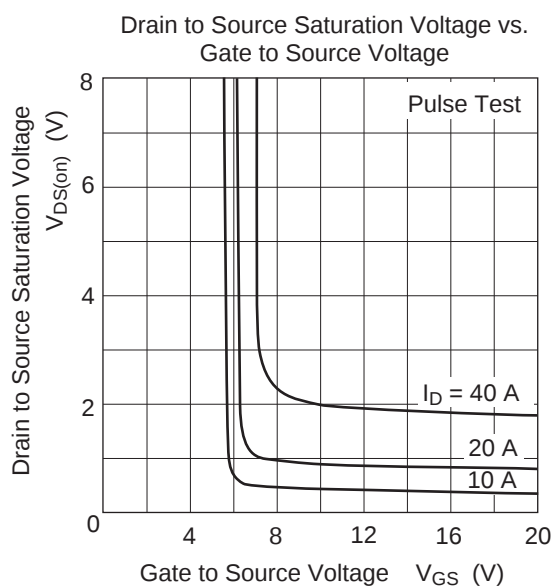
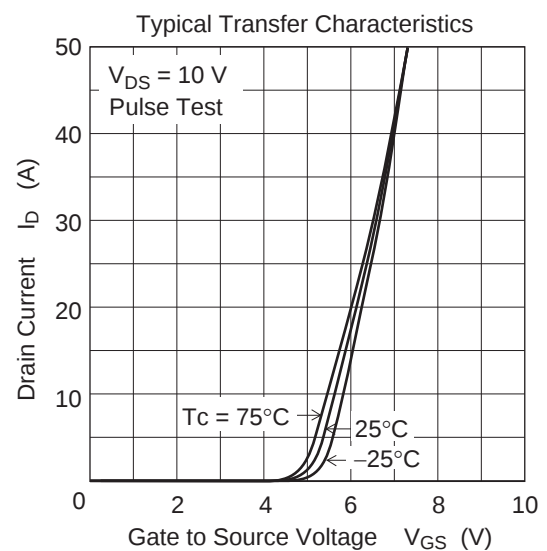
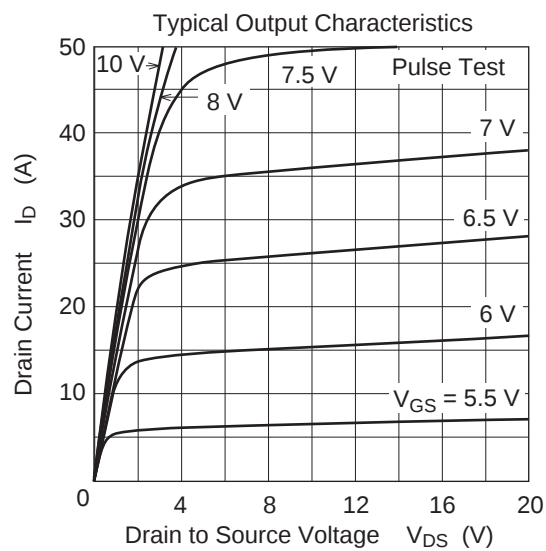
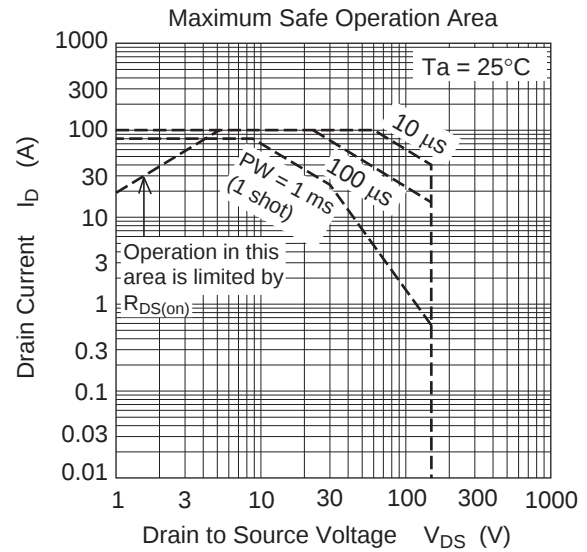
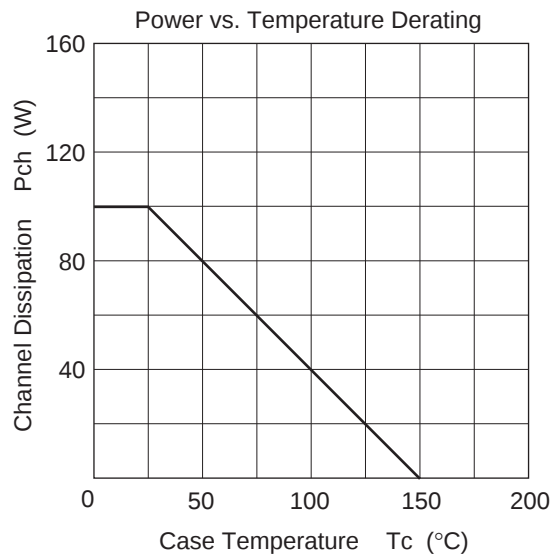
Electrical Characteristics

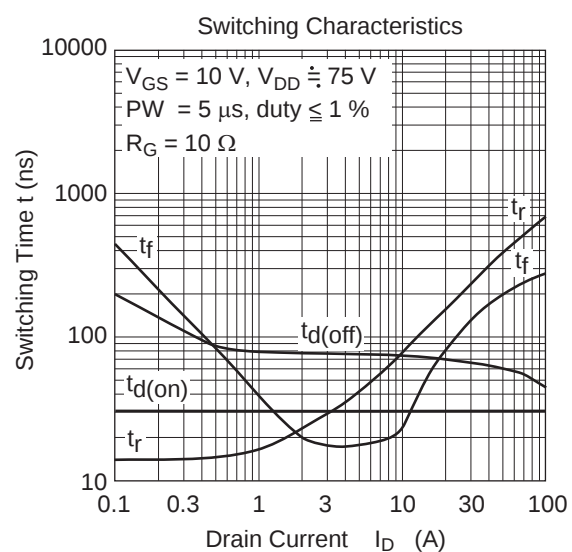
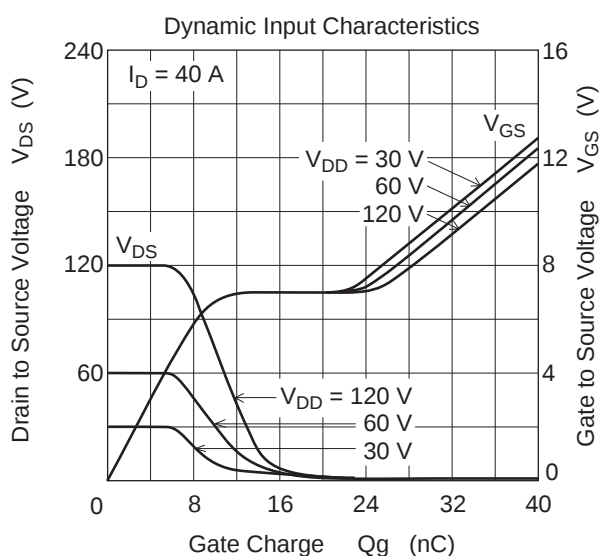
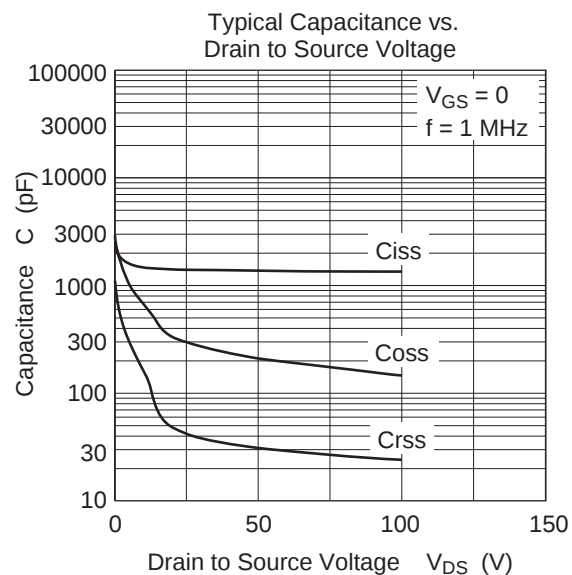
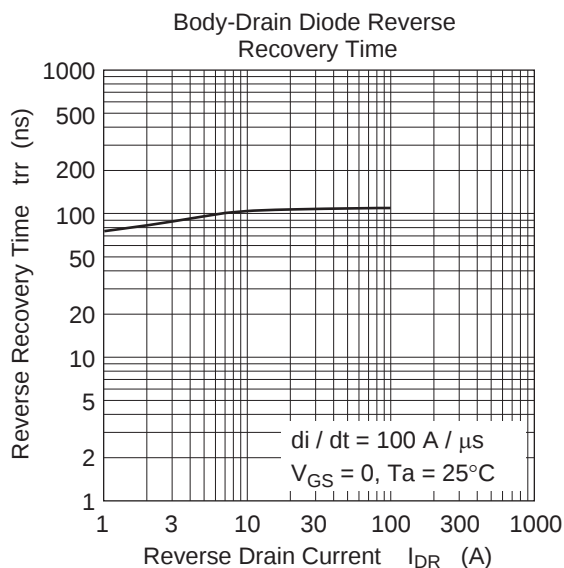
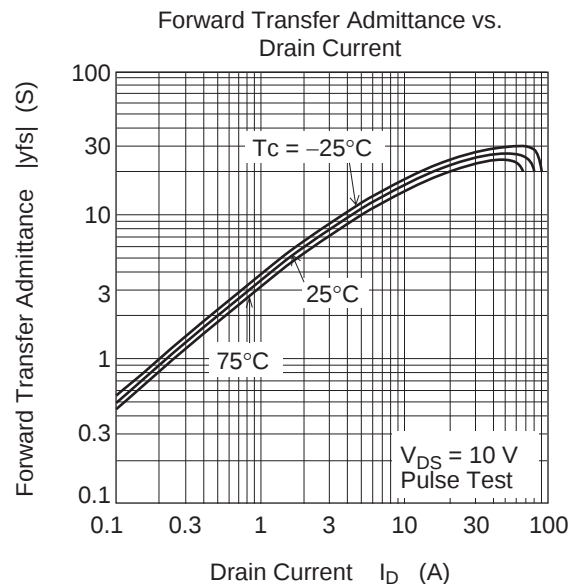
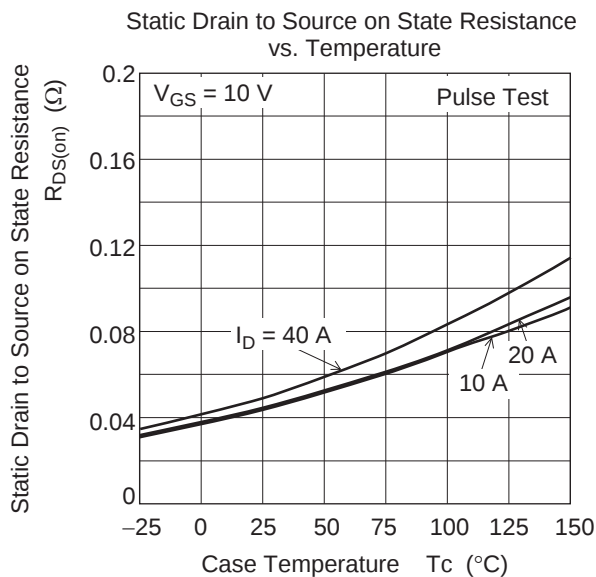
(Ta = 25°C)

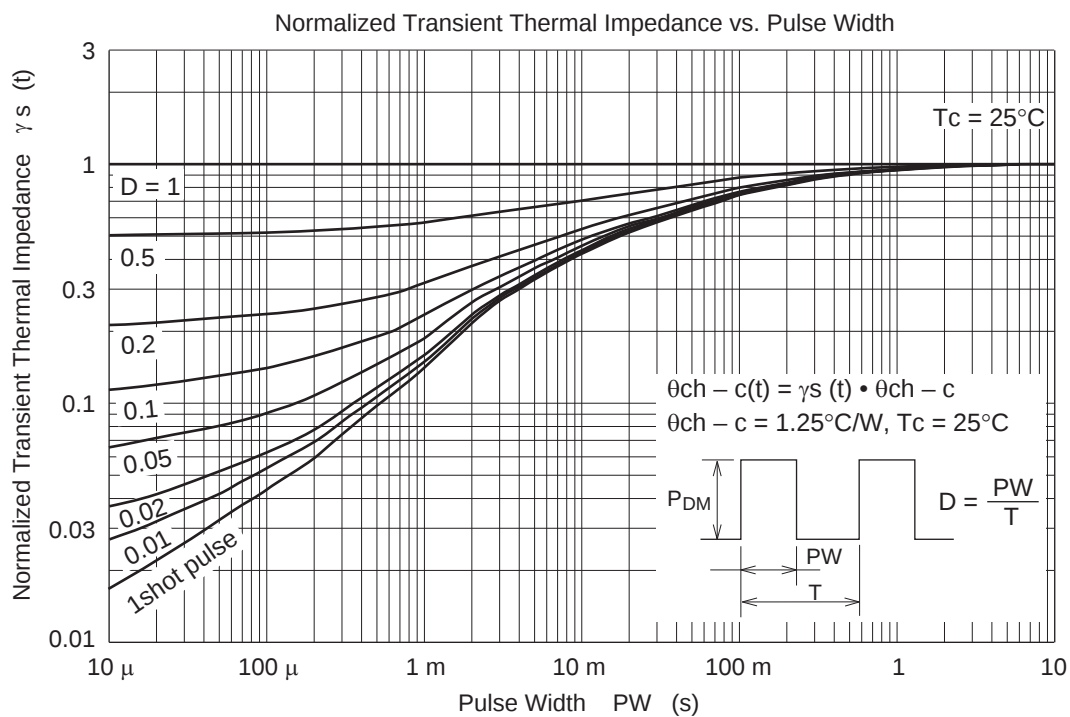
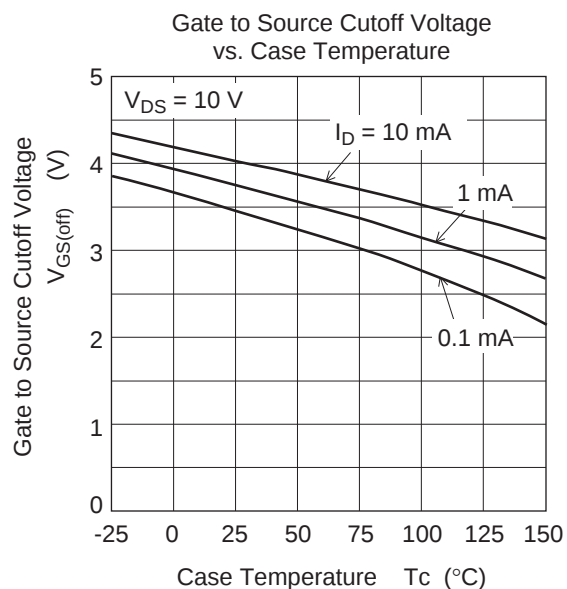
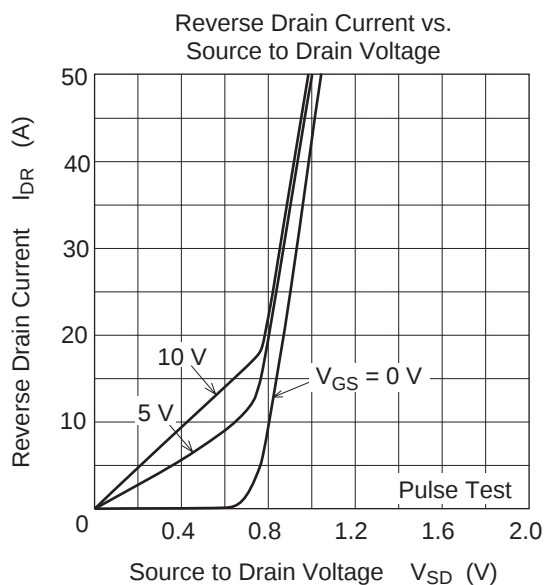
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	150	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero Gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 150 \text{ V}$, $V_{GS} = 0$
Gate to Source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	13	22	—	S	$I_D = 20 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Static Drain to Source on state resistance	$R_{DS(on)}$	—	0.045	0.052	Ω	$I_D = 20 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	1420	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	300	—	pF	
Reverse transfer capacitance	C_{rss}	—	42	—	pF	
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$I_D = 20 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 3.75 \Omega$ $R_g = 10 \Omega$
Rise time	t_r	—	170	—	ns	
Turn-off delay time	$t_{d(off)}$	—	70	—	ns	
Fall time	t_f	—	80	—	ns	
Total Gate charge	Q_g	—	35	—	nC	$V_{DD} = 120 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 40 \text{ A}$
Gate to Source charge	Q_{gs}	—	9	—	nC	
Gate to Drain charge	Q_{gd}	—	16	—	nC	
Body-Drain diode forward voltage	V_{DF}	—	1.0	1.5	V	$I_F = 40 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-Drain diode reverse recovery time	t_{rr}	—	110	—	ns	$I_F = 40 \text{ A}$, $V_{GS} = 0$ $diF/dt = 100 \text{ A}/\mu\text{s}$
Body-Drain diode reverse recovery charge	Q_{rr}	—	0.5	—	μC	

Notes: 4. Pulse test

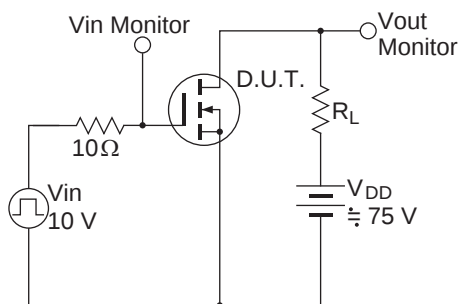
Main Characteristics



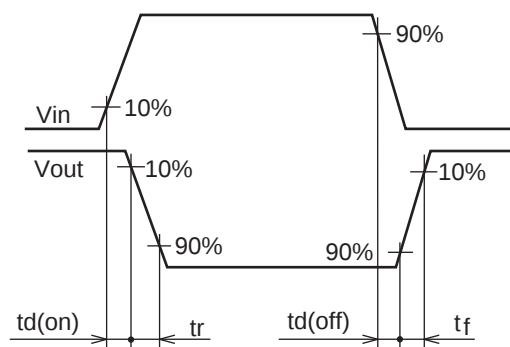




Switching Time Test Circuit

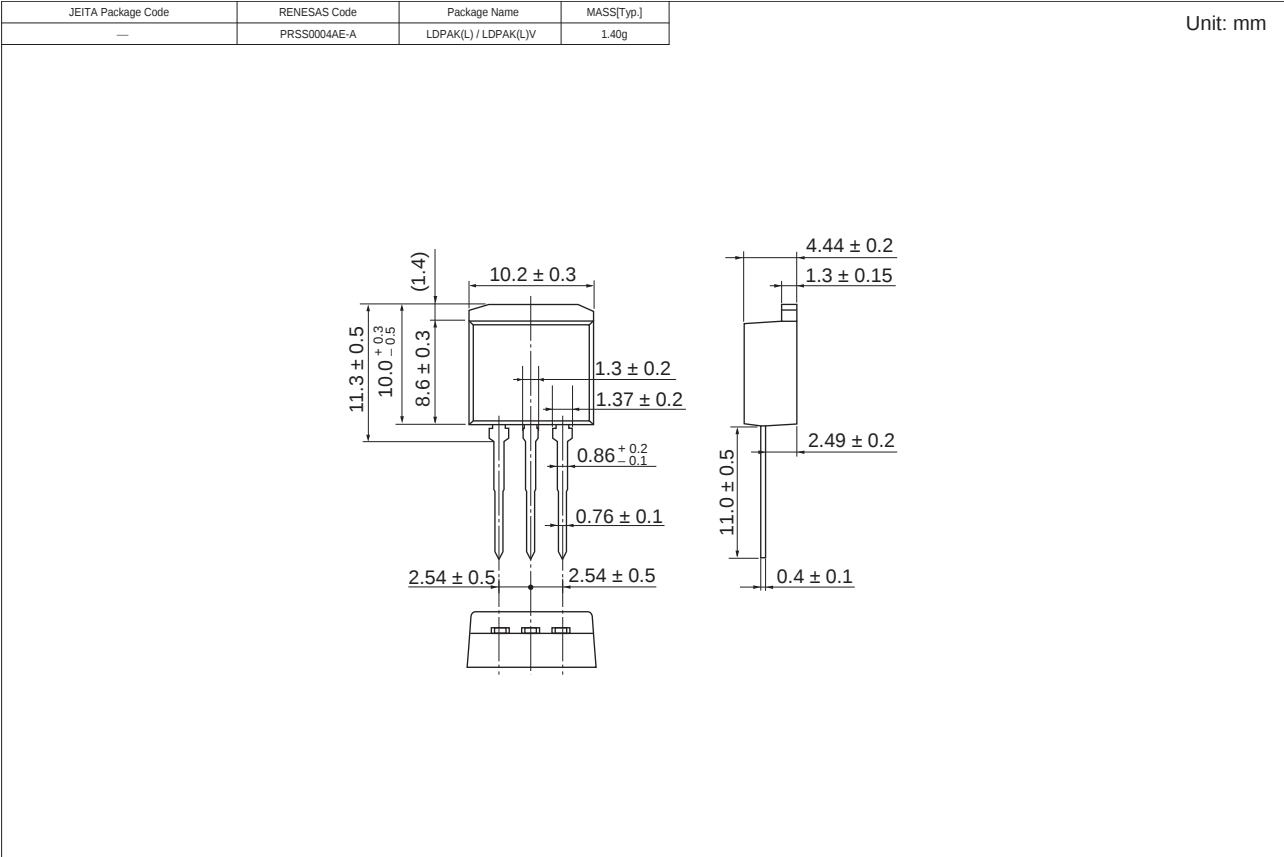


Waveform

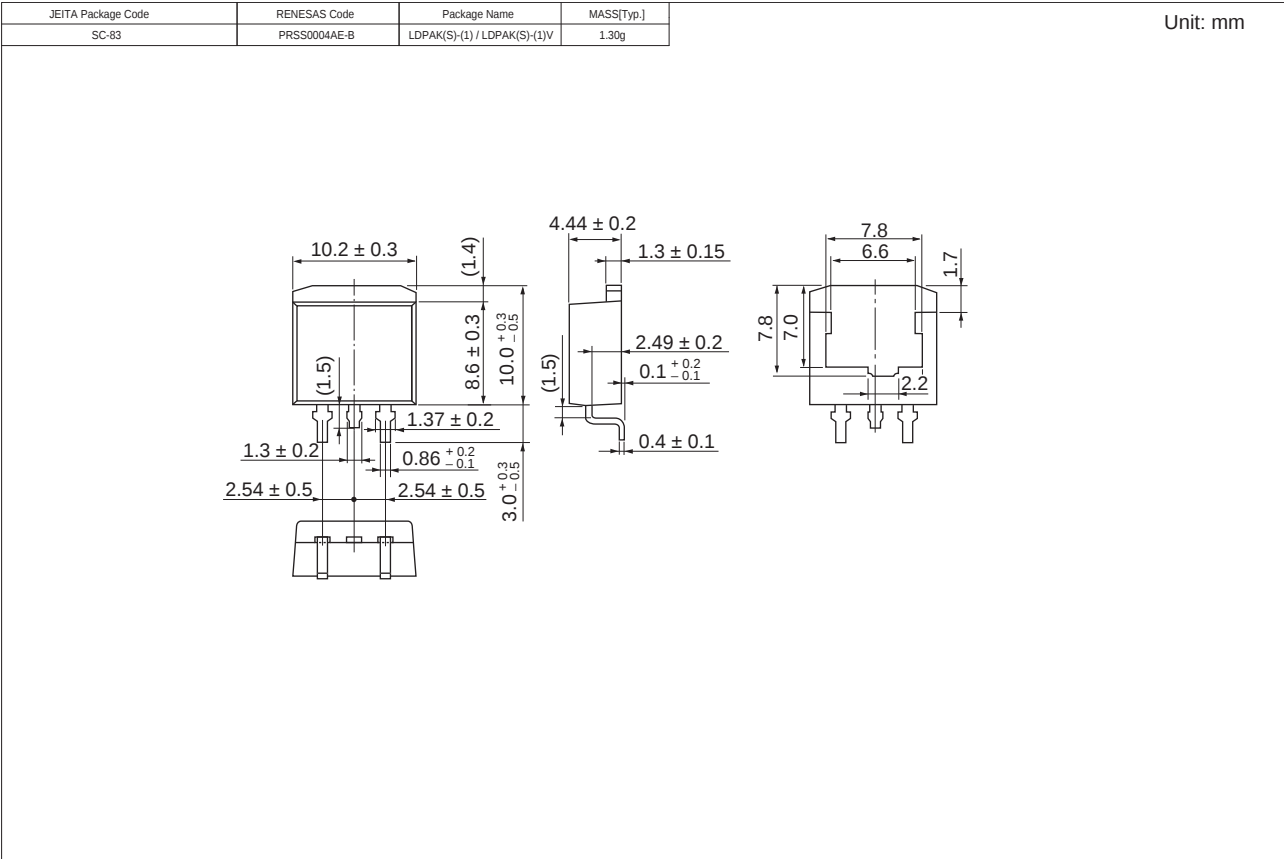


Package Dimensions

• RJK1535DPJ



• RJK1535DPE



• RJK1535DPF

JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]	Unit: mm
—	PRSS0004AE-C	LDBAK(S)-(2) / LDBAK(S)-(2)V	1.35g	

The drawing shows three views of the RJK1535DPF package with the following dimensions (all in mm):

- Top View:** Overall width 10.2 ± 0.3 , overall height 10.0 ± 0.3 . Pin pitch is 1.3 ± 0.2 . Pin width is 0.86 ± 0.1 . Pin length is 2.54 ± 0.5 . A central feature has a width of 1.37 ± 0.2 and a height of 8.6 ± 0.3 . A small feature on the right has a width of 1.4 .
- Side View:** Overall height 4.44 ± 0.2 . Pin height is 1.3 ± 0.15 . Pin thickness is 2.49 ± 0.2 . Pin width is 0.1 ± 0.1 . Pin length is 0.4 ± 0.1 . A central feature has a width of 2.3 .
- Bottom View:** Overall width 7.8 , overall height 7.0 . Pin pitch is 6.6 . Pin width is 1.7 . Pin length is 2.2 .

Ordering Information

Part Name	Quantity	Shipping Container
RJK1535DPE-LE	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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