

High Common Mode, Gain of 20, Bidirectional Precision Voltage Difference Amplifier

General Description

The LMP8276 is a fixed gain differential amplifier with a -2V to 16V input common mode voltage range and a supply voltage range of 4.75V to 5.5V . The LMP8276 is part of the LMP® precision amplifier family which will detect, amplify and filter small differential signals in the presence of high common mode voltages. The gain is fixed at 20 and is adequate to drive an ADC to full scale in most cases. This fixed gain is achieved in two separate stages, a preamplifier with gain of +10 and a second stage amplifier with a gain of +2. The internal signal path is brought out on two pins that provide a connection for a filter network.

The LMP8276 will function over an extended common mode input voltage range making the device suitable for applications with load dump events such as automotive systems.

The mid-rail offset adjustment pin enables the user to utilize this device for bidirectional current sensing. This is achieved by adjusting an externally set voltage reference. Accurate bidirectional load current measurements are achieved when monitoring the output with respect to this reference voltage.

Features

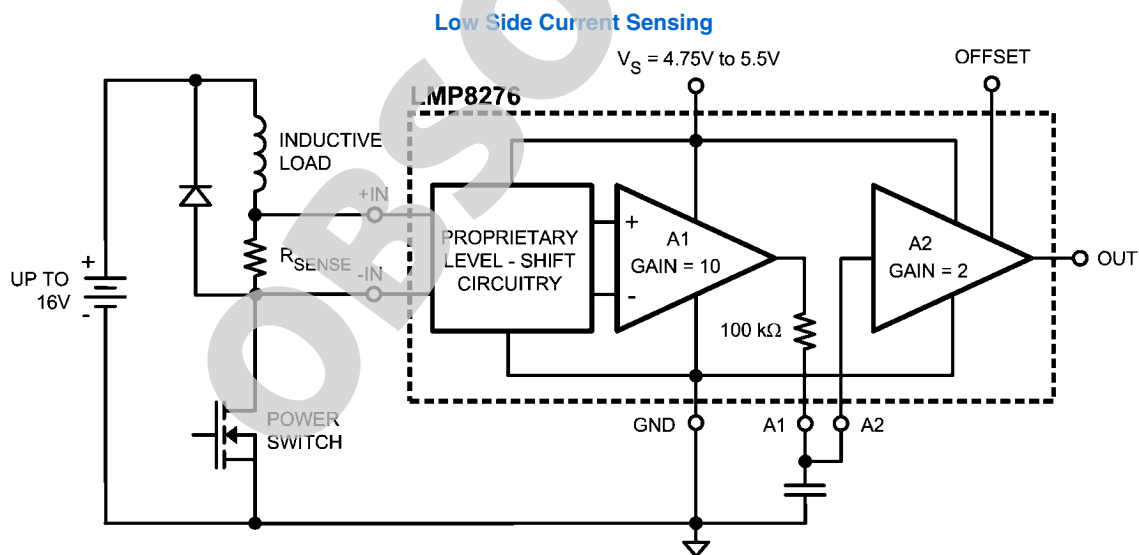
Typical Values, $T_A = 25^\circ\text{C}$

- Bidirectional current sense capability
- Input offset Voltage $\pm 2\text{ mV max}$
- TCVo's $\pm 30\text{ }\mu\text{V}/^\circ\text{C max}$
- CMRR 80 dB min
- Output voltage swing Rail-to-rail
- Bandwidth 80 kHz
- Operating temperature range (ambient) $-40^\circ\text{C to }125^\circ\text{C}$
- Supply voltage $4.75\text{V to }5.5\text{V}$
- Supply current 1 mA

Applications

- Fuel injection control
- High and low side driver configuration current sensing
- Power management systems

Typical Application



20130801

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

ESD Tolerance (Note 2)

Human Body Model	
For input pins only	±4000V
For All other pins	±2000V
Machine Model	200V
Supply Voltage (V_S - GND)	5.75V
Common Mode Voltage on +IN and -IN	
Transient (400 ms)	-7V to 45V

Storage Temperature Range	-65°C to +150°C
Junction Temperature (Note 3)	+150°C max
Soldering Information	
Infrared or Convection (20 sec)	235°C
Wave Soldering Lead Temp. (10 sec)	260°C

Operating Ratings (Note 1)

Temperature Range	
Packaged Devices (Note 3)	-40°C to +125°C
Supply Voltage (V_S - GND)	4.75V to 5.5V
Package Thermal Resistance (θ_{JA} (Note 3))	
8-Pin SOIC	190°C/W

5V Electrical Characteristics (Note 4)

Unless otherwise specified, all limits are guaranteed for $T_A = 25^\circ\text{C}$, $V_S = 5\text{V}$, GND = 0, $-2\text{V} \leq V_{CM} \leq 16\text{V}$, $R_L = \text{Open}$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Typ (Note 5)	Max	Units
V_{OS}	Input Offset Voltage	$V_{CM} = V_S/2$		±0.25	±2.0	mV
TC V_{OS}	Input Offset Voltage Drift	$V_{CM} = V_S/2$		±20	±30	$\mu\text{V}/^\circ\text{C}$
		$-40^\circ\text{C} \leq T_A \leq 25^\circ\text{C}$		±20	±35	
A2 I_B	Input Bias Current of A2	(Note 6)			±20	nA
I_S	Supply Current			1.0	1.2 1.4	mA
R_{CM}	Input Impedance Common Mode		160	200	240	k Ω
R_{DM}	Input Impedance Differential Mode		320	400	480	k Ω
CMVR	Input Common-Mode Voltage Range		-2		+16	V
DC CMRR	DC Common Mode Rejection Ratio	$0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	80	103		dB
		$-40^\circ\text{C} \leq T_A \leq 0^\circ\text{C}$	77			
AC CMRR	AC Common Mode Rejection Ratio (Note 7)	$-2\text{V} \leq V_{CM} \leq 16\text{V}$	80			dB
		$f = 1\text{ kHz}$		78		
PSRR	Power Supply Rejection Ratio	$4.75\text{V} \leq V_S \leq 5.5\text{V}$	70	80		dB
R_{F-INT}	Filter Resistor		97	100	103	k Ω
TCR _{F-INT}	Filter Resistor Drift			20		ppm/ $^\circ\text{C}$
	Midscale Offset Scaling Accuracy (Pin 7)				1	%
A_V	Total Gain		19.8	20	20.2	V/V
	Gain Drift				±25	ppm/ $^\circ\text{C}$
A_{V1}	A1 Gain		9.9	10	10.1	V/V
A_{V2}	A2 Gain		1.98	2	2.02	V/V
A1 V_{OUT}	A1 Output Voltage Swing			0.004	0.001	V
		VOL				
		VOH	4.80	4.95		
A2 V_{OUT}	A2 Output Voltage Swing (Note 8, Note 9)	$R_L = 100\text{ k}\Omega$ on Output		0.007	0.02	V
		VOL				
		VOH	4.80	4.99		
		$R_L = 10\text{ k}\Omega$ on Output		0.03		V
		VOL				
		VOH		4.95		
SR	Slew Rate (Note 10)			0.7		V/ μs
BW	Bandwidth			80		kHz
Noise	0.1 Hz to 10 Hz			5.7		μV_{PP}
	Spectral Density	$f = 1\text{ kHz}$		452		nV/ $\sqrt{\text{Hz}}$

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics Tables.

Note 2: Human Body Model is 1.5 k Ω in series with 100 pF. Machine Model is 0 Ω in series with 200 pF.

Note 3: The maximum power dissipation is a function of $T_{J(MAX)}$, θ_{JA} , and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / \theta_{JA}$. All numbers apply for packages soldered directly onto a PC board.

Note 4: Electrical table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device.

Note 5: Typical values represent the parametric norm at the time of characterization.

Note 6: Positive current corresponds to current flowing into the device.

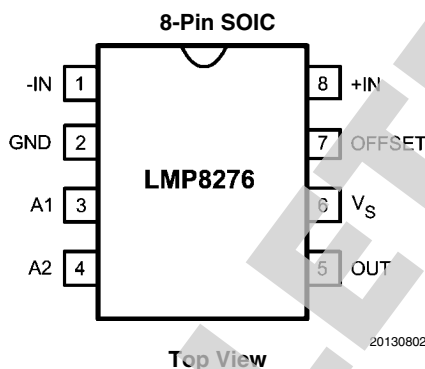
Note 7: AC Common Mode Signal is a 16 V_{PP} sine-wave (0V to 16V) at the given frequency.

Note 8: For VOL, R_L is connected to V_S and for VOH, R_L is connected to GND.

Note 9: For this test input is driven from A1 stage.

Note 10: Slew rate is the average of the rising and falling slew rates.

Connection Diagram

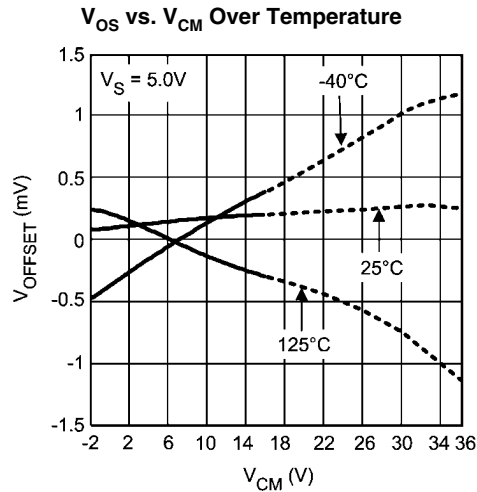


Ordering Information

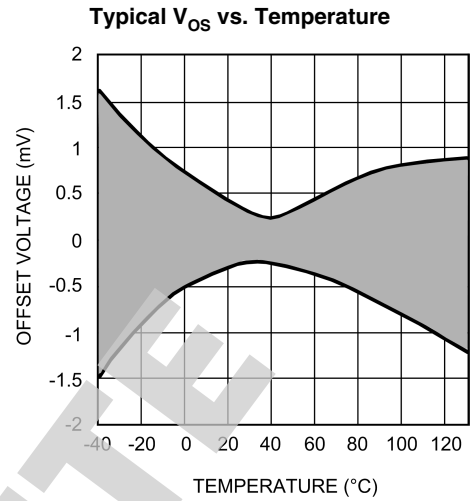
Package	Part Number	Package Marking	Transport Media	NSC Drawing
8-Pin SOIC	LMP8276MA	LMP8276MA	95 Units/Rail	M08A
	LMP8276MAX		2.5k Units Tape and Reel	

Typical Performance Characteristics

Unless otherwise specified: $T_A = 25^\circ\text{C}$, $V_S = 5\text{V}$, $V_{CM} = V_S/2$

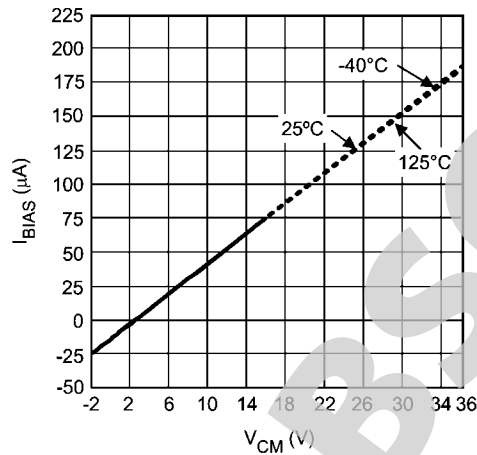


20130815



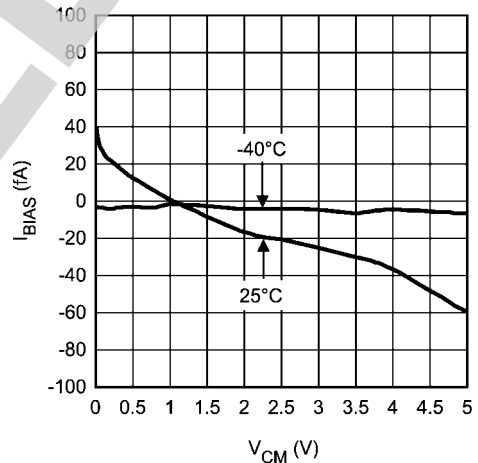
20130821

Input Bias Current Over Temperature (A1 Inputs)



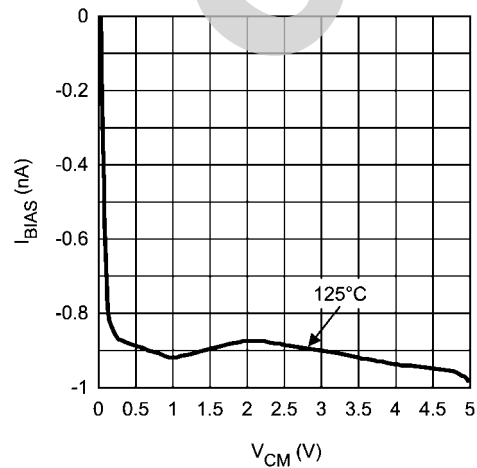
20130819

Input Bias Current Over Temperature (A2 Inputs)



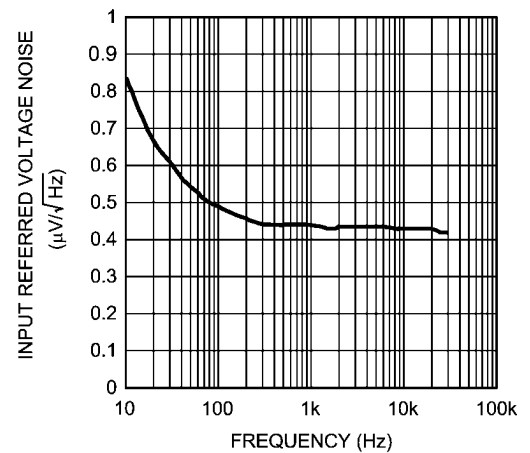
20130818

Input Bias Current Over Temperature (A2 Inputs)

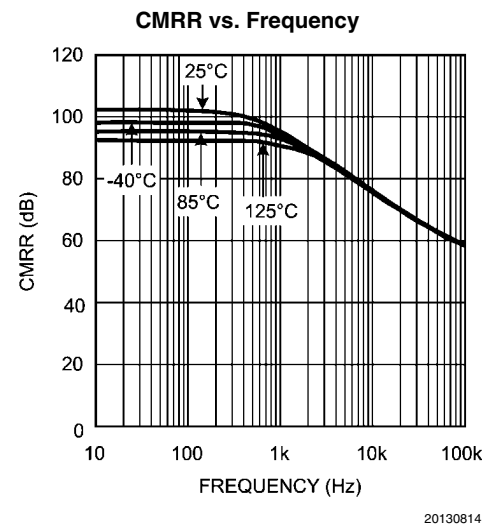
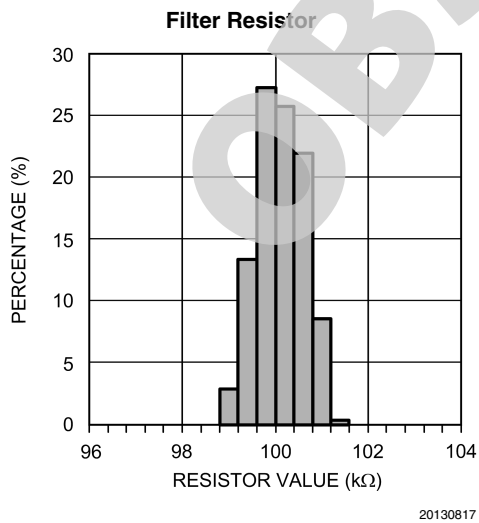
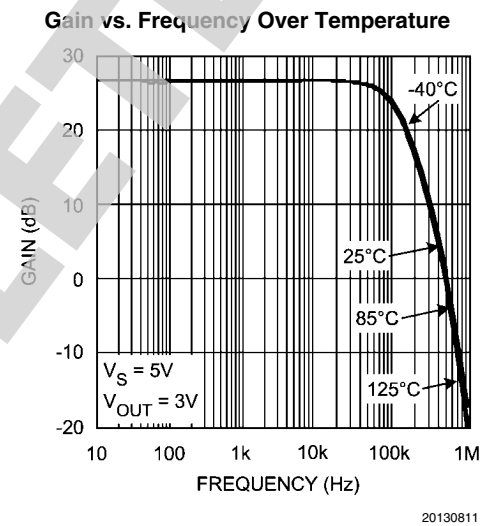
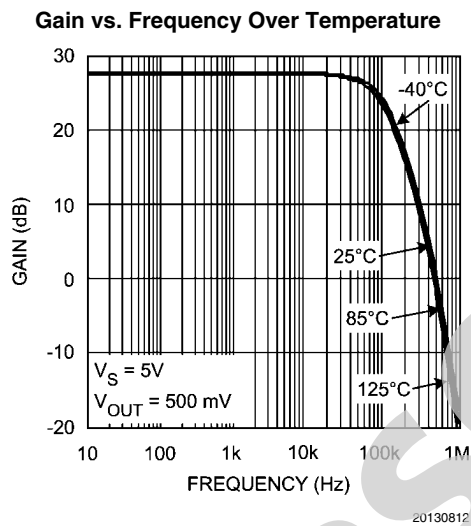
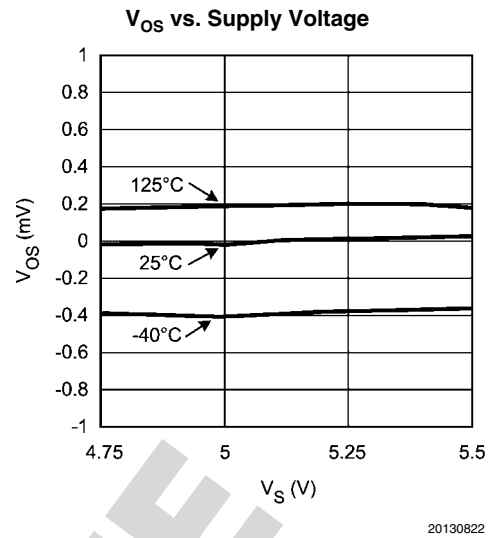
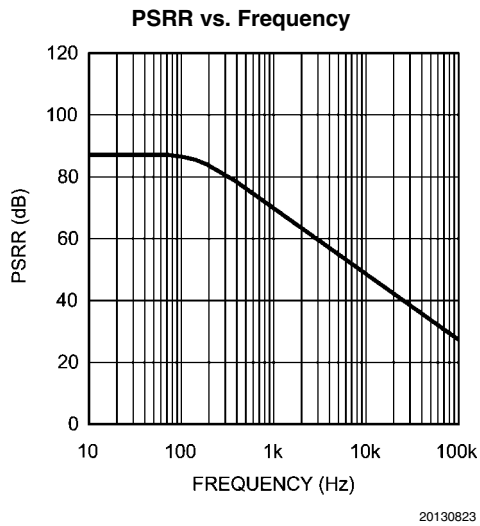


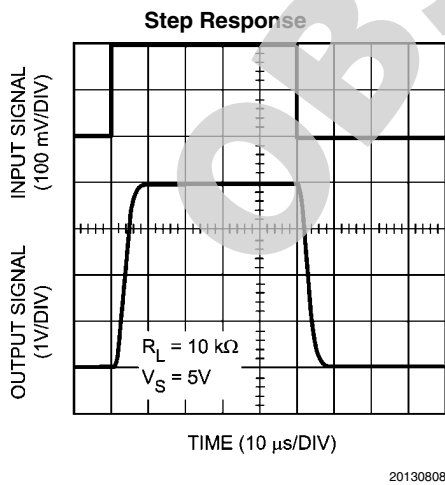
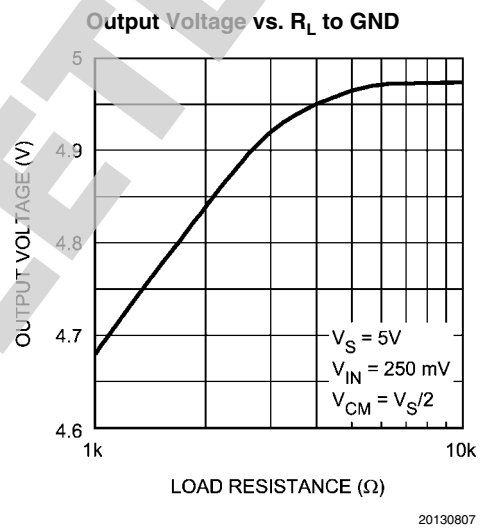
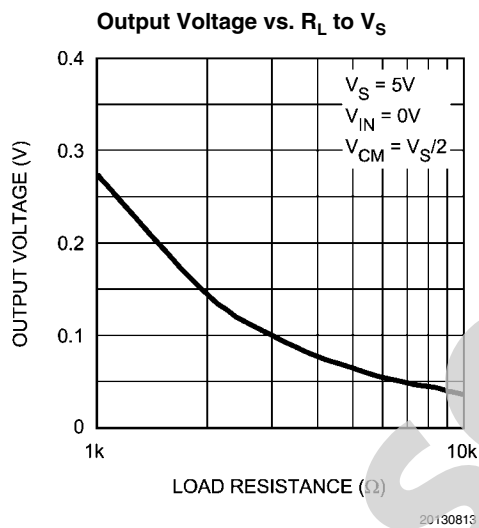
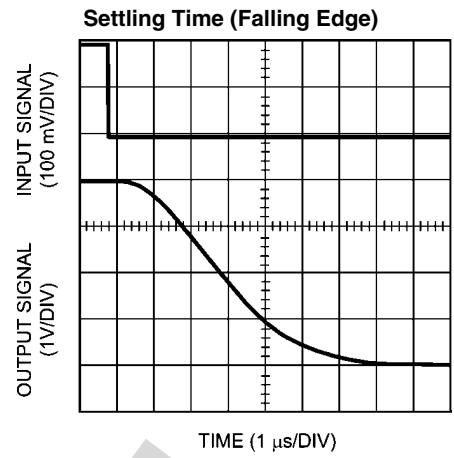
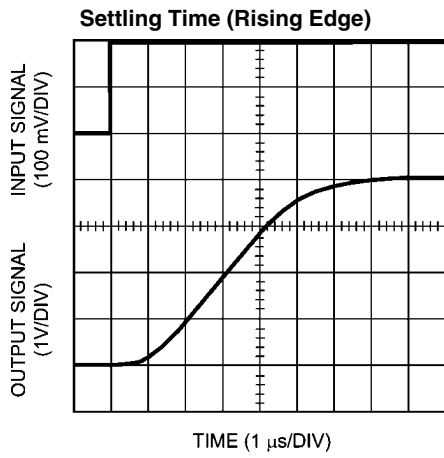
20130820

Input Referred Voltage Noise vs. Frequency



20130806





Application Note

LMP8276

The LMP8276 is a single supply amplifier with a fixed gain of 20 and a common mode voltage range of -2V to 16V. The fixed gain is achieved in two separate stages, a preamplifier with gain of +10 and a second stage amplifier with gain of +2. A block diagram of the LMP8276 is shown in [Figure 1](#).

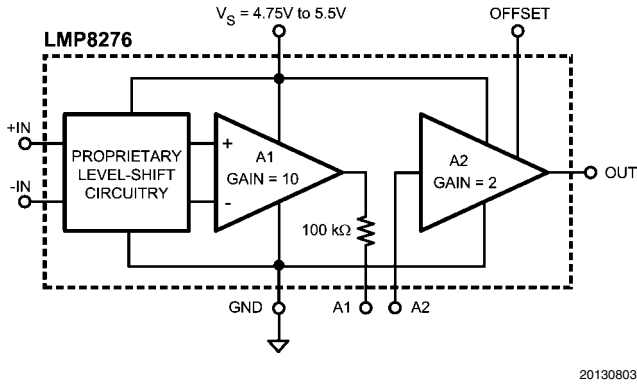


FIGURE 1. LMP8276

The overall offset of the LMP8276 is minimized by trimming amplifier A1. This is done so that the output referred offset of A1 cancels the input referred offset of A2 or $10V_{OS1} = -V_{OS2}$. Because of this offset voltage relationship, the offset of each individual amplifier stage may be more than the limit specified for the overall system in the datasheet tables. Care must be given when pin 3 and 4, A1 and A2, are connected to each other. If the signal going from A1 to A2 is amplified or attenuated (by use of amplifiers and resistors), the overall LMP8276 offset will be affected as a result. Filtering the signal between A1 and A2 or simply connecting the two pins will not change the offset of the LMP8276.

Referencing the input referred offset voltages of each stage, the following relationship holds:

$$\frac{(10V_{OS1}) + (V_{OS2})}{10} = V_{OS} \text{ (LMP8276)}$$

If the signal on pin 3 is scaled, attenuated or amplified, by a factor X then the offset of the overall system will become:

$$\frac{(10V_{OS1}) \times (X) + (V_{OS2})}{10(X)} = V_{OS} \text{ (LMP8276)}$$

MID-RAIL OFFSET ADJUSTMENT PIN

The external mid-rail offset adjustment pin enables the user to utilize the LMP8276 for bidirectional current sensing. If the offset pin, pin 7, is connected to ground then the LMP8276 is capable of sensing positive signals. When the offset pin is connected to V_S then $1/2V_S$ is added to the output and the amplifier can sense both positive and negative signals. If this offset pin is connected to any voltage between ground and V_S , then the output is increased by a voltage equal to half of that offset. Namely, if the offset is connected to V_X , then the output increases by $1/2V_X$. This relationship is shown in [Figure 2](#):

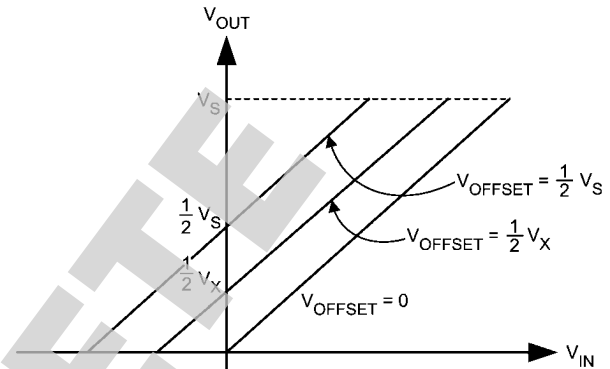


FIGURE 2. Mid-Rail Offset Adjustment Pin Function

Note that the offset pin, pin 7, needs to be connected at all times. If the pin is left floating, the LMP8276 will be operating in an undefined mode. Also pin 7 should be driven from a low impedance source.

In addition to shifting the output of the LMP8276 by $1/2V_X$, the offset voltage applied to the offset adjustment pin shifts the output of the first amplifier by $1/2V_X$. [Figure 3](#) shows a simplified schematic of how this offset adjustment is done. Note that there is a proprietary level shift stage as well as an amplification stage before A1. The combination of these two stages and A1 provide the 10X gain and also shift the output voltage level by half of the offset voltage applied. The output of A1 with respect to the inputs shown (V_{IN+} and V_{IN-}) is:

$$V_{OUT(A1)} = 10(V_{IN+} - V_{IN-}) + 1/2(V_{OFFSET})$$

The output of A2 with respect to its inputs, or the output of A1 is:

$$V_{OUT(A2)} = 1/2(V_{OFFSET}) + 2(V_{OUT(A1)} - 1/2(V_{OFFSET}))$$

Therefore:

$$V_{OUT(A2)} = 20(V_{IN+} - V_{IN-}) + 1/2(V_{OFFSET})$$

A2 has a gain of 2X and applying an offset voltage V_X will shift the output voltage level by $1/2V_X$. This shows that half of the voltage applied at the offset pin will be present on the output of both the amplification stages inside the LMP8276.

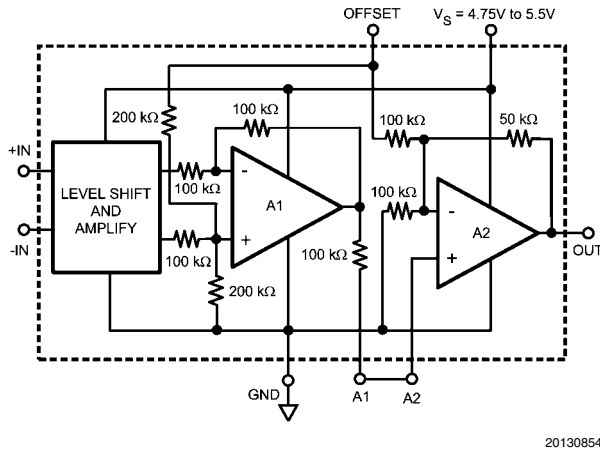


FIGURE 3. Mid-Rail Offset Adjustment Pin Schematic

POWER SUPPLY DECOUPLING

In order to decouple the LMP8276 from AC noise on the power supply, it is recommended to use a 0.1 μF on the supply pin. It is best to use a 0.1 μF capacitor in parallel with a 10 μF capacitor. This will generate an AC path to ground for most frequency ranges and will greatly reduce the noise introduced by the power supply.

SECOND ORDER LOW PASS FILTER

The LMP8276 can be used to build a second order Sallen-Key low pass filter. The general filter is shown in Figure 4.

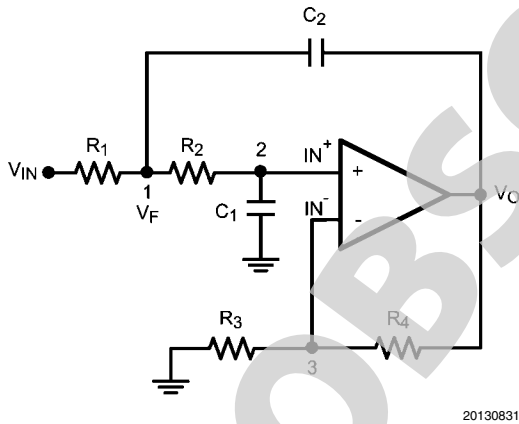


FIGURE 4. Second Order Low-Pass Filter

With the general transfer function

$$\frac{V_O}{V_{IN}} = \frac{K}{M - KN} \quad (1)$$

Where:

$$M = s^2 C_1 C_2 R_1 R_2 + s(R_1 C_1 + R_1 C_2 + C_1 R_2) + 1$$

$$N = s C_2 R_1$$

and

$$\frac{1}{K} = \frac{1}{A_{VOL}} + \frac{R_3}{R_3 + R_4}$$

K represents the sum of DC closed loop gain and the non-ideal behavior of the operational amplifier. Assuming ideal behavior, the equation for K reduces simply to DC gain, which is +2 for the LMP8276.

The LMP8276 can be used to realize this configuration as shown in Figure 5:

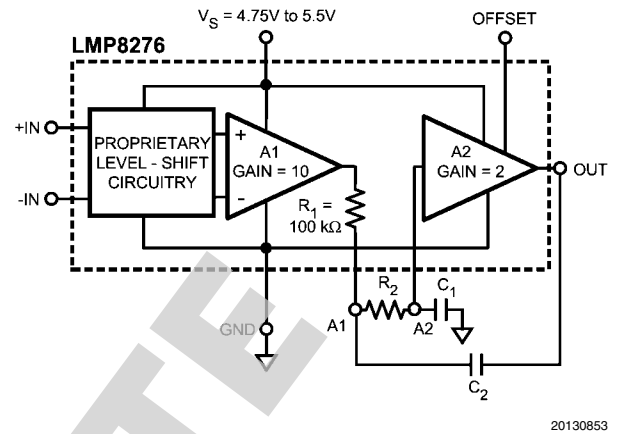


FIGURE 5. Low-Pass Filter With LMP8276

Using Equation 1, the filter parameters can be calculated as follows:

$$\omega_o = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$f_c = \frac{1}{2\pi \sqrt{R_1 R_2 C_1 C_2}}$$

$$Q = \frac{\sqrt{R_1 R_2 C_1 C_2}}{R_1 C_1 + R_2 C_1 + (1-K) R_1 C_2}$$

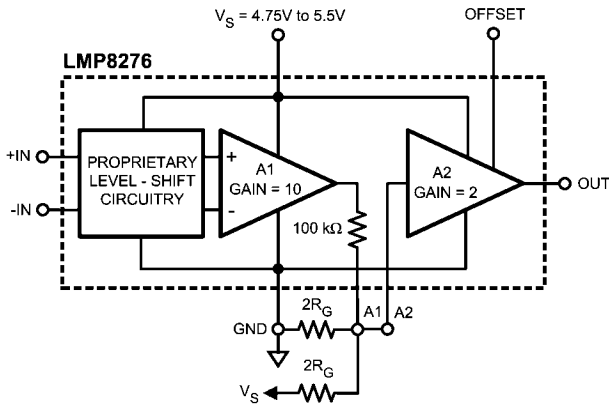
For the LMP8276, $R_1 = 100 \text{ k}\Omega$. Setting $R_1 = R_2$ and $C_1 = C_2$ results in a low pass filter with $Q = 1$. Since the values of resistors are predetermined, the corner frequency of this implementation of the filter depends on the capacitor values.

GAINS OTHER THAN 20

The LMP8276 has an internal gain of +20; however, this gain can be modified. The signal path between the two amplifiers is available as external pins.

GAINS LESS THAN 20

Figure 6 shows the configuration used to reduce the LMP8276 gain.



20130847

FIGURE 6. Gains Less Than 20

Where:

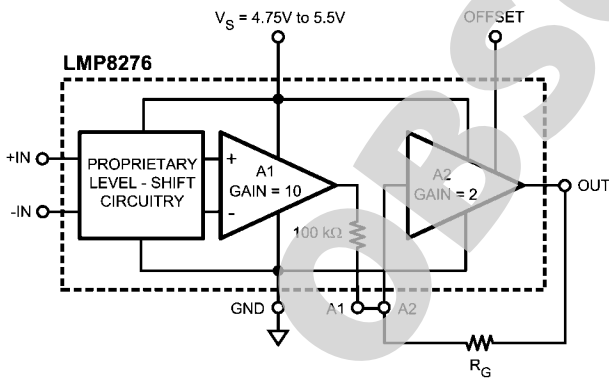
$$\text{GAIN (NEW)} = \frac{20 R_G}{R_G + 100 \text{ k}\Omega}$$

and

$$R_G = (100 \text{ k}\Omega) \frac{\text{GAIN (NEW)}}{20 - \text{GAIN (NEW)}}$$

GAINS GREATER THAN 20

A higher gain can be achieved by using positive feedback on the second stage amplifier, A2, of LMP8276. [Figure 7](#) shows the configuration:



20130850

FIGURE 7. Gains Greater Than 20

The total gain is given by:

$$\text{GAIN (NEW)} = \frac{20 R_G}{R_G - 100 \text{ k}\Omega} \quad (2)$$

which can be rearranged to calculate R_G :

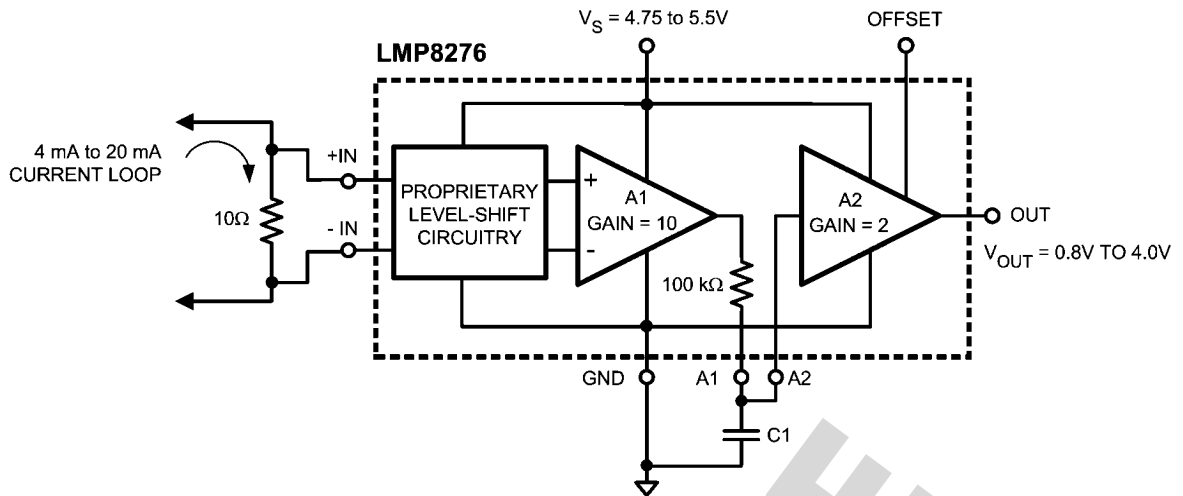
$$R_G = (100 \text{ k}\Omega) \frac{\text{GAIN (NEW)}}{\text{GAIN (NEW)} - 20}$$

The inverting gain of the second amplifier is set at 2, giving the total system gain of 20. The non-inverting gain which is achieved through positive feedback can be less than or equal to this gain without any issues. This implies a total system gain of 40 or less is easily achievable. Once the positive gain surpasses the negative gain, the system might oscillate.

As the value of gain resistor, R_G , approaches that of the internal 100 kΩ resistor, maintaining gain accuracy will become more challenging. This is because Gain (new) is inversely proportional to $(R_G - 100 \text{ k}\Omega)$, see [Equation 2](#). As $R_G \rightarrow 100 \text{ k}\Omega$, the denominator of [Equation 2](#) gets smaller. This smaller value will be comparable to the tolerance of the 100 kΩ resistor and R_G and hence the gain will be dominated by accuracy level of these resistors and the gain tolerance will be determined by the tolerance of the external resistor used for R_G and the 3% tolerance of the internal 100 kΩ resistor.

CURRENT LOOP RECEIVER

Many types of process control instrumentation use 4 to 20 mA transmitters to transmit the sensor's analog value to a central control room. The LMP8276 can be used as a current loop receiver as shown in [Figure 8](#).



20130828

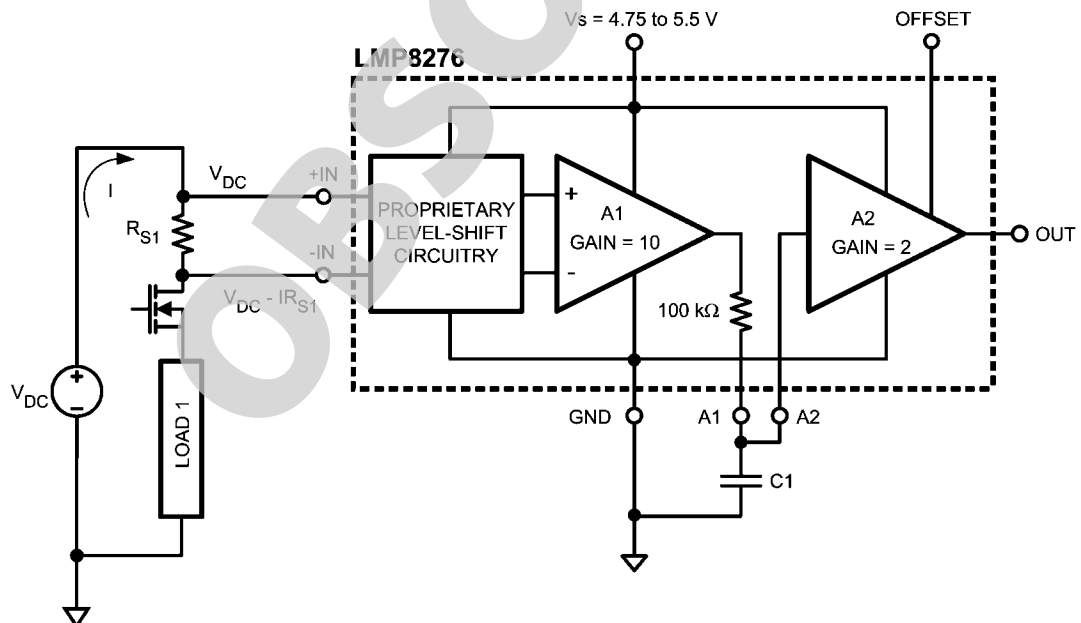
FIGURE 8. Current Loop Receiver

HIGH SIDE CURRENT SENSING

High side current measurement requires a differential amplifier with gain. Here the DC voltage source represents a common mode voltage with the +IN input at the supply voltage and the -IN input very close to the supply voltage. The LMP8276 can be used with a common mode voltage, V_{DC} in this case, of up to 16V.

The LMP8276 can be used for high side current sensing. The large common mode voltage range of this device allows it to

sense signals outside of its supply voltage range. Also, the LMP8276 has very high CMRR, which enables it to sense very small signals in the presence of larger common mode signals. The system in Figure 9 couples these two characteristics of the LMP8276 in an automotive application. The signal through R_{S1} is detected and amplified by the LMP8276 in the presence of a common mode signal of up to 16V with the highest accuracy.



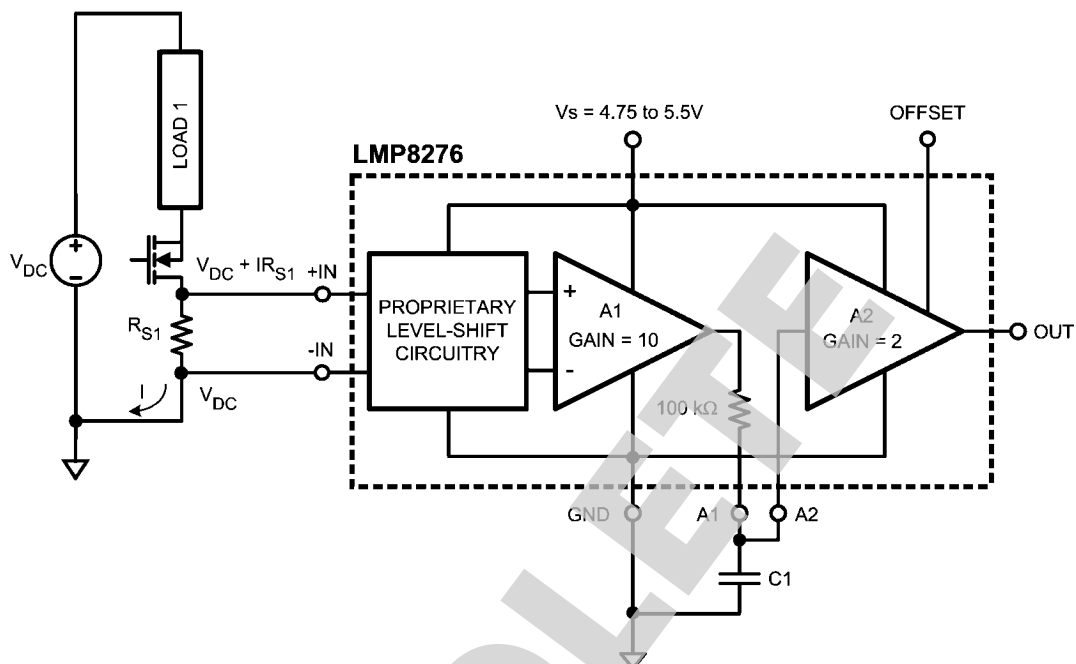
20130829

FIGURE 9. High Side Current Sensing

LOW SIDE CURRENT SENSING

Low side current measurements can cause a problem for operational amplifiers by exceeding the negative common mode voltage limit of the device. In [Figure 10](#), the load current is returning to the power source through a common connection that has a parasitic resistance. The voltage drop across the parasitic resistances can cause the ground connection of the

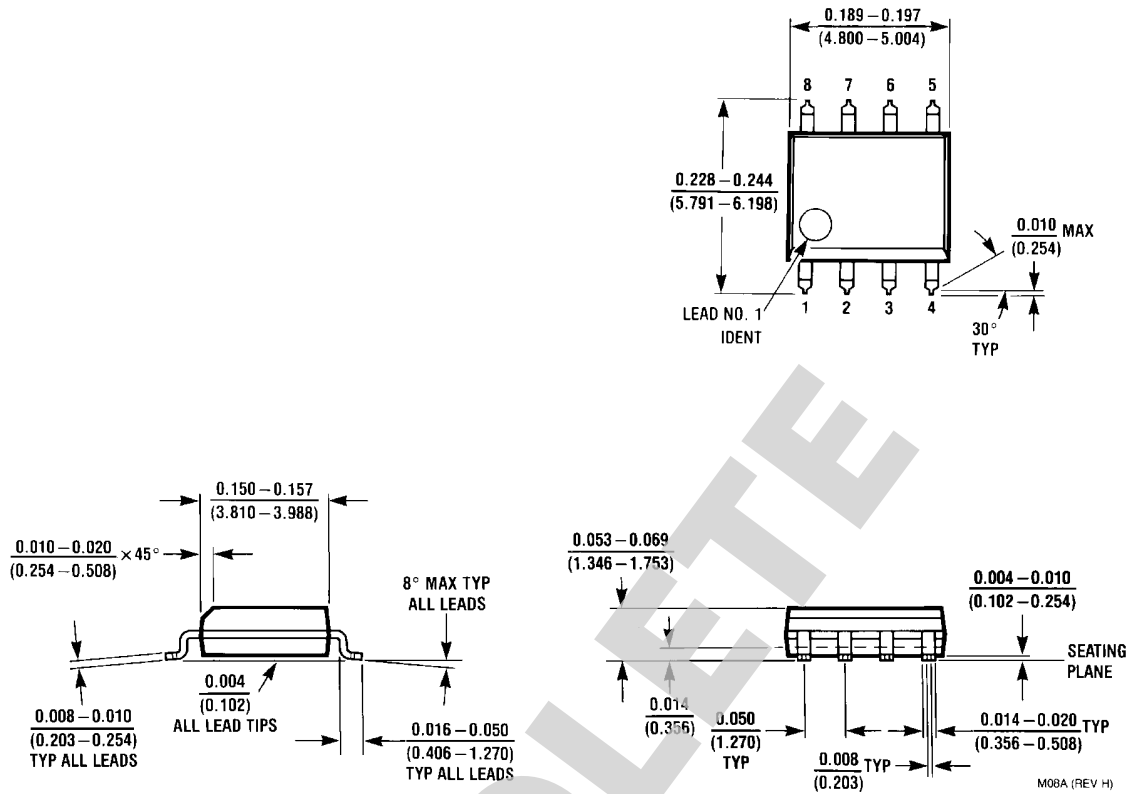
measurement circuits to be at a positive voltage with respect to the common side of the sense resistor. This will result in one or both of the inputs being negative with respect to the circuit's ground. The LMP8276 has a wide input common mode voltage range of -2V to 16V and will function in this condition.



20130830

FIGURE 10. Low Side Current Sensing

Physical Dimensions inches (millimeters) unless otherwise noted



8-Pin SOIC
NS Package Number M08A

Notes

OBSOLETE

Notes

For more National Semiconductor product information and proven design tools, visit the following Web sites at:
www.national.com

Products		Design Support	
Amplifiers	www.national.com/amplifiers	WEBENCH® Tools	www.national.com/webench
Audio	www.national.com/audio	App Notes	www.national.com/appnotes
Clock and Timing	www.national.com/timing	Reference Designs	www.national.com/refdesigns
Data Converters	www.national.com/adc	Samples	www.national.com/samples
Interface	www.national.com/interface	Eval Boards	www.national.com/evalboards
LVDS	www.national.com/lvds	Packaging	www.national.com/packaging
Power Management	www.national.com/power	Green Compliance	www.national.com/quality/green
Switching Regulators	www.national.com/switchers	Distributors	www.national.com/contacts
LDOs	www.national.com/ldo	Quality and Reliability	www.national.com/quality
LED Lighting	www.national.com/led	Feedback/Support	www.national.com/feedback
Voltage References	www.national.com/vref	Design Made Easy	www.national.com/easy
PowerWise® Solutions	www.national.com/powerwise	Applications & Markets	www.national.com/solutions
Serial Digital Interface (SDI)	www.national.com/sdi	Mil/Aero	www.national.com/milaero
Temperature Sensors	www.national.com/tempsensors	SolarMagic™	www.national.com/solarmagic
PLL/VCO	www.national.com/wireless	PowerWise® Design University	www.national.com/training

THE CONTENTS OF THIS DOCUMENT ARE PROVIDED IN CONNECTION WITH NATIONAL SEMICONDUCTOR CORPORATION ("NATIONAL") PRODUCTS. NATIONAL MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE ACCURACY OR COMPLETENESS OF THE CONTENTS OF THIS PUBLICATION AND RESERVES THE RIGHT TO MAKE CHANGES TO SPECIFICATIONS AND PRODUCT DESCRIPTIONS AT ANY TIME WITHOUT NOTICE. NO LICENSE, WHETHER EXPRESS, IMPLIED, ARISING BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT.

TESTING AND OTHER QUALITY CONTROLS ARE USED TO THE EXTENT NATIONAL DEEMS NECESSARY TO SUPPORT NATIONAL'S PRODUCT WARRANTY. EXCEPT WHERE MANDATED BY GOVERNMENT REQUIREMENTS, TESTING OF ALL PARAMETERS OF EACH PRODUCT IS NOT NECESSARILY PERFORMED. NATIONAL ASSUMES NO LIABILITY FOR APPLICATIONS ASSISTANCE OR BUYER PRODUCT DESIGN. BUYERS ARE RESPONSIBLE FOR THEIR PRODUCTS AND APPLICATIONS USING NATIONAL COMPONENTS. PRIOR TO USING OR DISTRIBUTING ANY PRODUCTS THAT INCLUDE NATIONAL COMPONENTS, BUYERS SHOULD PROVIDE ADEQUATE DESIGN, TESTING AND OPERATING SAFEGUARDS.

EXCEPT AS PROVIDED IN NATIONAL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, NATIONAL ASSUMES NO LIABILITY WHATSOEVER, AND NATIONAL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY RELATING TO THE SALE AND/OR USE OF NATIONAL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE CHIEF EXECUTIVE OFFICER AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

Life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness.

National Semiconductor and the National Semiconductor logo are registered trademarks of National Semiconductor Corporation. All other brand or product names may be trademarks or registered trademarks of their respective holders.

Copyright© 2011 National Semiconductor Corporation

For the most current product information visit us at www.national.com



**National Semiconductor
Americas Technical
Support Center**
Email: support@nsc.com
Tel: 1-800-272-9959

**National Semiconductor Europe
Technical Support Center**
Email: europe.support@nsc.com

**National Semiconductor Asia
Pacific Technical Support Center**
Email: ap.support@nsc.com

**National Semiconductor Japan
Technical Support Center**
Email: jpn.feedback@nsc.com