

R5F11FLJANA

Product Info

Product Spec

Basic Information	
Product Status	New
Family	RL78
Series	RL78/G1x
Group	RL78/G1H

Memory	
Program memory	256 KB Flash memory
RAM	24 KB
Data Flash	8 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RL78 (16-bit)
Max. Frequency	32 MHz
Sub-Clock (32.768 kHz)	YES
On-chip Oscillator Freq.	High-Speed: 32, 24, 16, 12, 8, 4, 1MHz Low-speed: 15kHz
Real-Time Clock (RTC)	YES
Power-On Reset	YES
Low Voltage Detection	YES
External Interrupt Pins	7
I/O Ports	41

Timers	
16-bit Timers	9 ch
Watchdog Timers	1 ch
Other Timers	12-bit Interval Timer x 1 ch

Analog	
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A/D Converters	10-bit x 6 ch
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Interfaces	
CSIs	4 ch
UARTs	2 ch
I2Cs	2 ch

Operating Conditions	
Operating Voltage	1.8 to 3.6 V
Operating Temperature	-40 to 85 °C (Ambient Temperature)

Remarks	
Remarks	920MHZ Radio communication

Package	
Pins	64
Terminal pitch (mm)	0.5 mm
Package Type	HVQFN
Dimensions (mm)	9x9 mm ²
Drawing Link	 pvqn0064kc_a
Mounting height (mm)[MAX]	1 mm
Mass (g) [TYP,]	0.21 g
Renesas code	PVQN0064KC-A (old: -)
JEITA code	P-HVQFN64-9x9-0.50
Terminal material - Base	Cu alloy
Terminal material - Surface	Ni/Pd/Au