SDAS075A - D2661, APRIL 1982 - REVISED MAY 1986

- Buffer Version of 'ALS10A
- Package Options include Plastic Small Outline DIPs and Ceramic Chip Carriers in Addition to the Standard 300-mil Plastic and Ceramic DIPs.
- Dependable Texas Instruments Quality and Reliability

description

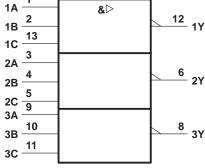
These devices contain three independent 3-input NAND buffers. They perform the Boolean functions $Y = \overline{A \cdot B \cdot C}$ or $Y = \overline{A + B + C}$ in positive logic.

The SN54ALS1010A is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS1010A is characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each gate)

I	NPUT	3	OUTPUT
Α	В	С	Υ
Н	Н	Н	L
L	Χ	Χ	Н
Х	L	Χ	Н
Χ	Χ	L	Н

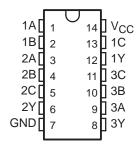
logic symbol †



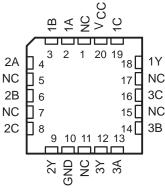
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54ALS1010A . . . J PACKAGE SN74ALS1010A . . . D OR N PACKAGE (TOP VIEW)

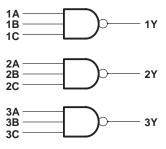


SN54ALS1010A . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram (positive logic)



SN54ALS1010A, SN74ALS1010A TRIPLE 3-INPUT POSITIVE-NAND BUFFERS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted) SN74ALS1010A 0°C to 70°C Storage temperature range –65°C to 150°C

recommended operating conditions

			SN54ALS1010A			SN74ALS1010A		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
٧ _{IH}	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			-1			-2.6	mA
l _{OL}	Low-level output current			12			24	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS		SN54	SN54ALS1010A			SN74ALS1010A		
PARAMETER			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2			V _{CC} -2			
Voн	V _{CC} = 4.5 V,	$I_{OH} = -1 \text{ mA}$	2.4	3.3					V
	V _{CC} = 4.5 V,	$I_{OH} = -2.6 \text{ mA}$				2.4	3.3		
.,	$V_{CC} = 4.5 \text{ V},$	I_{OL} = 12 mA		0.25	0.4		0.25	0.4	V
V_{OL}	V _{CC} = 4.5 V,	I _{OL} = 24 mA					0.35	0.5	V
ΙΙ	V _{CC} = 5.5 V,	V _I = 7 V			0.1			0.1	mA
lН	V _{CC} = 5.5 V,	V _I = 2.7 V			20			20	μΑ
ΙΙL	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
IO [‡]	V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	-30		-112	mA
ICCH	V _{CC} = 5.5 V,	V _I = 0		0.65	1.2		0.65	1.2	mA
^I CCL	V _{CC} = 5.5 V,	V _I = 4.5 V		3.6	5.8		3.6	5.8	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = 25^{\circ}C$ 'ALS1010A TYP	SN54ALS	$C_L = 50$ $R_L = 50$ $T_A = MI$			UNIT
t _{PLH}	A or B	~	5	2	12	2	8	no
^t PHL	AUID	A OI D		2	12	2	8	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

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